

DATA SHEET

GTL2005

Quad GTL/GTL+ to LVTTTL/TTL
bidirectional non-latched translator

Product specification
Supersedes data of 1999 Jun 23

1999 Sep 17

Quad GTL/GTL+ to LVTTTL/TTL
bidirectional non-latched translator

GTL2005

FEATURES

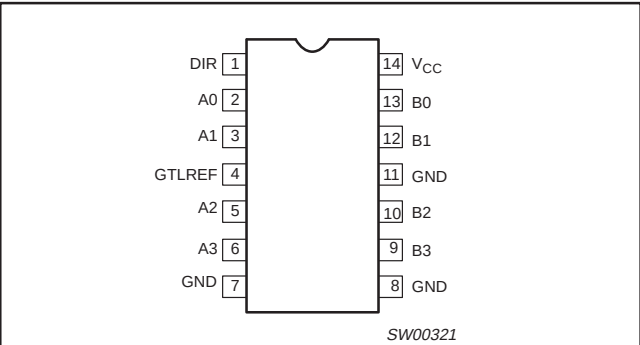
- Operates as a quad GTL/GTL+ sampling receiver or as a LVTTTL/TTL to GTL/GTL+ driver
- Quad bidirectional bus interface
- Live insertion/extraction permitted
- Latch-up protection exceeds 500 mA per JESD78
- ESD protection exceeds 2000 V HBM per JESD22-A114, and 1000 V CDM per JESD22-CC101

DESCRIPTION

The GTL2005 is a quad translating transceiver designed for 3.3 V system interface with a GTL/GTL+ bus.

The direction pin allows the part to function as either a GTL to TTL sampling receiver or as a TTL to GTL interface.

PIN CONFIGURATION



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS $T_{amb} = 25^{\circ}C$	TYPICAL		UNIT
			B to A	A to B	
t_{PLH} t_{PHL}	Propagation delay An to Bn or Bn to An	$C_L = 50\text{ pF}$; $V_{CC} = 3.3\text{ V}$	2.1 1.9	4.1 4.3	ns
C_{IN}	Input capacitance DIR	$V_I = 0\text{ V}$ or V_{CC}	3.0	3.0	pF
$C_{I/O}$	I/O pin capacitance	Outputs disabled; $V_{I/O} = 0\text{ V}$ or 3.0 V	7.8	4.5	pF

ORDERING INFORMATION

PACKAGES	TEMPERATURE RANGE	ORDER CODE	DWG NUMBER
14-Pin Plastic TSSOP Type II	$-40^{\circ}C$ to $+85^{\circ}C$	GTL2005 PW DH	SOT402-1

PIN DESCRIPTION

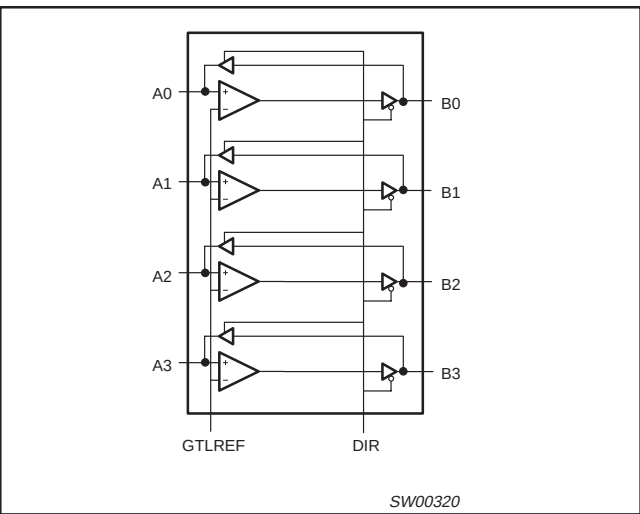
PIN NUMBER	SYMBOL	NAME AND FUNCTION
1	DIR	Direction control input
2, 3, 5, 6	A0 – A3	Data inputs/outputs (A side, GTL)
13, 12, 10, 9	B0 – B3	Data inputs/outputs (B side, TTL)
4	GTLREF	GTL reference voltage
7, 8, 11	GND	Ground (0 V)
14	V _{CC}	Positive supply voltage

FUNCTION TABLE

INPUT	INPUT/OUTPUT	
	B	A
H	Inputs	Bn = An
L	An = Bn	Inputs

H = HIGH voltage level
L = LOW voltage level

LOGIC SYMBOL



Quad GTL/GTL+ to LVTTTL/TTL bidirectional non-latched translator

GTL2005

ABSOLUTE MAXIMUM RATINGS¹

In accordance with the Absolute Maximum System (IEC 134); voltages are referenced to GND (ground = 0 V).

SYMBOL	PARAMETER	TEST CONDITIONS	RATING	UNIT
V_{CC}	DC supply voltage		−0.5 to +4.6	V
I_{IK}	DC input diode current	$V_I < 0$	−50	mA
V_I	DC input voltage ³	A port	−0.5 to +7.0	V
		B port	−0.5 to +4.6	V
I_{OK}	DC output diode current	$V_O < 0$	−50	mA
V_O	DC output voltage ³	Output in Off or High state; A port	−0.5 to +7.0	V
		Output in Off or High state; B port	−0.5 to +4.6	V
I_{OL}	Current into any output in the LOW state	A port	128	mA
		B port	80	mA
I_{OH}	Current into any output in the HIGH state	A port	−64	mA
T_{stg}	Storage temperature range		−60 to +150	°C

NOTES:

- Stresses beyond those listed may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability. The maximum junction temperature of this integrated circuit should not exceed 150°C.
- The input and output negative voltage ratings may be exceeded if the input and output clamp current ratings are observed.

RECOMMENDED OPERATING CONDITIONS¹

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V_{CC}	Supply voltage		0		3.6	V
V_{TT}	Termination voltage	GTL	1.14	1.2	1.26	V
		GTL+	1.35	1.5	1.65	
V_{REF}	Supply voltage	GTL	0.74	0.8	0.87	V
		GTL+	0.87	1.0	1.10	
V_I	Input voltage	A port	0	0	V_{TT}	V
		Except A port	0		5.5	
V_{IH}	HIGH-level input voltage	A port	$V_{REF} + 50 \text{ mV}$			V
		Except A port	2			
V_{IL}	LOW-level input voltage	A port			$V_{REF} - 50 \text{ mV}$	V
		Except A port			0.8	
I_{OH}	HIGH-level output current	B port			−12	mA
I_{OL}	LOW-level output current	A port			40	mA
		B port			12	mA
T_{amb}	Operating free-air temperature range		−40		85	°C

NOTE:

- Unused control inputs must be held HIGH or LOW to prevent them from floating.

Quad GTL/GTL+ to LVTTTL/TTL bidirectional non-latched translator

GTL2005

DC ELECTRICAL CHARACTERISTICS

Over recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			-40°C to +85°C			
			MIN	TYP ¹	MAX	
V _{OH}	B port	V _{CC} = 3.0 to 3.6 V; I _{OH} = -100 μA	V _{CC} -0.2			V
		V _{CC} = 3.0 V; I _{OH} = -12 mA	2.0			
V _{OL}	A port	V _{CC} = 3.0 V; I _{OL} = 40 mA			0.4	V
	B port	V _{CC} = 3.0 V; I _{OL} = 12 mA			0.8	V
I _I	Control inputs	V _{CC} = 3.6 V; V _I = V _{CC} or GND			± 1	μA
	A port	V _{CC} = 3.6 V; V _I = V _{TT} or GND			± 1	
	B port	V _{CC} = 0 or 3.6 V; V _I = 5.5			10	
		V _{CC} = 3.6 V; V _I = V _{CC}			± 1	
		V _{CC} = 3.6 V; V _I = 0 V			-5	
I _{OFF}	A port	V _{CC} = 0 V; V _I or V _O = 0 to 4.5 V			± 100	μA
I _{EX}	B port	V _O = 5.5 V; V _{CC} = 3.0 V		50	125	μA
I _{CC}	A or B port	V _{CC} = 3.6 V; V _I = V _{CC} or GND; I _O = 0			3	mA
ΔI _{CC} ³	B port or control inputs	V _{CC} = 3.6 V; V _I = V _{CC} -0.6 V			500	μA
C _I	Control inputs	V _I = 3.0 V or 0		3		pF
C _{IO}	B port	V _O = 3.0 V or 0		7.8		pF
	A port	V _O = V _{TT} or 0		4.5		

NOTES:

1. All typical values are measured at V_{CC} = 3.3 V and T_{amb} = 25°C.
2. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
3. This is the increase in supply current for each input that is at the specified TTL voltage level rather than V_{CC} or GND.

AC CHARACTERISTICS (3.3 V ± 0.3 V RANGE)

SYMBOL	PARAMETER	WAVEFORM	LIMITS (GTL)			LIMITS (GTL+)			UNIT
			$V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$ $V_{REF} = 0.8\text{ V}$			$V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$ $V_{REF} = 1.0\text{ V}$			
			MIN	TYP ¹	MAX	MIN	TYP ¹	MAX	
t _{PLH} t _{PHL}	Bn to An	1		2.1 1.9	2.3 2.6		2.1 1.9	2.3 2.6	ns
t _{PLH} t _{PHL}	An to Bn	2		4.1 4.4	5.4 5.4		4.2 3.8	5.3 4.8	ns

NOTES:

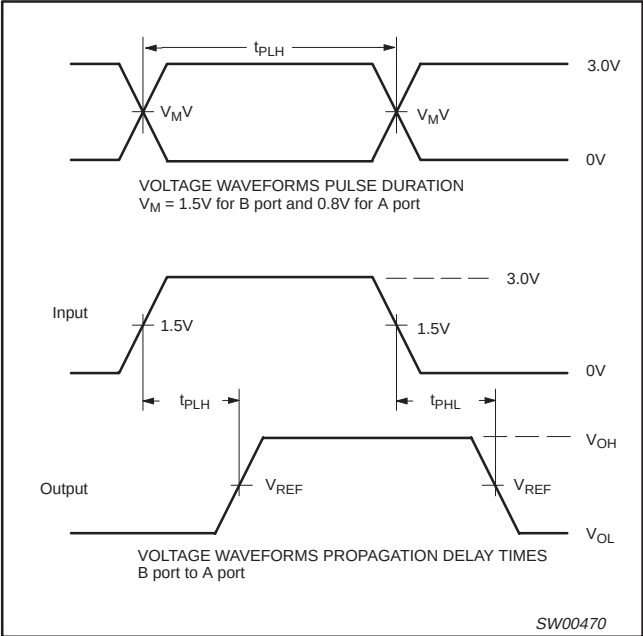
1. All typical values are at V_{CC} = 3.3 V and T_{amb} = 25°C.

Quad GTL/GTL+ to LVTTTL/TTL
bidirectional non-latched translator

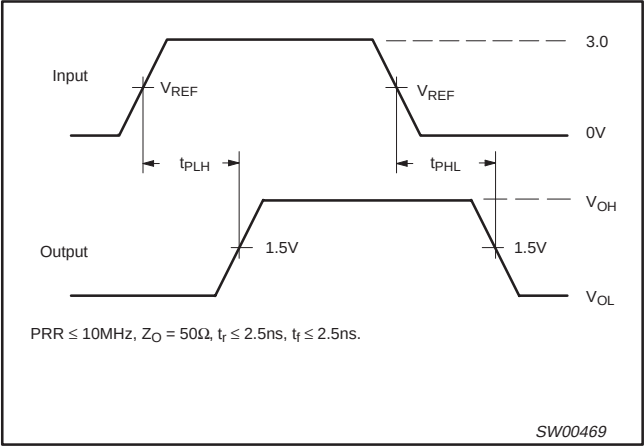
GTL2005

AC WAVEFORMS

$V_M = 1.5\text{ V}$ at $V_{CC} \geq 3.0\text{ V}$, $V_M = V_{CC}/2$ at $V_{CC} \leq 2.7\text{ V}$ for B ports and control pins
 $V_M = V_{REF}$ for A ports



Waveform 1.



Waveform 2.

TEST CIRCUIT

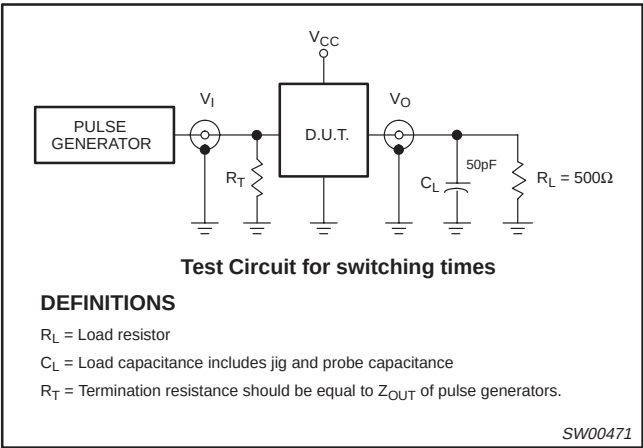


Figure 1. Load circuitry for switching times

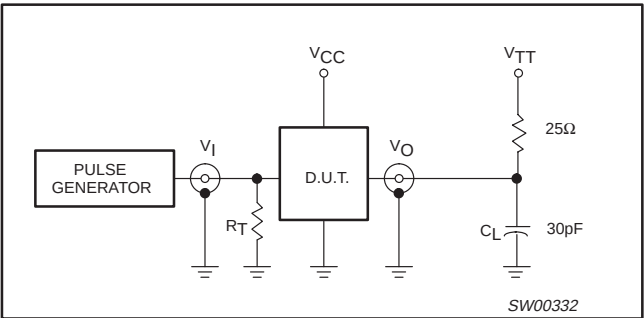


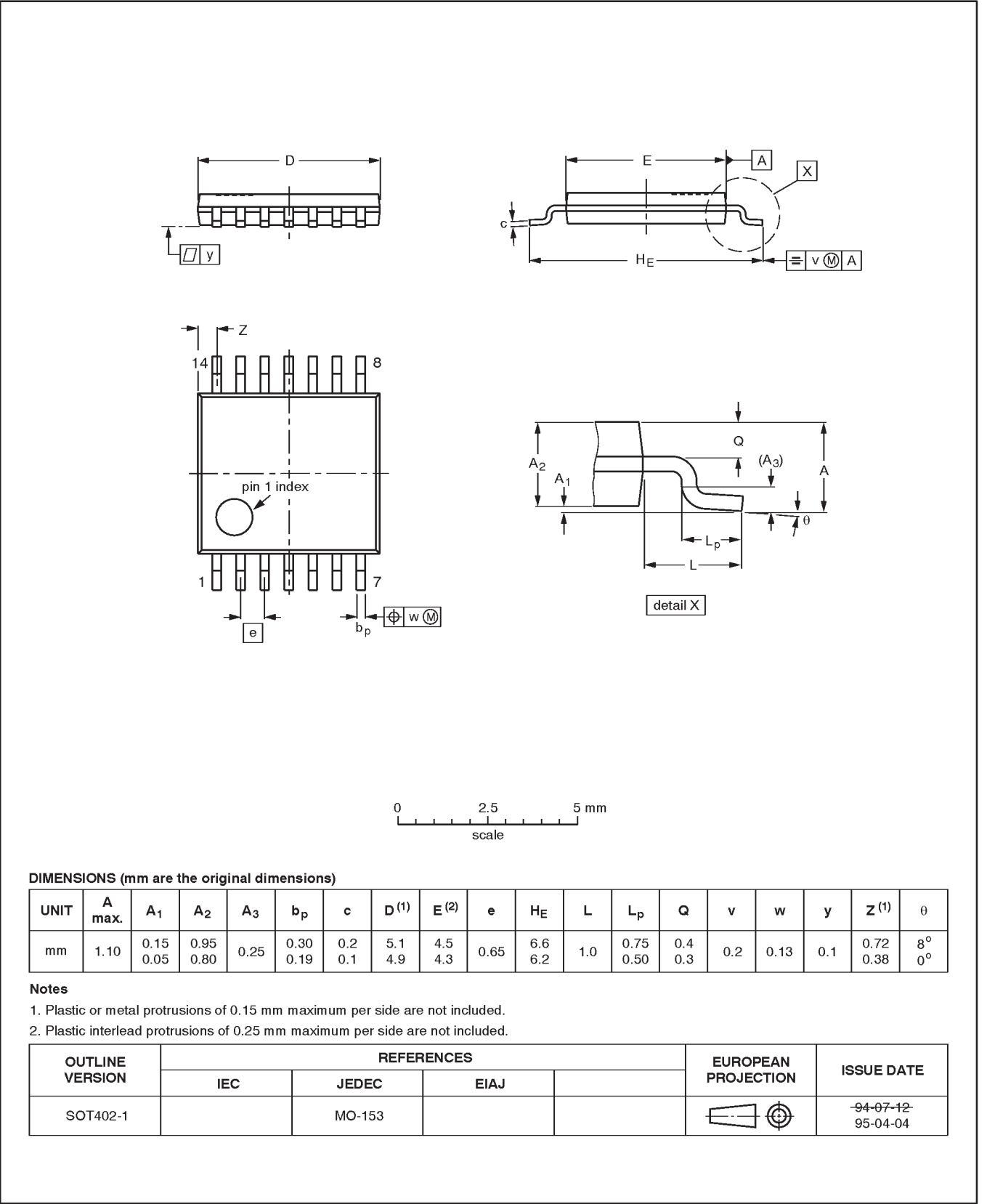
Figure 2. Load circuit for A outputs

Quad GTL/GTL+ to LVTTTL/TTL
bidirectional non-latched translator

GTL2005

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1



Quad GTL/GTL+ to LVTTTL/TTL
bidirectional non-latched translator

GTL2005

NOTES

Quad GTL/GTL+ to LVTTTL/TTL bidirectional non-latched translator

GTL2005

Data sheet status

Data sheet status	Product status	Definition [1]
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
Product specification	Production	This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.

[1] Please consult the most recently issued datasheet before initiating or completing a design.

Definitions

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information — Applications that are described herein for any of these products are for illustrative purposes only. Philips Semiconductors make no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Disclaimers

Life support — These products are not designed for use in life support appliances, devices or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips Semiconductors customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips Semiconductors for any damages resulting from such application.

Right to make changes — Philips Semiconductors reserves the right to make changes, without notice, in the products, including circuits, standard cells, and/or software, described or contained herein in order to improve design and/or performance. Philips Semiconductors assumes no responsibility or liability for the use of any of these products, conveys no license or title under any patent, copyright, or mask work right to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified.

Philips Semiconductors
811 East Arques Avenue
P.O. Box 3409
Sunnyvale, California 94088-3409
Telephone 800-234-7381

© Copyright Philips Electronics North America Corporation 1999
All rights reserved. Printed in U.S.A.

Date of release: 12-99

Document order number:

9397 750 06695

Let's make things better.