National Semiconductor

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DS3695/DS3695T/DS3696/DS3696T/DS3697/DS3698 Multipoint RS485/RS422 Transceivers/Repeaters

DS3695/DS3695T/DS3696/DS3696T/DS3697/DS3698
Multipoint RS485/RS422 Transceivers/Repeaters

General Description

The DS3695, DS3696, DS3697 and DS3698 are high speed differential TRI-STATE® bus/line transceivers/repeaters designed to meet the requirements of EIA standard RS485 with extended common mode range (+12V to −7V), for multipoint data transmission.

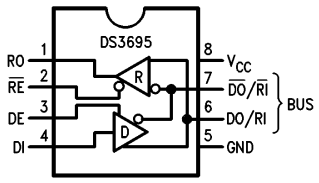
The driver and receiver outputs feature TRI-STATE capability. The driver outputs remain in TRI-STATE over the entire common mode range of +12V to −7V. Bus faults that cause excessive power dissipation within the device trigger a thermal shutdown circuit, which forces the driver outputs into the high impedance state. The DS3696 and DS3698 provide an output pin TS (thermal shutdown) which reports the occurrence of the thermal shutdown of the device. This is an "open collector" pin with an internal 10 kΩ pull-up resistor. This allows the line fault outputs of several devices to be wire OR-ed.

Both AC and DC specifications are guaranteed over the 0°C to 70°C temperature and 4.75V to 5.25V supply voltage range.

Features

- Meets EIA standard RS485 for multipoint bus transmission and is compatible with RS-422
- 15 ns driver propagation delays with 2 ns skew (typical)
- Single +5V supply
- −7V to +12V bus common mode range permits ±7V ground difference between devices on the bus
- Thermal shutdown protection
- High impedance to bus with driver in TRI-STATE or with power off, over the entire common mode range allows the unused devices on the bus to be powered down
- Combined impedance of a driver output and receiver input is less than one RS485 unit load, allowing up to 32 transceivers on the bus
- 70 mV typical receiver hysteresis

Connection and Logic Diagrams



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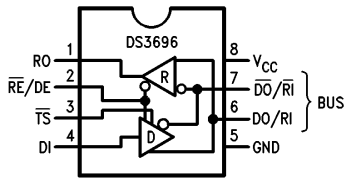
Top View



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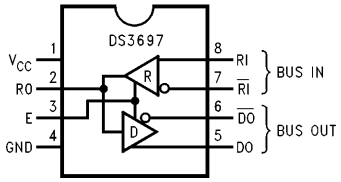
Top View



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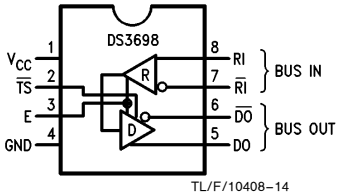
Top View



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Top View

Order Number DS3695N, DS3696N, DS3697N, DS3698N,
DS3695TN, DS3696TN, DS3695TJ or DS3696TJ
See NS Package Number J08A or N08E

Note: $\overline{TS}$ pin was $\overline{LF}$ (Line Fault) in previous datasheets and reports the occurrence of a thermal shutdown of the device.

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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage, V_{CC}	7V
Control Input Voltages	7V
Driver Input Voltage	7V
Driver Output Voltages	+15V/−10V
Receiver Input Voltages (DS3695, DS3696)	+15V/−10V
Receiver Common Mode Voltage (DS3697, DS3698)	±25V
Receiver Output Voltage	5.5V
Continuous Power Dissipation @ 25°C N Package	1.07W (Note 4)
Storage Temperature Range	−65°C to +150°C
Lead Temperature (Soldering, 4 sec.)	260°C

Recommended Operating Conditions

	Min	Max	Units
Supply Voltage, V_{CC}	4.75	5.25	V
Bus Voltage	−7	+12	V
Operating Free Air Temp. (T_A)			
Commercial	0	+70	°C
Industrial	−40	+85	°C

Electrical Characteristics 0°C ≤ T_A ≤ +70°C, 4.75V < V_{CC} < 5.25V unless otherwise specified (Notes 2 & 3)

Symbol	Parameter		Conditions		Min	Typ	Max	Units
V_{OD1}	Differential Driver Output Voltage (Unloaded)		I_O = 0				5	V
V_{OD2}	Differential Driver Output Voltage (with Load)		*(Figure 1)*	R = 50Ω; (RS-422) (Note 5)	2			V
				R = 27Ω; (RS-485)	1.5			V
ΔV_{OD}	Change in Magnitude of Driver Differential Output Voltage for Complementary Output States		*(Figure 1)*	R = 27Ω			0.2	V
V_{OC}	Driver Common Mode Output Voltage						3.0	V
$\Delta\lvert V_{OC}\rvert$	Change in Magnitude of Driver Common Mode Output Voltage for Complementary Output States						0.2	V
V_{IH}	Input High Voltage	DI, DE, $\overline{RE}$, E, $\overline{RE}$/DE			2			V
V_{IL}	Input Low Voltage						0.8	V
V_{CL}	Input Clamp Voltage			I_{IN} = −18 mA			−1.5	V
I_{IL}	Input Low Current			V_{IL} = 0.4V			−200	μA
I_{IH}	Input High Current			V_{IH} = 2.4V			20	μA
I_{IN}	Input Current	DO/RI, $\overline{DO}$/$\overline{RI}$ RI, $\overline{RI}$	V_{CC} = 0V or 5.25V $\overline{RE}$/DE or DE = 0V	V_{IN} = 12V			+1.0	mA
				V_{IN} = −7V			−0.8	mA
I_{OZD}	TRI-STATE Current DS3697 & DS3698	DO, $\overline{DO}$	V_{CC} = 0V or 5.25V, E = 0V −7V < V_O < +12V				±100	μA
V_{TH}	Differential Input Threshold Voltage for Receiver		−7V ≤ V_{CM} ≤ +12V		−0.2		+0.2	V
ΔV_{TH}	Receiver Input Hysteresis		V_{CM} = 0V			70		mV
V_{OH}	Receiver Output High Voltage		I_{OH} = −400 μA		2.4			V
V_{OL}	Output Low Voltage	RO	I_{OL} = 16 mA (Note 5)				0.5	V
		$\overline{TS}$	I_{OL} = 8 mA				0.45	V
I_{OZR}	OFF-State (High Impedance) Output Current at Receiver		V_{CC} = Max 0.4V ≤ V_O ≤ 2.4V				±20	μA
R_{IN}	Receiver Input Resistance		−7V ≤ V_{CM} ≤ +12V		12			kΩ
I_{CC}	Supply Current		No Load (Note 5)	Driver Outputs Enabled		42	60	mA
				Driver Outputs Disabled		27	40	mA

Electrical Characteristics (Continued)

$0°C \le T_A \le +70°C$, $4.75V < V_{CC} < 5.25V$ unless otherwise specified (Notes 2 & 3)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
I_{OSD}	Driver Short-Circuit Output Current	$V_O = -7V$ (Note 5)			−250	mA
		$V_O = +12V$ (Note 5)			+250	mA
I_{OSR}	Receiver Short-Circuit Output Current	$V_O = 0V$	−15		−85	mA

Note 1: ''Absolute Maximum Ratings'' are those beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the device should be operated at these limits. The tables of ''Electrical Characteristics'' provide conditions for actual device operation.

Note 2: All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to device ground unless otherwise specified.

Note 3: All typicals are given for $V_{CC} = 5V$ and $T_A = 25°C$.

Note 4: Derate linearly at 11.1 mW/°C to 570 mW at 70°C.

Note 5: All limits for which Note 5 is applied must be derated by 10% for DS3695T and DS3696T. Other parameters remain the same for this extended temperature range device ($-40°C \le T_A \le +85°C$).

Switching Characteristics

$0°C \le T_A \le +70°C$, $4.75V < V_{CC} < 5.25V$ unless otherwise specified (Notes 3, 6)

Receiver Switching Characteristics *(Figures 2, 3* and *4)*

Symbol	Conditions	Min	Typ	Max	Units
t_{PLH}	$C_L = 15$ pF S1 and S2 Closed	15	25	37	ns
t_{PHL}		15	25	37	ns
$\lvert t_{PLH} - t_{PHL} \rvert$		0			ns
t_{PLZ}	$C_L = 15$ pF, S2 Open	5	12	16	ns
t_{PHZ}	$C_L = 15$ pF, S1 Open	5	12	16	ns
t_{PZL}	$C_L = 15$ pF, S2 Open	7	15	20	ns
t_{PZH}	$C_L = 15$ pF, S1 Open	7	15	20	ns

Driver Switching Characteristics

Symbol	Conditions	Min	Typ	Max	Units
SINGLE ENDED CHARACTERISTICS *(Figures 5, 6* and *7)*					
t_{PLH}	$R_{LDIFF} = 60\Omega$, $C_{L1} = C_{L2} = 100$ pF	9	15	22	ns
t_{PHL}		9	15	22	ns
$t_{SKEW} \lvert t_{PLH} - t_{PHL} \rvert$			2	8	ns
t_{PLZ}	$C_L = 15$ pF, S2 Open	7	15	30	ns
t_{PHZ}	$C_L = 15$ pF, S1 Open	7	15	30	ns
t_{PZL}	$C_L = 100$ pF, S2 Open	30	35	50	ns
t_{PZH}	$C_L = 100$ pF, S1 Open	30	35	50	ns
DIFFERENTIAL CHARACTERISTICS (*Figures 5* and *8*)					
t_r, t_f	$R_{LDIFF} = 60\Omega$, $C_{L1} = C_{L2} = 100$ pF	6	10	18	ns

Note 6: Switching Characteristics apply for DS3695, DS3695T, DS3696, DS3696T, DS3697 only.

AC Test Circuits and Switching Waveforms

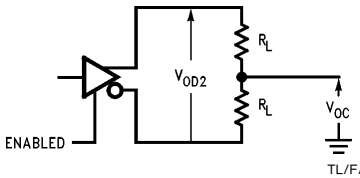

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FIGURE 1. Driver V_{OD} and V_{OC}

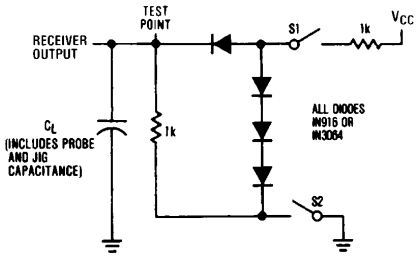

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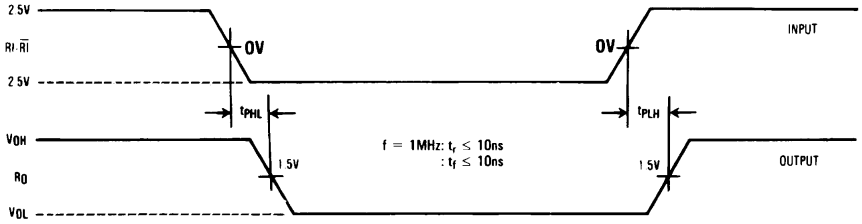
FIGURE 2. Receiver Propagation Delay Test Circuit



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Note: Differential input voltage may be realized by grounding $\overline{RI}$ and pulsing RI between +2.5V and −2.5V.

FIGURE 3. Receiver Input-to-Output Propagation Delay Timing

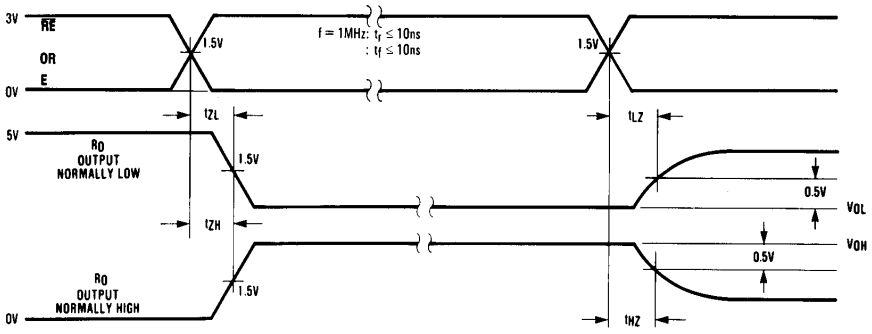

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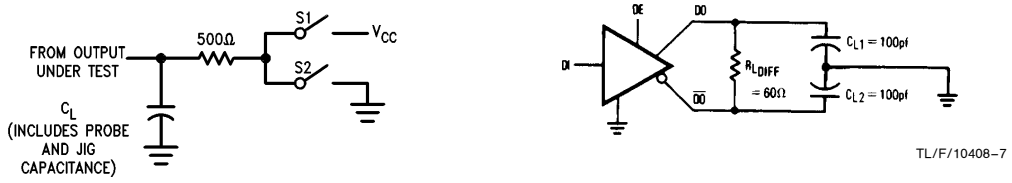
FIGURE 4. Receiver Enable/Disable Propagation Delay Timing

AC Test Circuits and Switching Waveforms (Continued)



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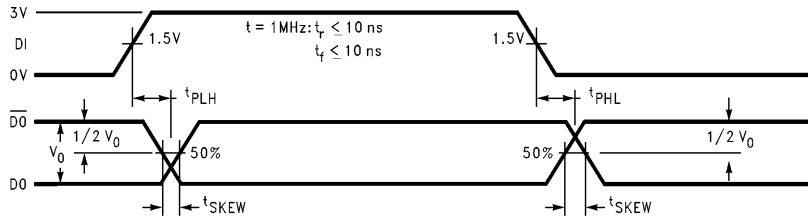
Note: Unless otherwise specified the switches are closed.

FIGURE 5. Driver Propagation Delay and Transition Time Test Circuits



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Note: t_{PLH} and t_{PHL} are measured to the respective 50% points. t_{SKEW} is the difference between propagation delays of the complementary outputs.

FIGURE 6. Driver Input-to-Output Propagation Delay Timing (Single-Ended)

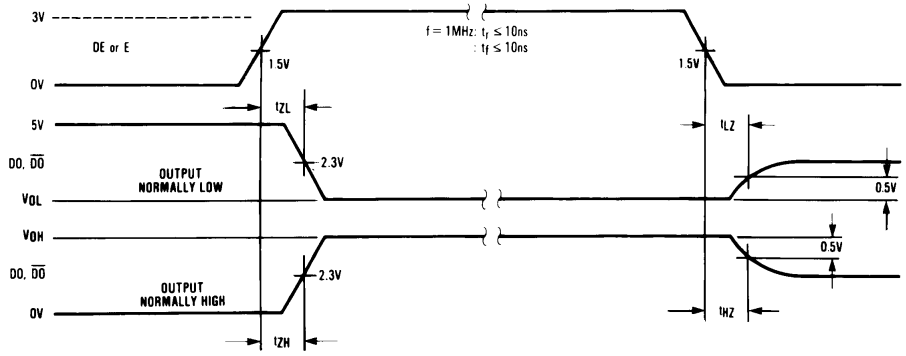

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FIGURE 7. Driver Enable/Disable Propagation Delay Timing

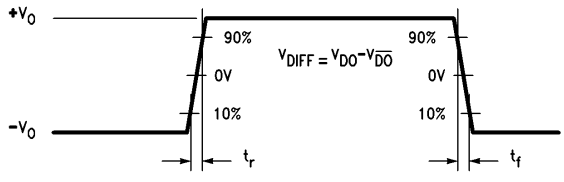

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FIGURE 8. Driver Differential Transition Timing

Function Tables

DS3695/DS3696 Transmitting

Inputs			Thermal Shutdown	Outputs		
$\overline{RE}$	DE	DI		$\overline{DO}$	DO	$\overline{TS}$* (DS3696 Only)
X	1	1	OFF	0	1	H
X	1	0	OFF	1	0	H
X	0	X	OFF	Z	Z	H
X	1	X	ON	Z	Z	L

DS3695/DS3696 Receiving

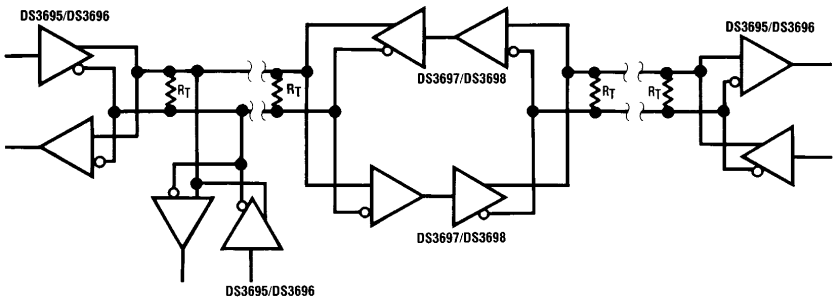
Inputs			Outputs	
$\overline{RE}$	DE	RI−$\overline{RI}$	RO	$\overline{TS}$* (DS3696 Only)
0	0	$\geq +0.2V$	1	H
0	0	$\leq -0.2V$	0	H
1	0	X	Z	H

DS3697/DS3698

Inputs		Thermal Shutdown	Outputs			
E	RI-$\overline{RI}$		$\overline{DO}$	DO	RO (DS3697 Only)	$\overline{TS}$* (DS3698 Only)
1	$\geq +0.2V$	OFF	0	1	1	H
1	$\leq -0.2V$	OFF	1	0	0	H
0	X	OFF	Z	Z	Z	H
1	$\geq +0.2V$	ON	Z	Z	1	L
1	$\leq -0.2V$	ON	Z	Z	0	L

X—Don't care condition
Z—High impedance state
*$\overline{TS}$ is an "open collector" output with an on-chip 10 kΩ pull-up resistor that reports the occurrence of a thermal shutdown of the device.

Typical Application



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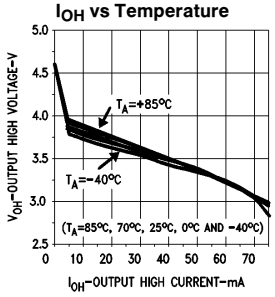
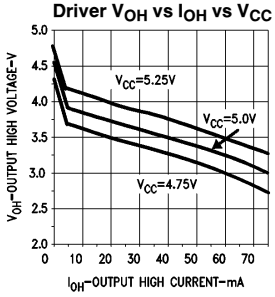
Note: Repeater control logic not shown, see AN-702.

Typical Performance Characteristics

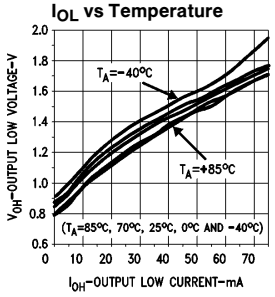
Driver V_{OH} vs I_{OH} vs Temperature



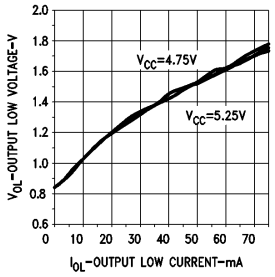
Driver V_{OH} vs I_{OH} vs V_{CC}



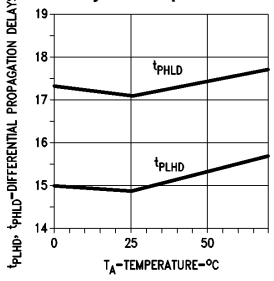
Driver V_{OL} vs I_{OL} vs Temperature



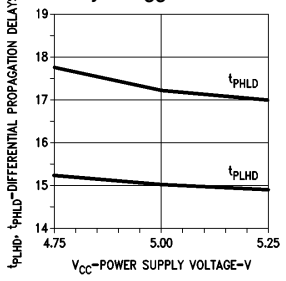
Driver V_{OL} vs I_{OL} vs V_{CC}



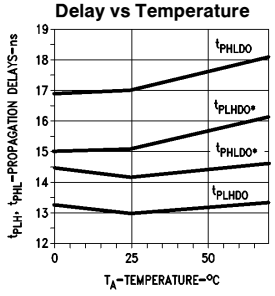
Driver Differential Propagation Delay vs Temperature



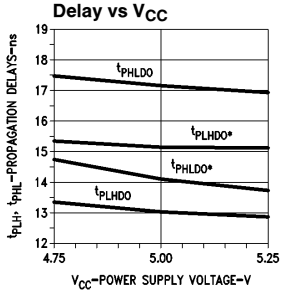
Driver Differential Propagation Delay vs V_{CC}



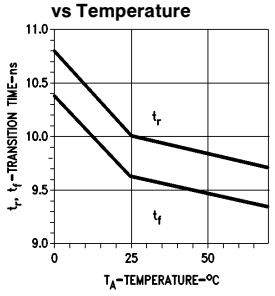
Driver Single-Ended Propagation Delay vs Temperature



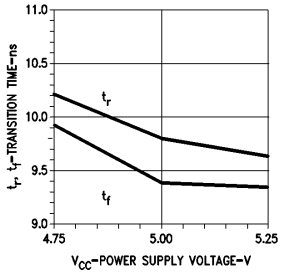
Driver Single-Ended Propagation Delay vs V_{CC}



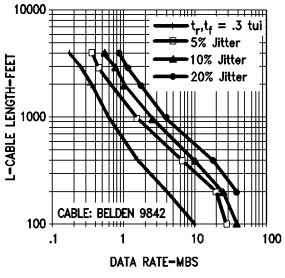
Driver Transition Time vs Temperature



Driver Transition Time vs V_{CC}



Cable Length vs Data Rate



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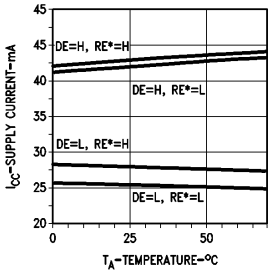
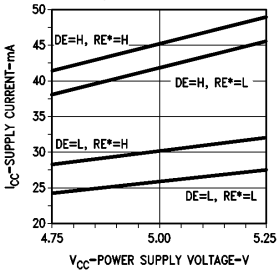
Typical Performance Characteristics (Continued)

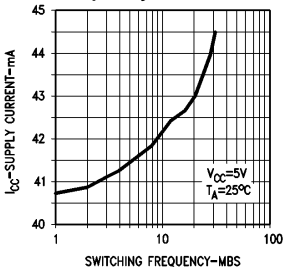
Supply Current vs Temperature



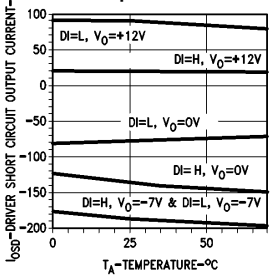
Supply Current vs Power Supply Voltage



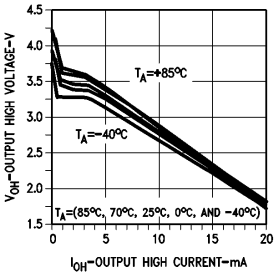
Driver I_{CC} vs Switching Frequency



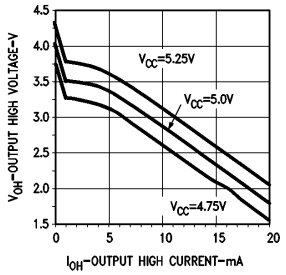
Driver Short Circuit Current vs Temperature



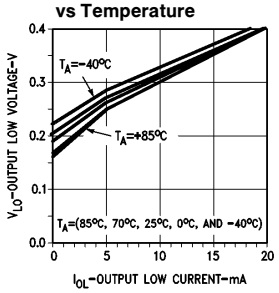
Receiver V_{OH} vs I_{OH} vs Temperature



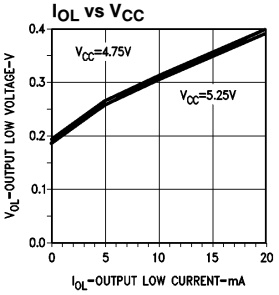
Receiver V_{OH} vs I_{OH} vs V_{CC}



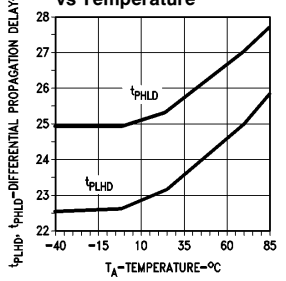
Receiver V_{OL} vs I_{OL} vs Temperature



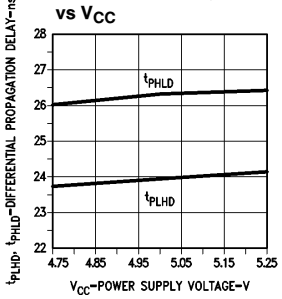
Receiver V_{OL} vs I_{OL} vs V_{CC}



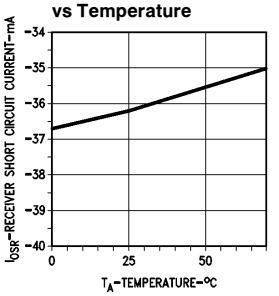
Receiver Differential Propagation Delay vs Temperature



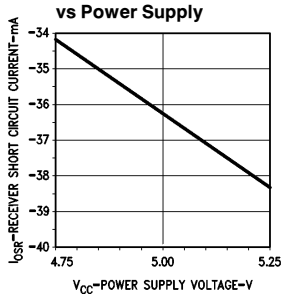
Receiver Differential Propagation Delay vs V_{CC}



Receiver Short Circuit Current vs Temperature



Receiver Short Circuit Current vs Power Supply



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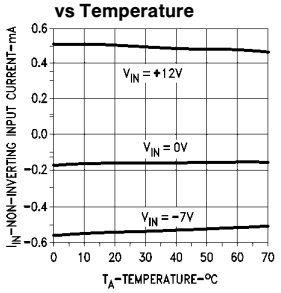
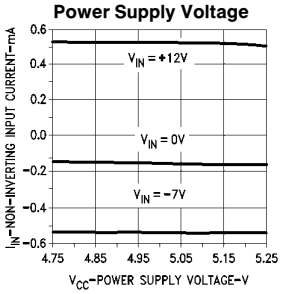
Typical Performance Characteristics (Continued)

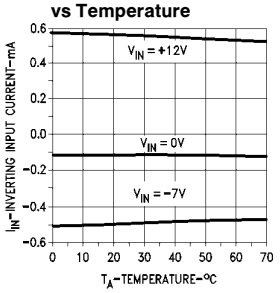
Receiver Non-Inverting Input Current vs Temperature



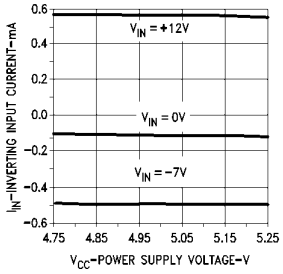
Receiver Non-Inverting Input Current vs Power Supply Voltage



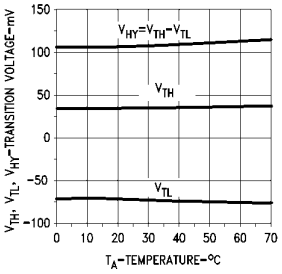
Receiver Inverting Input Current vs Temperature



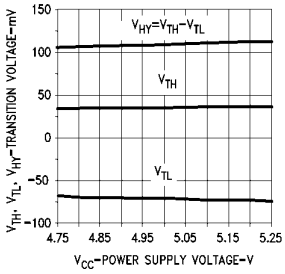
Receiver Inverting Input Current vs Power Supply Voltage



Hysteresis and Differential Transition Voltage vs Temperature



Hysteresis and Differential Transition Voltage vs V_{CC}



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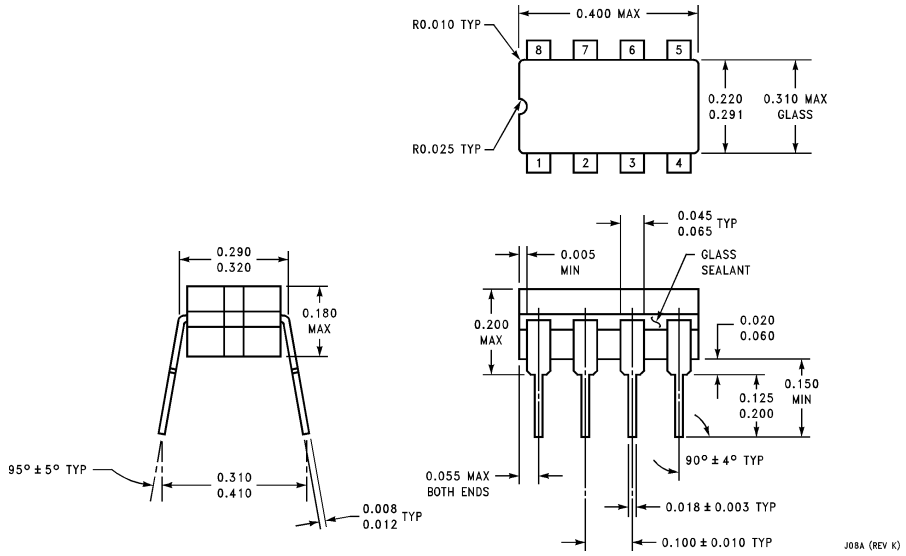
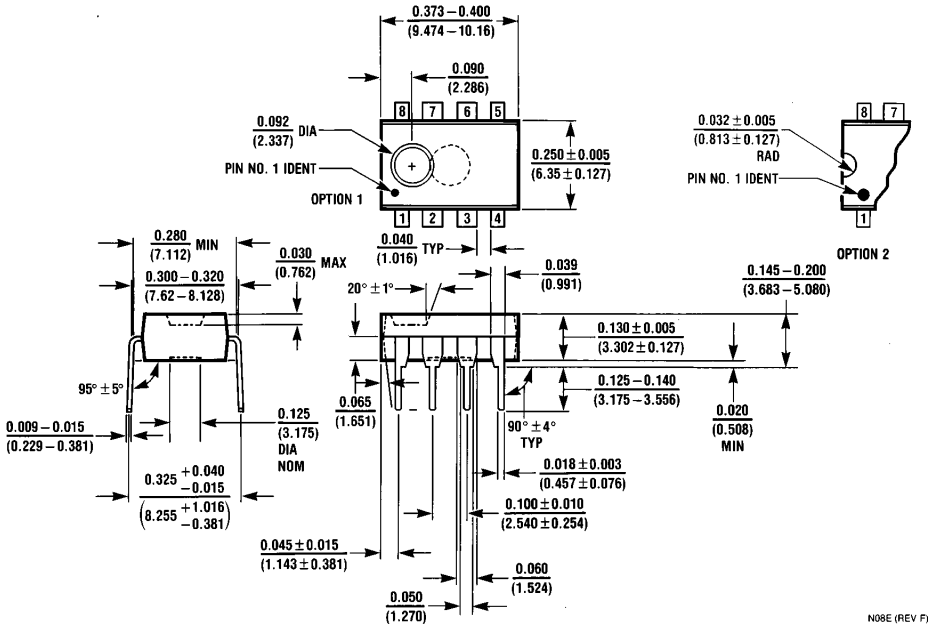
Physical Dimensions inches (millimeters) unless otherwise noted



8-Lead Ceramic Dual-In-Line Package (J)
Order Number DS3695TJ or DS3696TJ
NS Package Number J08A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



8-Lead Molded Dual-In-Line Package (N)
Order Number DS3695N, DS3696N, DS3697N, DS3698N, DS3695TN or DS3696TN
NS Package Number N08E

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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