

No.3820

2SK1725

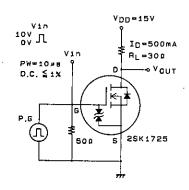
N-Channel MOS Silicon FET Very High-Speed Switching Applications

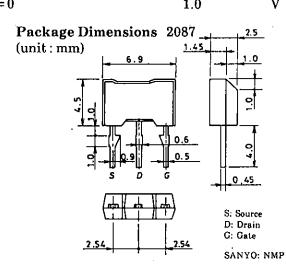
Features

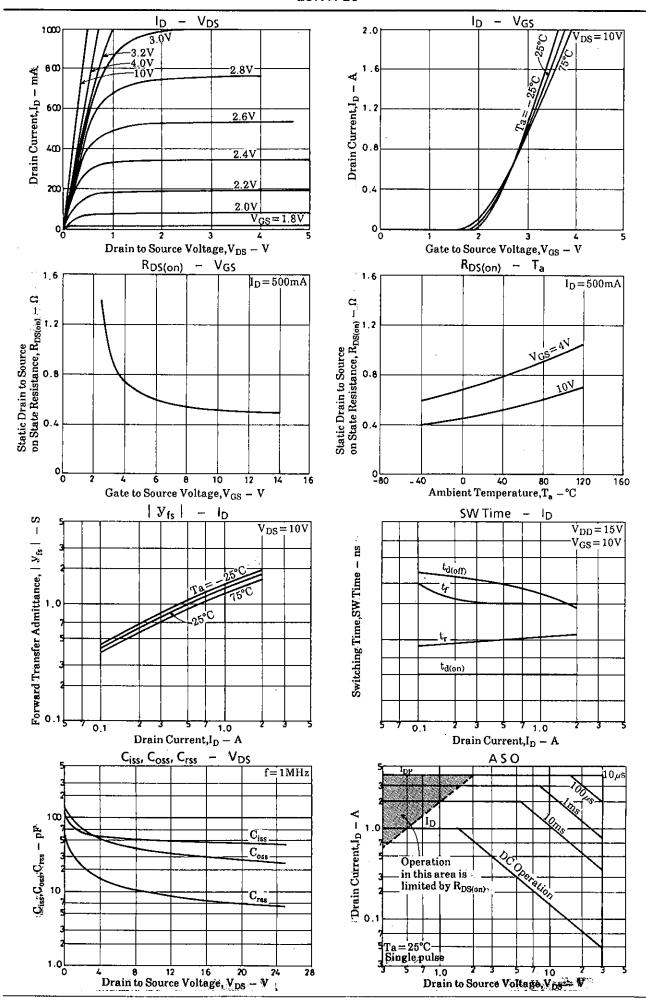
- · Low ON resistance.
- · Very high-speed switching.
- · Low-voltage drive.
- · Meets radial taping.

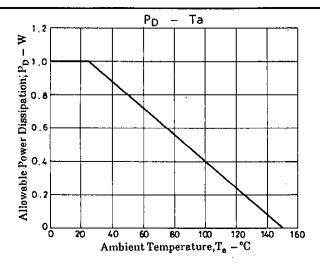
Absolute Maximum Ratings at Ta Drain to Source Voltage Gate to Source Voltage Drain Current(DC) Drain Current(Pulse) Allowable Power Dissipation Channel Temperature Storage Temperature	a = 25°C V_{DSS} V_{GSS} I_{D} I_{DP} P_{D} T_{ch} T_{stg}	PW≤10 μ s, duty cycle≤1%	- 55 to +	30 ±15 1 4 1 150	unit V V A W °C °C	
Electrical Characteristics at Ta=	25°C		min	typ	max	unit
D-S Breakdown Voltage		$I_D = 1 \text{mA}, V_{GS} = 0$	30	оyр	max	V
Zero Gate Voltage	IDSS	$V_{DS} = 30V, V_{GS} = 0$	00		10	μ A
Drain Current	∿D99	· DS co., · GS c				<i>,</i>
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0$			±10	μ A
Cutoff Voltage	V _{GS(off)}	$V_{DS} = 10V, I_D = 1mA$	1.0		2.0	V
Forward Transfer Admittance		$V_{DS} = 10V, I_D = 500 \text{mA}$	0.6	1.0		S
Static Drain to Source	R _{DS(on)}	$I_D = 500 \text{mA}, V_{GS} = 10 \text{V}$		0.5	0.75	Ω
on State Resistance	R _{DS(on)}	$I_D = 500 \text{mA}, V_{GS} = 4 \text{V}$		0.75	1.1	Ω
Input Capacitance	Ciss	$V_{DS} = 10V, f = 1MHz$		50		рF
Output Capacitance	Coss	$V_{DS} = 10V_{f} = 1MHz$		35		рF
Reverse Transfer Capacitance	Crss	$V_{DS} = 10V, f = 1MHz$		10		рF
Turn-ON Delay Time	$\mathbf{t_{d(on)}}$	See specified Test Circuit.		5		ns
Rise Time	$\mathbf{t_r}$	"		10		ns
Turn-OFF Delay Time	$\mathbf{t_{d(off)}}$	"		30		ns
Fall Time	tf	"		20		ns
Diode Forward Voltage	$\dot{ m v}_{ m SD}$	$I_S = 1A, V_{GS} = 0$		1.0		V

Switching Time Test Circuit









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