TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (L^2-π-MOSⅤ)

2SK2200

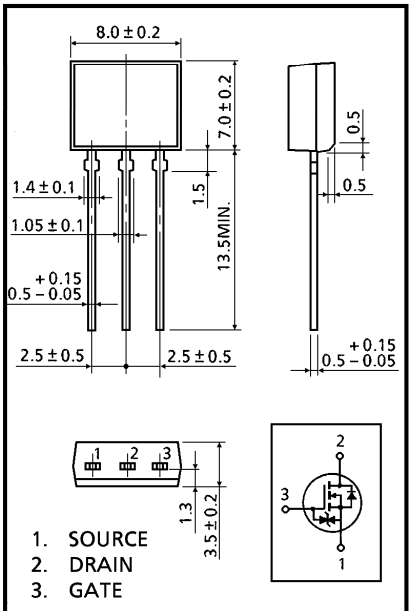
HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS

CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

INDUSTRIAL APPLICATIONS

- 4V Gate Drive
- Low Drain-Source ON Resistance : $R_{DS\ (ON)} = 0.28\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}| = 3.5S$ (Typ.)
- Low Leakage Current : $I_{DSS} = 100\mu A$ (Max.) ($V_{DS} = 100V$)
- Enhancement-Mode : $V_{th} = 0.8 \sim 2.0V$ ($V_{DS} = 10V$, $I_D = 1mA$)

Unit in mm



JEDEC	—
EIAJ	—
TOSHIBA	2-8M1B

Weight : 0.55g

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DSS}	100	V
Drain-Gate Voltage ($R_{GS} = 20k\Omega$)		V_{DGR}	100	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	DC	I_D	3	A
	Pulse	I_{DP}	12	A
Drain Power Dissipation (Ta = 25°C)		P_D	1.3	W
Single Pulse Avalanche Energy**		E_{AS}	140	mJ
Avalanche Current		I_{AR}	3	A
Repetitive Avalanche Energy*		E_{AR}	0.13	mJ
Channel Temperature		T_{ch}	150	°C
Storage Temperature Range		T_{stg}	−55~150	°C

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Ambient	$R_{th\ (ch-a)}$	96.1	°C / W

Note ;

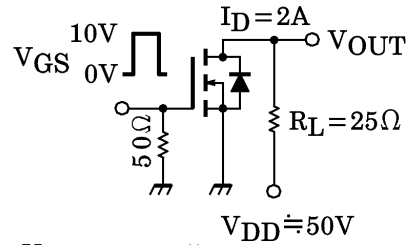
* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

** $V_{DD} = 50V$, Starting $T_{ch} = 25°C$, $L = 25mH$, $R_G = 25\Omega$, $I_{AR} = 3A$

This transistor is an electrostatic sensitive device. Please handle with caution.

961001EAA2

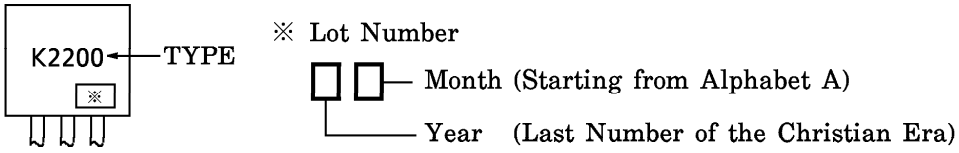
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		I_{GSS}	$V_{GS}=\pm16V$, $V_{DS}=0V$	—	—	±10	μA
Drain Cut-off Current		I_{DSS}	$V_{DS}=100V$, $V_{GS}=0V$	—	—	100	μA
Drain-Source Breakdown Voltage		$V_{(BR)\,DSS}$	$I_D=10mA$, $V_{GS}=0V$	100	—	—	V
Gate Threshold Voltage		V_{th}	$V_{DS}=10V$, $I_D=1mA$	0.8	—	2.0	V
Drain-Source ON Resistance		$R_{DS\,(ON)}$	$V_{GS}=4V$, $I_D=2A$	—	0.36	0.45	Ω
			$V_{GS}=10V$, $I_D=2A$	—	0.28	0.35	
Forward Transfer Admittance		$\|Y_{fs}\|$	$V_{DS}=10V$, $I_D=2A$	1.5	3.5	—	S
Input Capacitance		C_{iss}	$V_{DS}=10V$, $V_{GS}=0V$ f=1MHz	—	280	—	pF
Reverse Transfer Capacitance		C_{rss}		—	50	—	
Output Capacitance		C_{oss}		—	105	—	
Switching Time	Rise Time	t_r	 V_{IN} : t_r, $t_f<5ns$, Duty ≦ 1%, $t_w=10\mu s$	—	20	—	ns
	Turn-on Time	t_{on}		—	50	—	
	Fall Time	t_f		—	40	—	
	Turn-off Time	t_{off}		—	170	—	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Q_g	$V_{DD}\fallingdotseq80V$, $V_{GS}=10V$ $I_D=3A$	—	13.5	—	nC
Gate-Source Charge		Q_{gs}		—	8.5	—	
Gate-Drain ("Miller") Charge		Q_{gd}		—	5	—	

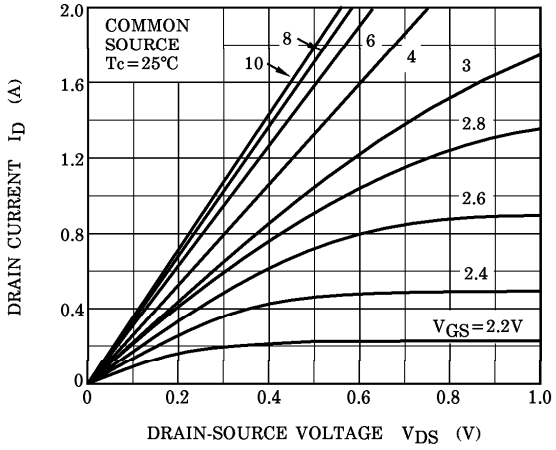
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I_{DR}	—	—	—	3	A
Pulse Drain Reverse Current	I_{DRP}	—	—	—	12	A
Diode Forward Voltage	V_{DSF}	$I_{DR}=3A$, $V_{GS}=0V$	—	—	−1.5	V
Reverse Recovery Time	t_{rr}	$I_{DR}=3A$, $V_{GS}=0V$ $dI_{DR}/dt=50A/\mu s$	—	100	—	ns
Reverse Recovery Charge	Q_{rr}		—	0.2	—	μC

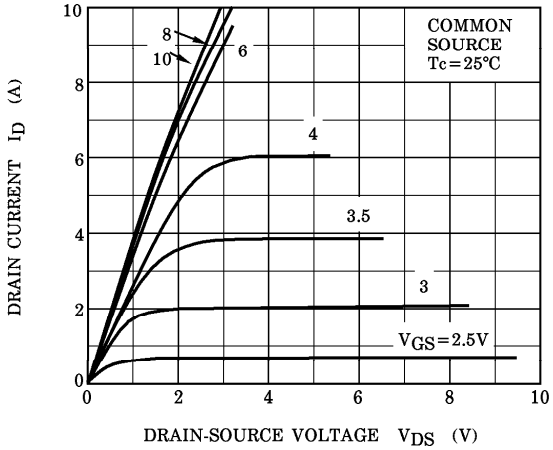
MARKING



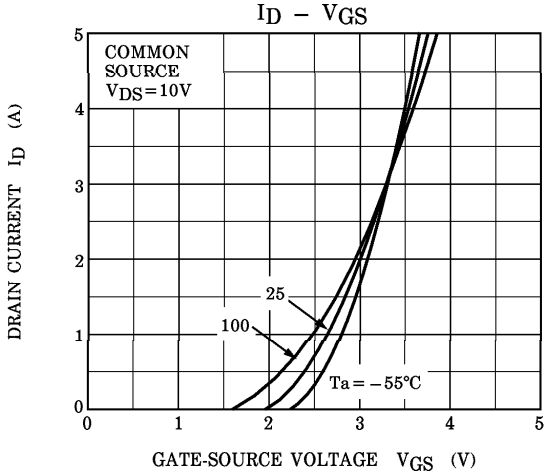
$I_D - V_{DS}$



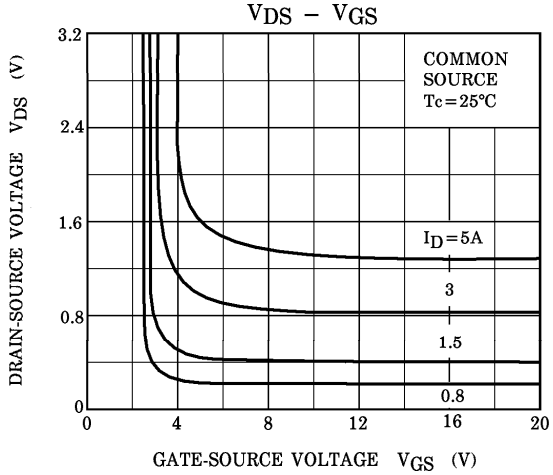
$I_D - V_{DS}$



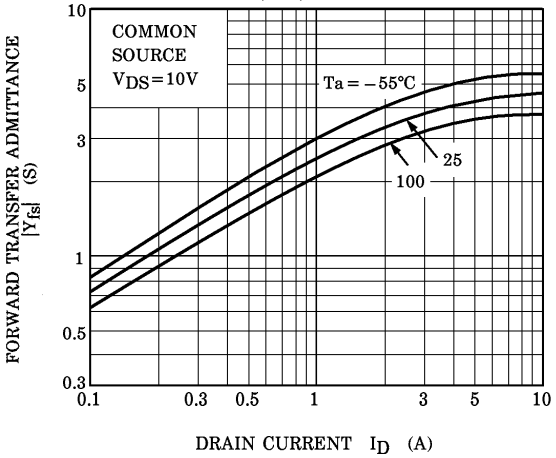
$I_D - V_{GS}$



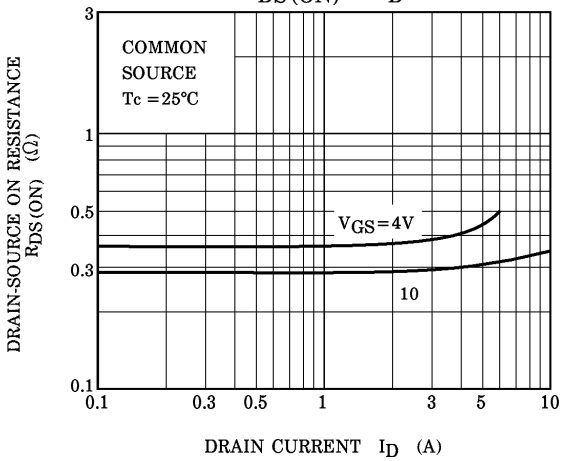
$V_{DS} - V_{GS}$



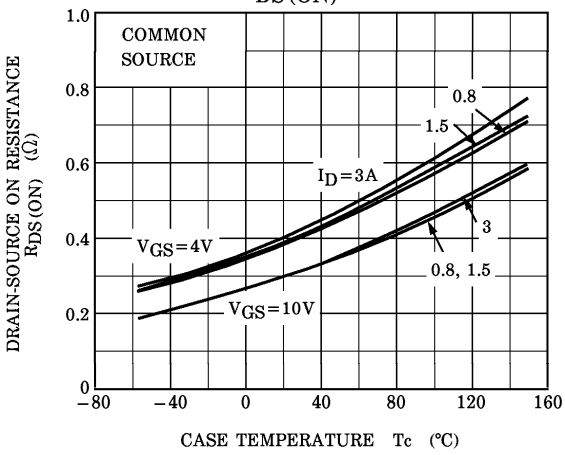
$|Y_{fs}| - I_D$



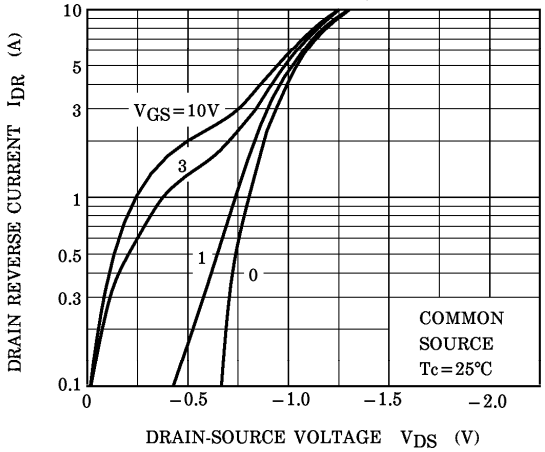
$R_{DS\,(ON)} - I_D$



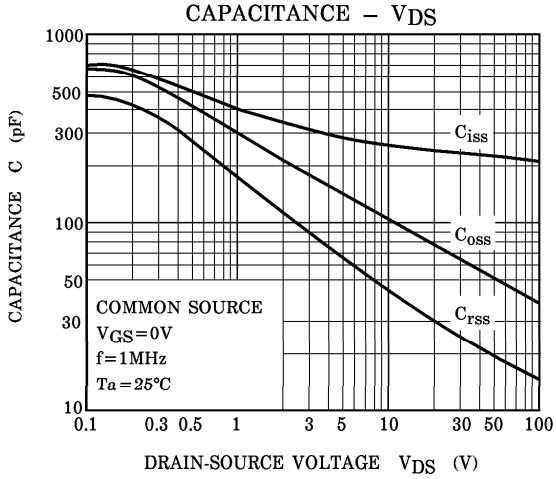
$R_{DS(ON)}$ – Tc



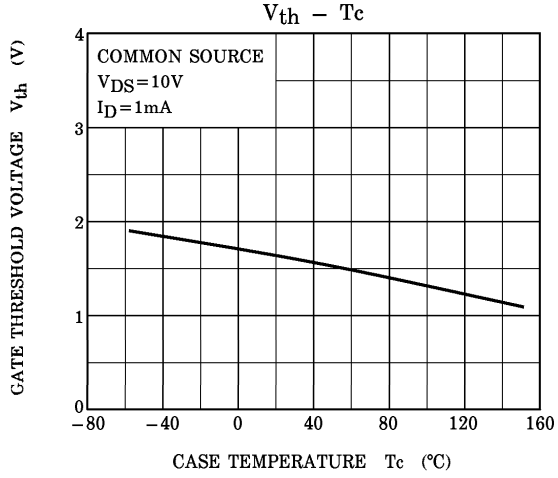
I_{DR} – V_{DS}



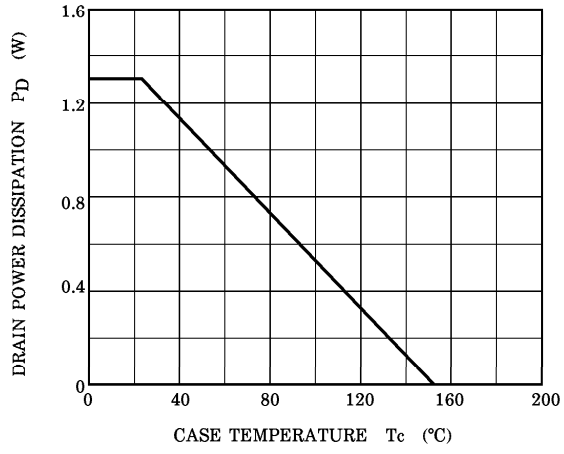
CAPACITANCE – V_{DS}



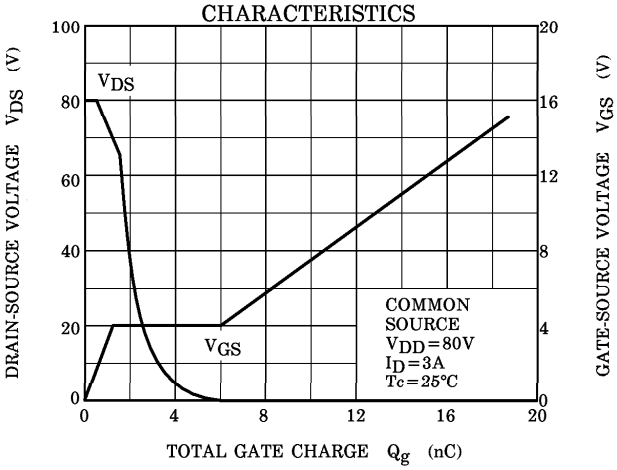
V_{th} – Tc



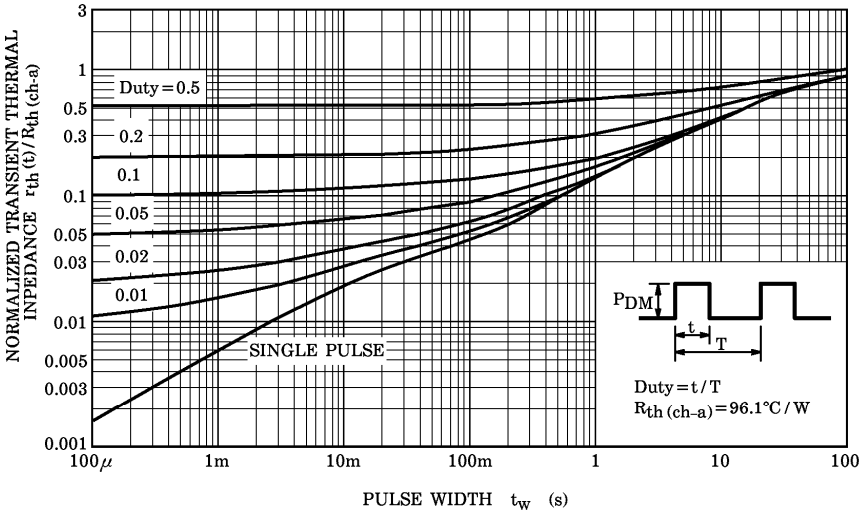
P_D – Tc



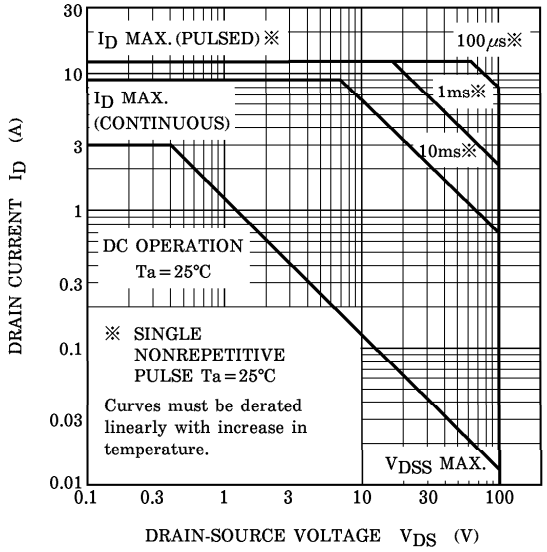
DYNAMIC INPUT / OUTPUT CHARACTERISTICS



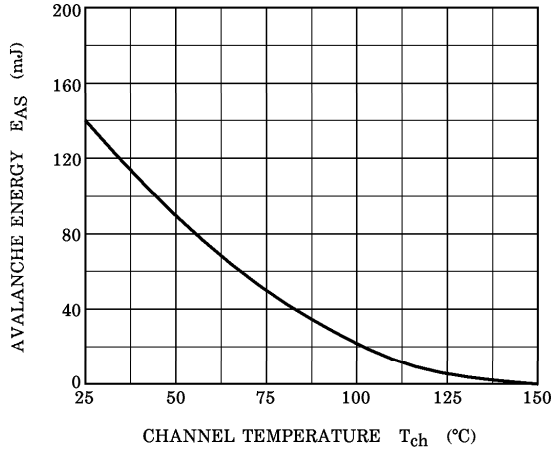
r_{th} – t_w

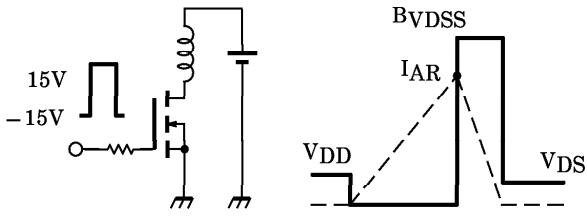


SAFE OPERATING AREA



E_{AS} – T_{ch}





TEST CIRCUIT WAVE FORM

Peak I_{AR} = 3A, R_G = 25Ω
V_{DD} = 25V, L = 25mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$