

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (π -MOS V)

2SK2551

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS

CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

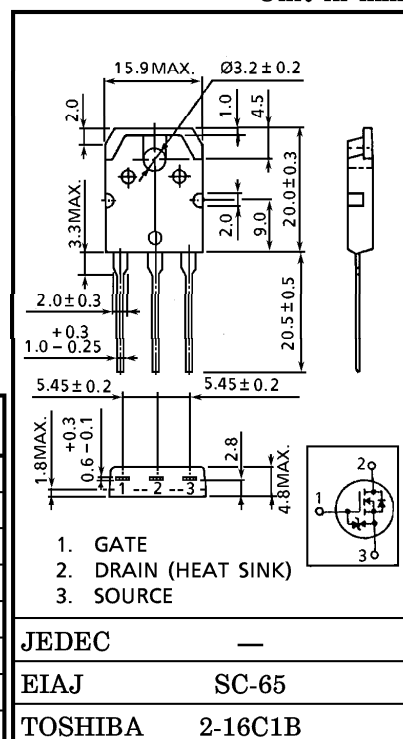
- Low Drain-Source ON Resistance : $R_{DS(ON)} = 7.2m\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}| = 50S$ (Typ.)
- Low Leakage Current : $I_{DSS} = 100\mu A$ (Max.) ($V_{DS} = 50V$)
- Enhancement-Mode : $V_{th} = 1.5 \sim 3.0V$ ($V_{DS} = 10V$, $I_D = 1mA$)

MAXIMUM RATINGS ($T_a = 25^\circ C$)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DSS}	50	V
Drain-Gate Voltage ($R_{GS} = 20k\Omega$)		V_{DGR}	50	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	DC	I_D	50	A
	Pulse	I_{DP}	200	A
Drain Power Dissipation ($T_c = 25^\circ C$)		P_D	150	W
Single Pulse Avalanche Energy**		E_{AS}	894	mJ
Avalanche Current		I_{AR}	50	A
Repetitive Avalanche Energy*		E_{AR}	15	mJ
Channel Temperature		T_{ch}	150	$^\circ C$
Storage Temperature Range		T_{stg}	$-55 \sim 150$	$^\circ C$

INDUSTRIAL APPLICATIONS

Unit in mm



Weight : 4.6g

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th(ch-c)}$	0.833	$^\circ C / W$
Thermal Resistance, Channel to Ambient	$R_{th(ch-a)}$	50	$^\circ C / W$

Note ;

* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

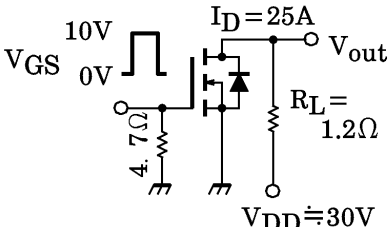
** $V_{DD} = 25V$, Starting $T_{ch} = 25^\circ C$, $L = 440\mu H$, $R_G = 25\Omega$, $I_{AR} = 50A$

This transistor is an electrostatic sensitive device.
please Handle with caution.

961001EAA2

- TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

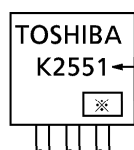
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		I _{GSS}	V _{GS} =±16V, V _{DS} =0V	—	—	±10	μA
Drain Cut-off Current		I _{DSS}	V _{DS} =50V, V _{GS} =0V	—	—	100	μA
Drain-Source Breakdown Voltage		V (BR) DSS	I _D =10mA, V _{GS} =0V	50	—	—	V
Gate Threshold Voltage		V _{th}	V _{DS} =10V, I _D =1mA	1.5	—	3.0	V
Drain-Source ON Resistance		R _{DS} (ON)	V _{GS} =10V, I _D =25A	—	7.2	11	mΩ
Forward Transfer Admittance		Y _{fs}	V _{DS} =10V, I _D =25A	30	50	—	S
Input Capacitance		C _{iss}	V _{DS} =10V, V _{GS} =0V, f=1MHz	—	4000	—	pF
Reverse Transfer Capacitance		C _{rss}		—	800	—	
Output Capacitance		C _{oss}		—	2000	—	
Switching Time	Rise Time	t _r	 V _{IN} : t _r , t _f <5ns, Duty ≤1%, t _w =10μs	—	25	—	ns
	Turn-on Time	t _{on}		—	40	—	
	Fall Time	t _f		—	120	—	
	Turn-off Time	t _{off}		—	360	—	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Q _g	V _{DD} ≐40V, V _{GS} =10V I _D =50A	—	130	—	nC
Gate-Source Charge		Q _{gs}		—	90	—	
Gate-Drain (“Miller”) Charge		Q _{gd}		—	40	—	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I_{DR}	—	—	—	50	A
Pulse Drain Reverse Current	I_{DRP}	—	—	—	200	A
Diode Forward Voltage	V_{DSF}	$I_{DR} = 50A, V_{GS} = 0V$	—	—	-1.7	V
Reverse Recovery Time	t_{rr}	$I_{DR} = 50A, V_{GS} = 0V$	—	140	—	ns
Reverse Recovery Charge	Q_{rr}	$dI_{DR} / dt = 50A / \mu s$	—	77	—	nC

MARKING



TYPE

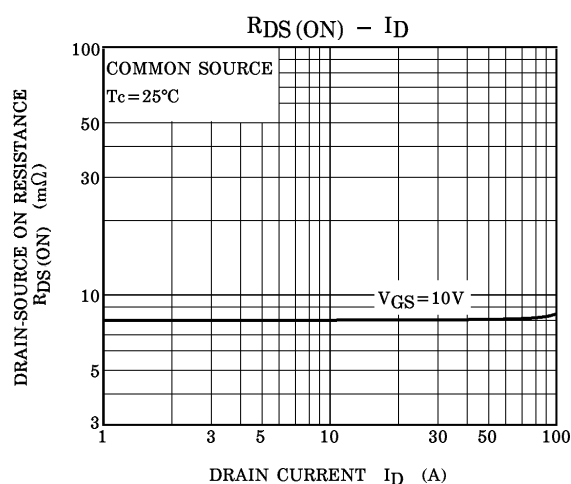
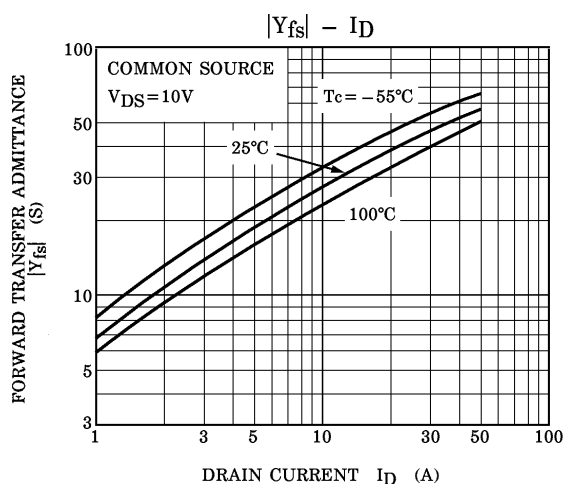
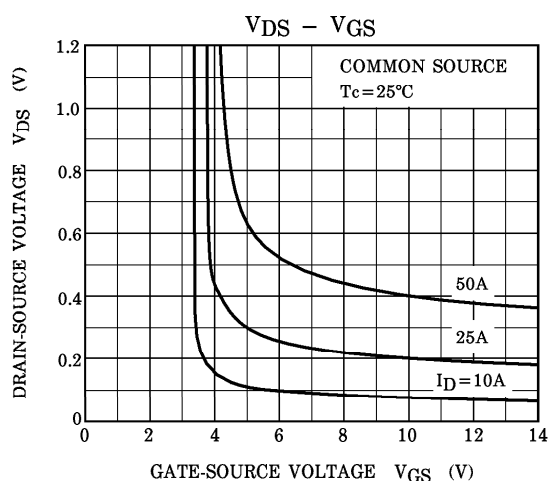
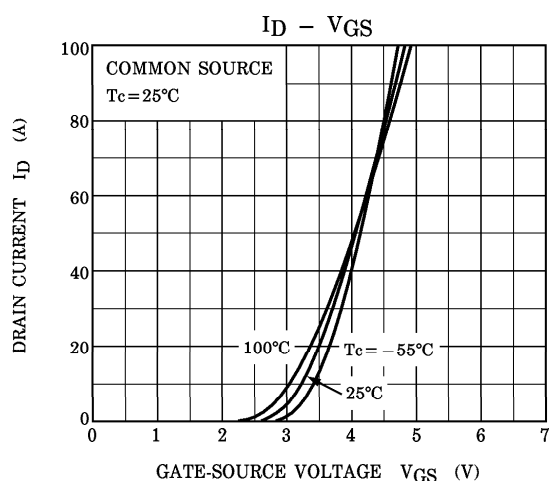
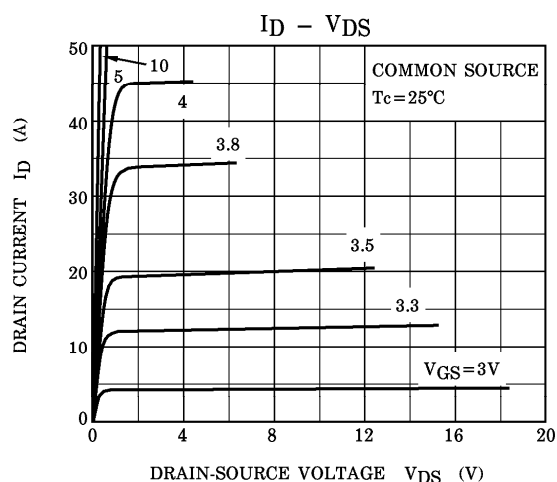
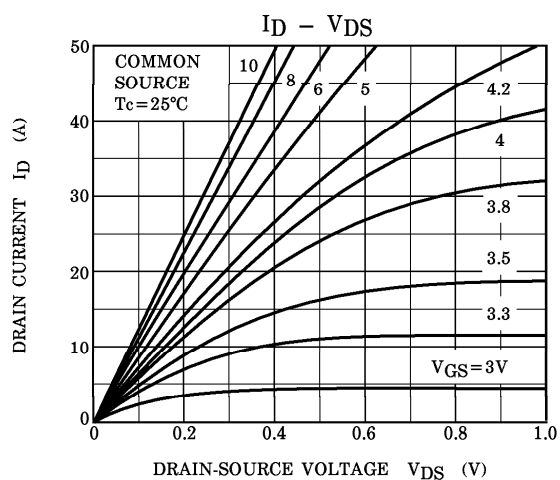
※ Lot Number

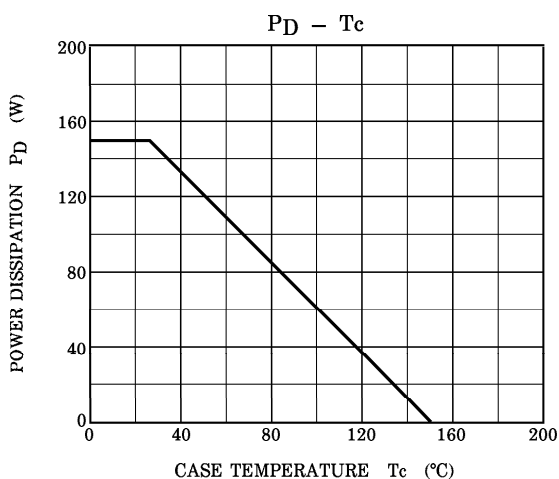
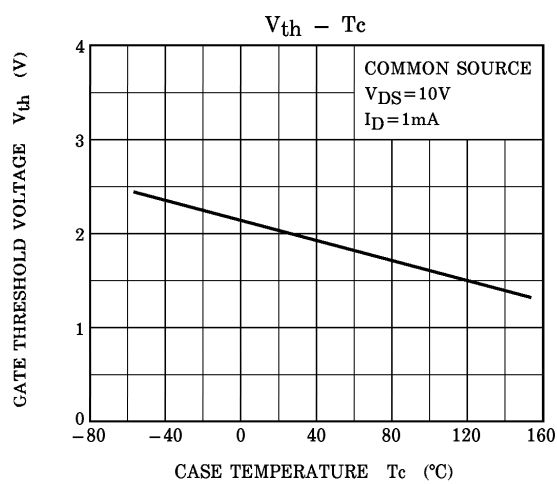
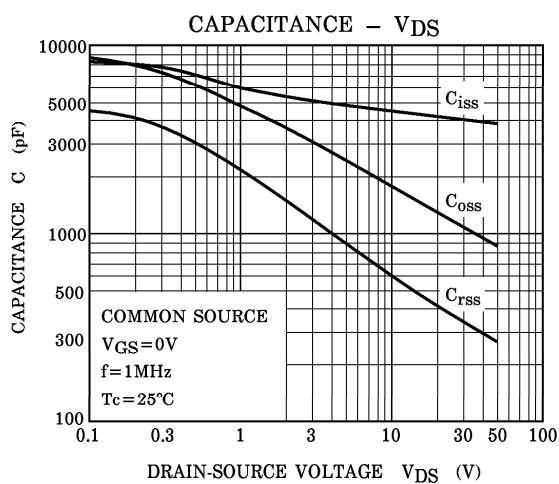
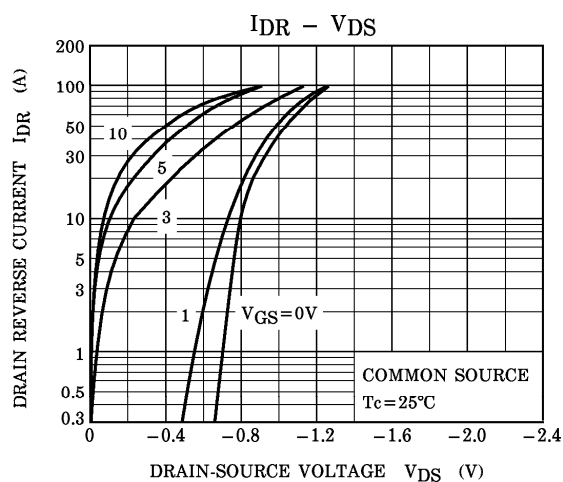
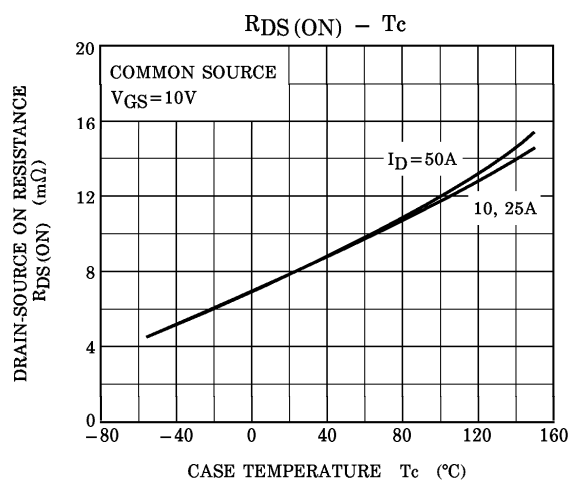


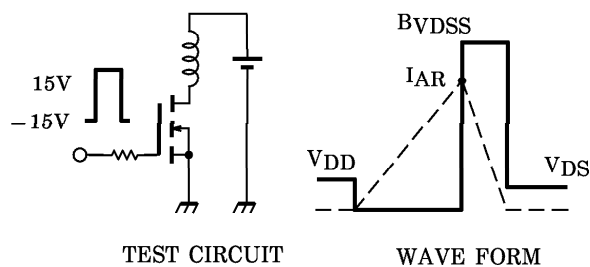
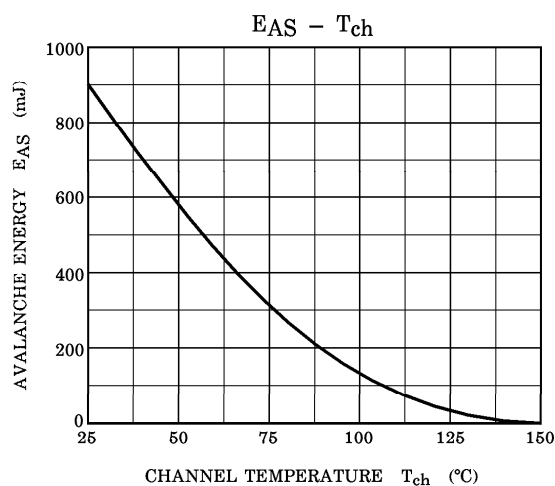
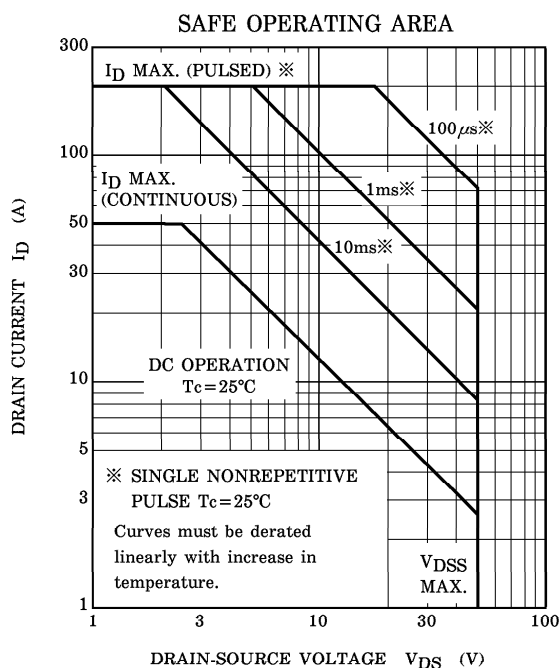
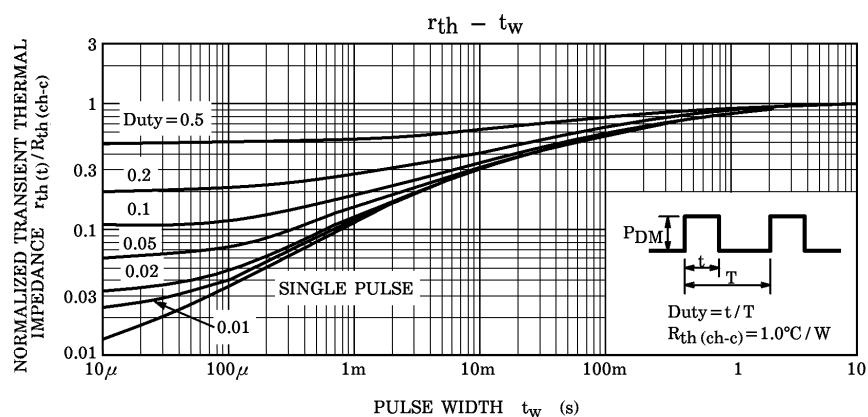
Month (Starting from Alphabet A)



Year (Last Number of the Christian Era)







Peak $I_{AR} = 50A$, $R_G = 25\Omega$
 $V_{DD} = 25V$, $L = 440\mu H$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$