# MOS FIELD EFFECT TRANSISTOR **2SK3811**

## SWITCHING N-CHANNEL POWER MOS FET

## DESCRIPTION

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The 2SK3811 is N-channel MOS Field Effect Transistor designed for high current switching applications.

## FEATURES

- Super low on-state resistance
- $R_{DS(on)}$  = 1.8 m $\Omega$  MAX. (V<sub>GS</sub> = 10 V, I<sub>D</sub> = 55 A)
- High Current Rating: ID(DC) = ±110 A

## ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGs = 0 V)	VDSS	40	V
Gate to Source Voltage (VDs = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±110	Α
Drain Current (pulse) Note1	D(pulse)	±440	А
Total Power Dissipation (Tc = $25^{\circ}$ C)	<b>P</b> T1	213	W
Total Power Dissipation (T <sub>A</sub> = $25^{\circ}$ C)	P <sub>T2</sub>	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Energy Note2	Eas	518	mJ
Repetitive Avalanche Current Note3	lar	72	Α
Repetitive Avalanche Energy Note3	Ear	518	mJ

#### **Notes 1.** PW $\leq$ 10 $\mu$ s, Duty Cycle $\leq$ 1%

- 2. Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 20 V, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20  $\rightarrow$  0 V, L = 100  $\mu$ H
- **3.** RG = 25  $\Omega$ , Tch(peak)  $\leq$  150°C

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## ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3811-ZP	TO-263 (MP-25ZP)



(TO-263)

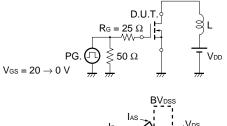
ELECTRICAL CHARACTERISTICS (TA = 25°C)

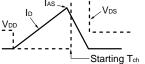
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V			10	μA
Gate Leakage Current	lgss	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±100	nA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 55 A	45	89		S
Drain to Source On-state Resistance Note	RDS(on)	Vgs = 10 V, Id = 55 A		1.4	1.8	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		17700		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		2200		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		1300		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 20 V, I <sub>D</sub> = 55 A		54		ns
Rise Time	tr	V <sub>GS</sub> = 10 V		140		ns
Turn-off Delay Time	td(off)	Rg = 0 Ω		130		ns
Fall Time	tr			21		ns
Total Gate Charge	QG	VDD = 32 V		260		nC
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10 V		57		nC
Gate to Drain Charge	Qgd	I <sub>D</sub> = 110 A		83		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 110 A, VGS = 0 V		0.87	1.5	V
Reverse Recovery Time	trr	I⊧ = 110 A, V₀s = 0 V		60		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		80		nC

Note Pulsed

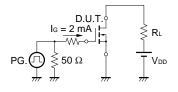
#### TEST CIRCUIT 1 AVALANCHE CAPABILITY

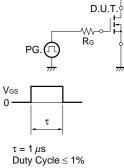
#### **TEST CIRCUIT 2 SWITCHING TIME**

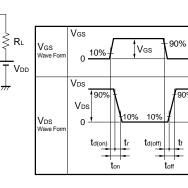




#### **TEST CIRCUIT 3 GATE CHARGE**



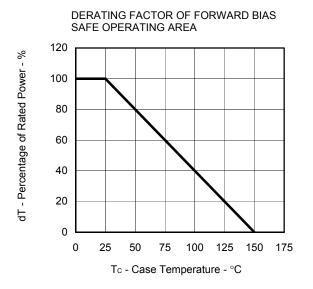


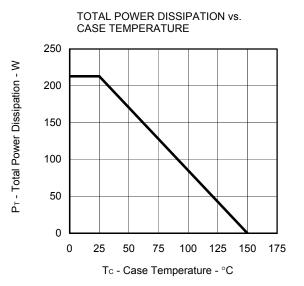


90%

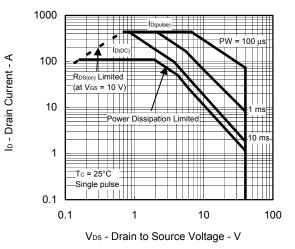
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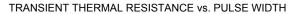
## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

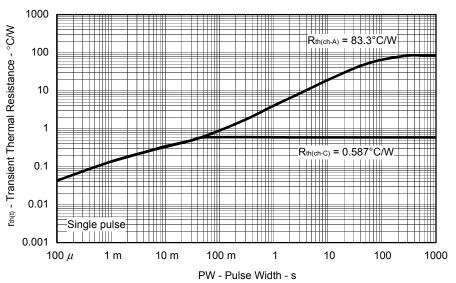


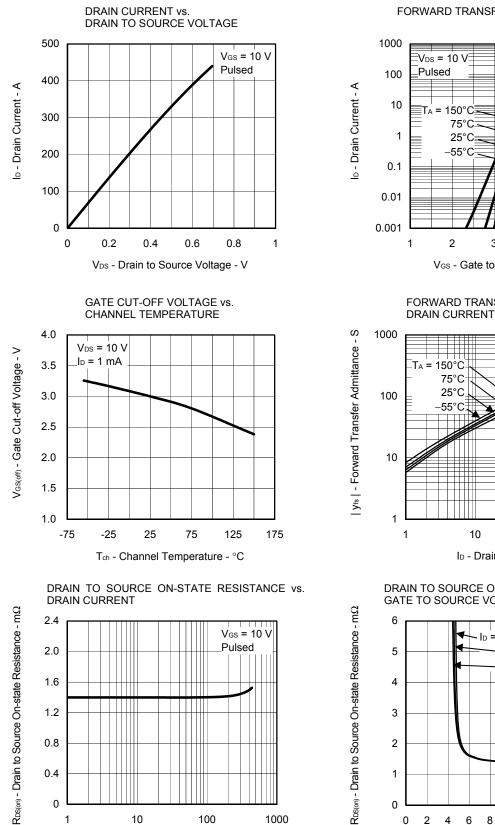


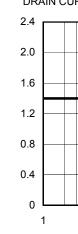
FORWARD BIAS SAFE OPERATING AREA







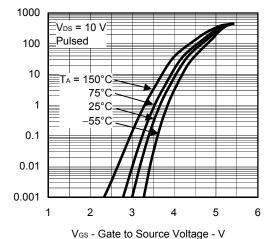




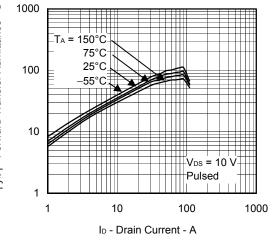
V<sub>GS</sub> = 10 V Pulsed 1000 10 100

ID - Drain Current - A

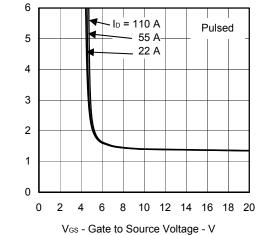
FORWARD TRANSFER CHARACTERISTICS

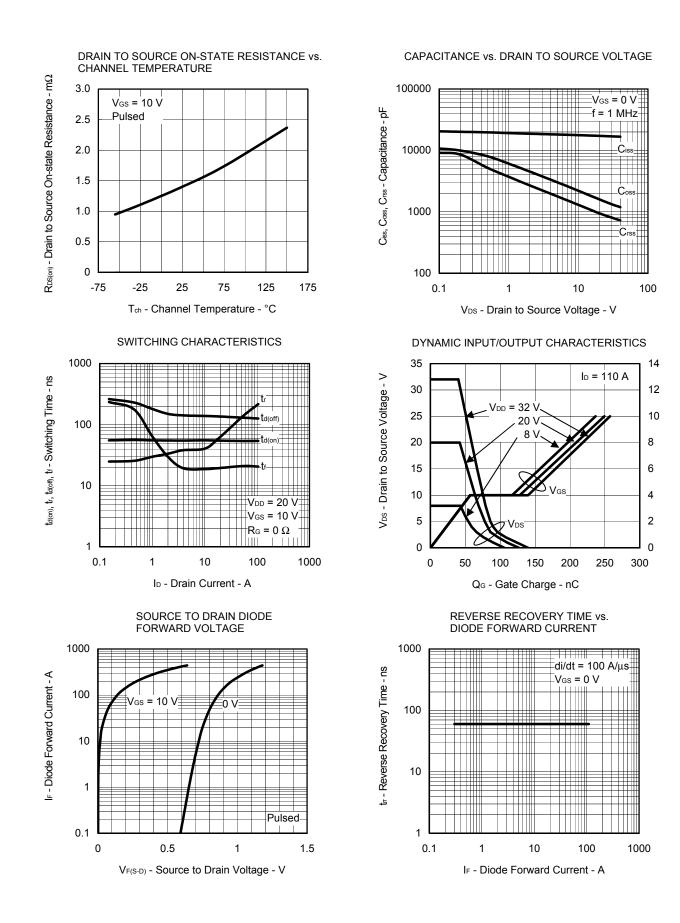


FORWARD TRANSFER ADMITTANCE vs.

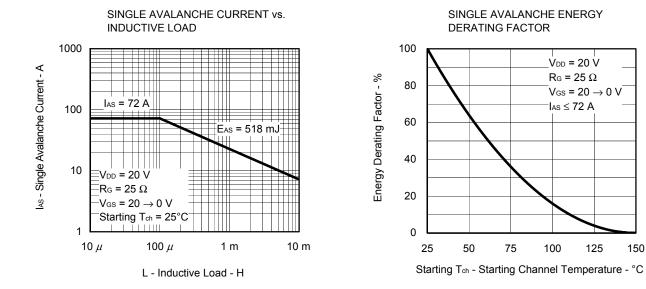


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE





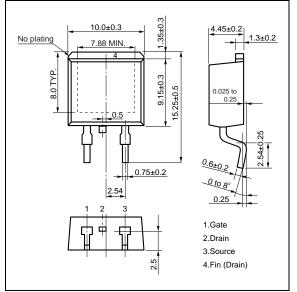
V<sub>GS</sub> - Gate to Source Voltage - V



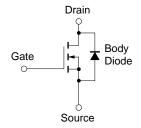
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## PACKAGE DRAWING (Unit: mm)

## TO-263 (MP-25ZP)



#### EQUIVALENT CIRCUIT



**Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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