

Silicon NPN Power Transistors

2SC792

DESCRIPTION

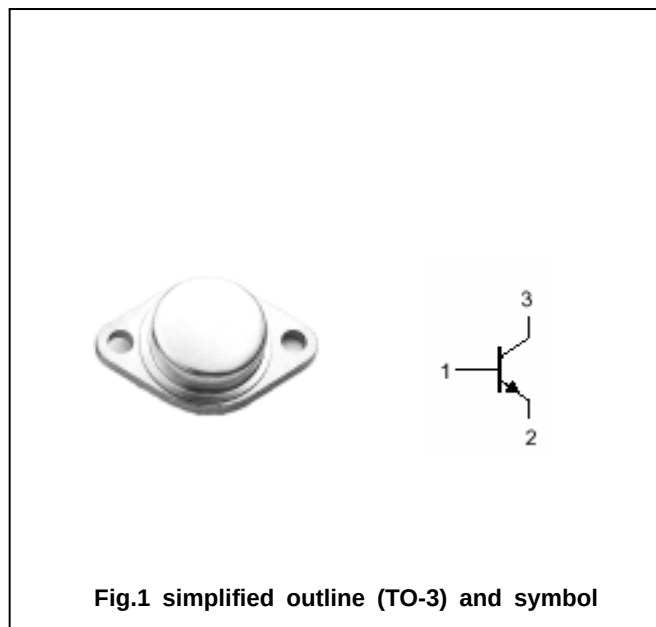
- With TO-3 package
- High breakdown voltage

APPLICATIONS

- For voltage regulator, inverter, switching mode power supply applications

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

Absolute maximum ratings($T_a = ^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	400	V
V_{CEO}	Collector-emitter voltage	Open base	300	V
V_{EBO}	Emitter-base voltage	Open collector	5	V
I_C	Collector current		1.5	A
P_C	Collector power dissipation	$T_C = 25^\circ\text{C}$	50	W
T_j	Junction temperature		150	$^\circ\text{C}$
T_{stg}	Storage temperature		-55~150	$^\circ\text{C}$

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CHARACTERISTICS

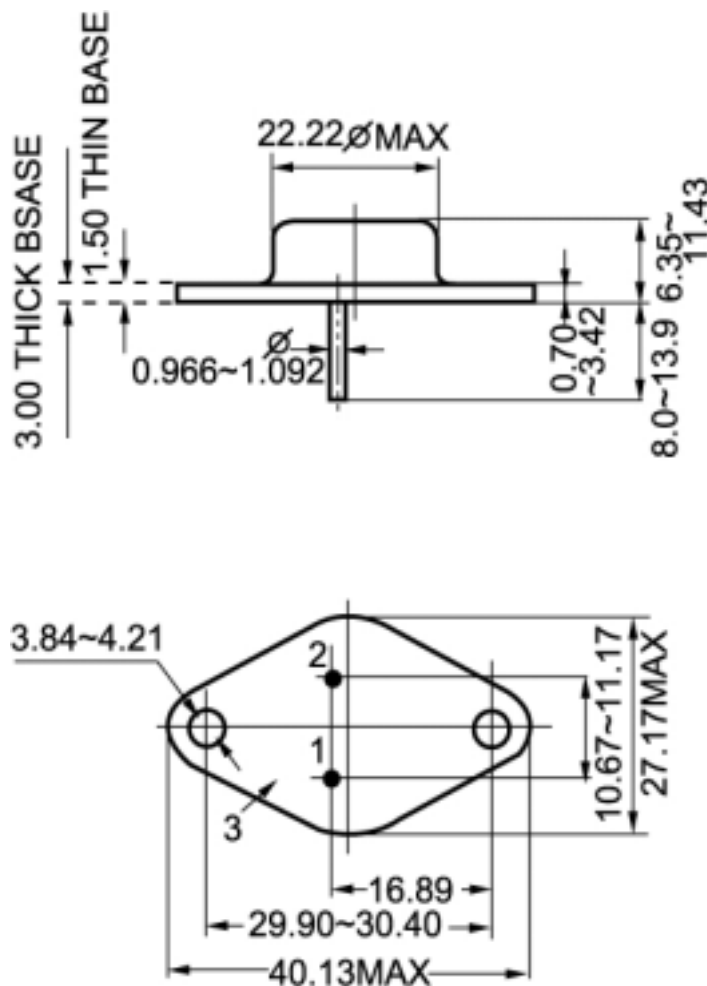
T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =10mA ; I _B =0	300			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA ; I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =1.5A; I _B =0.3A			5.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =1.5A; I _B =0.3A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =300V; I _E =0			50	μA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			50	μA
h _{FE}	DC current gain	I _C =0.3A ; V _{CE} =10V	30		200	
f _T	Transition frequency	I _C =0.1A ; V _{CE} =10V		10		MHz

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PACKAGE OUTLINE

Fig.2 outline dimensions (unindicated tolerance: $\pm 0.1\text{mm}$)