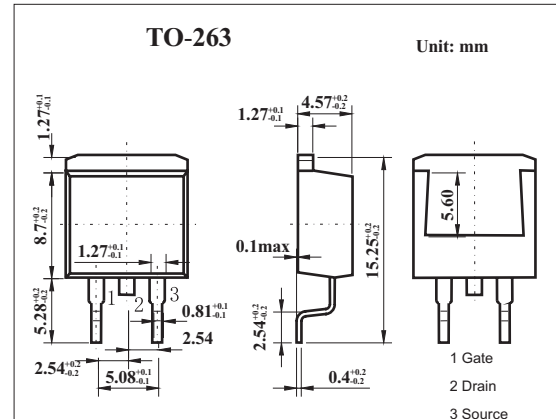


MOS Field Effect Transistor

2SK3575

Features

- 4.5V drive available.
- Low on-state resistance,
 $R_{DS(on)1} = 4.5\text{m}\Omega$ MAX. ($V_{GS} = 10\text{V}$, $I_D = 42\text{A}$)
- Low gate charge
 $Q_G = 70\text{nC}$ TYP. ($V_{DD} = 24\text{V}$, $V_{GS} = 10\text{V}$, $I_D = 83\text{A}$)
- Built-in gate protection diode
- Surface mount device available



Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain to source voltage	V_{DS}	30	V
Gate to source voltage	V_{GS}	± 20	V
Drain current	I_D	± 83	A
	I_{DP}^*	± 332	A
Power dissipation	P_D	105	W
		1.5	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

* $PW \leq 10\text{ }\mu\text{s}$, Duty Cycle $\leq 1\%$

Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Drain cut-off current	I_{DSS}	$V_{DS}=30\text{V}, V_{GS}=0$			10	μA
Gate leakage current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS}=0$			± 100	μA
Gate cut off voltage	$V_{GS(off)}$	$V_{DS}=10\text{V}, I_D=1\text{mA}$	1.5		2.5	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS}=10\text{V}, I_D=42\text{A}$	27			S
Drain to source on-state resistance	$R_{DS(on)1}$	$V_{GS}=10\text{V}, I_D=42\text{A}$		3.3	4.5	$\text{m}\Omega$
	$R_{DS(on)2}$	$V_{GS}=4.5\text{V}, I_D=42\text{A}$		4.3	6.4	$\text{m}\Omega$
Input capacitance	C_{iss}	$V_{DS}=10\text{V}, V_{GS}=0, f=1\text{MHz}$		3700		pF
Output capacitance	C_{oss}			1430		pF
Reverse transfer capacitance	C_{rss}			500		pF
Turn-on delay time	t_{on}	$I_D=42\text{A}, V_{GS(on)}=10\text{V}, R_G=10\Omega, V_{DD}=15\text{V}$		26		ns
Rise time	t_r			27		ns
Turn-off delay time	t_{off}			110		ns
Fall time	t_f			40		ns
Total Gate Charge	Q_G	$V_{DD} = 24\text{V}$ $V_{GS} = 10\text{V}$ $I_D = 83\text{A}$		70		nC
Gate to Source Charge	Q_{GS}			12		nC
Gate to Drain Charge	Q_{GD}			20		nC