National Semiconductor

100415 1024 x 1-Bit Random Access Memory

General Description

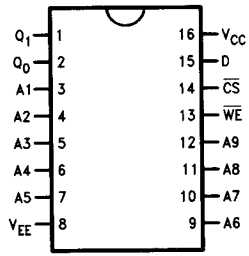
The 100415 is a 1024-bit read/write Random Access Memory (RAM), organized as 1024 words by one bit per word and designed for high-speed scratchpad, control and buffer storage applications. The device includes full on-chip address decoding, separate Data Input and non-inverting Data Output lines, as well as an active-LOW Chip Select line.

Features

- Address access time—10 ns max
- Chip select access time—5.0 ns max
- Open-emitter output for easy memory expansion
- Power dissipation—0.79 mW/bit typ
- Power dissipation decreases with increasing temperature
- Polyamide die coat for alpha immunity

Connection Diagrams

16-Pin Ceramic Flatpak

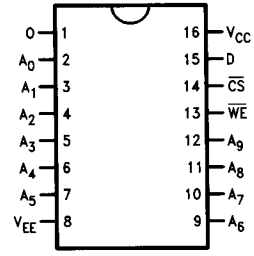


TL/D/9639–11

Top View

Order Number 100415FC10
See NS Package Number W16A*

16-Pin Ceramic Dual-In-Line Package



TL/D/9639–2

Top View

Order Number 100415DC10
See NS Package Number J16A*

*For most current package information, contact product marketing.

Optional Processing QR = Burn-In

Pin Names

$\overline{WE}$	Write Enable Input (Active LOW)
$\overline{CS}$	Chip Select Input (Active LOW)
A_0–A_9	Address Inputs
D	Data Input
O	Data Output