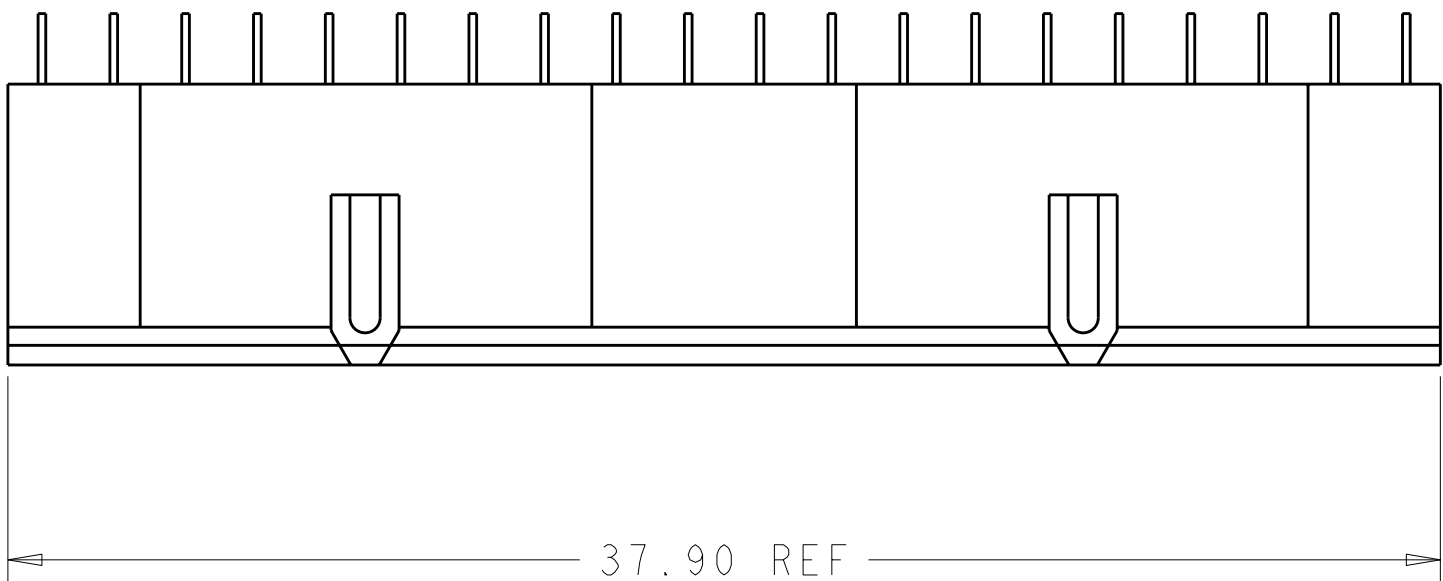


REVISIONS				
LOC	DIST	P	LYR	DESCRIPTION
-	-	-	-	DATE
-	-	-	-	DWN
-	-	-	-	APVD
1				PROPOSED
2				NAME CHANGE, APP. AND PROD. SPEC ADDED
3				ADDED PLATING DESCRIPTION
4				ADDED WEIGHT TO TITLE BLOCK

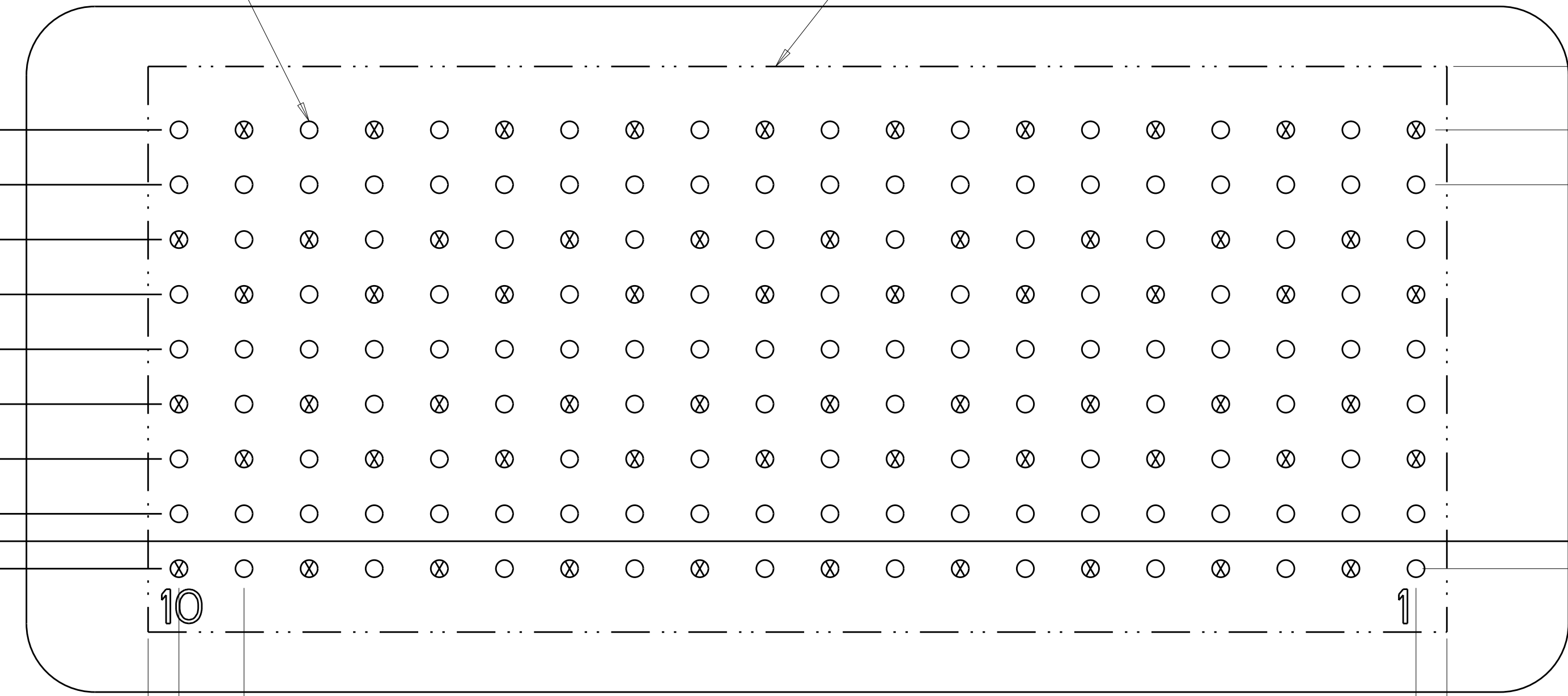
$\oplus \varnothing 0.05 \textcircled{S} \begin{matrix} X \\ Y \end{matrix}$

FINISHED HOLE SIZE
AND PLATED THRU HOLES

CONNECTOR
OUTLINE



ROW I
ROW H
ROW G
ROW F
ROW E
ROW D
ROW C
ROW B
ROW A



- = SIGNAL
- ⊗ = GROUND

RECOMMENDED PC BOARD LAYOUT
COMPONENT SIDE SHOWN
SCALE 8:1

1 MATERIAL:
CONTACT - COPPER ALLOY
HOUSING - LCP, COLOR - BLACK

2 FINISH:
MATING AREA: 1.27μm MIN GOLD OVER
1.25μm MIN NICKEL

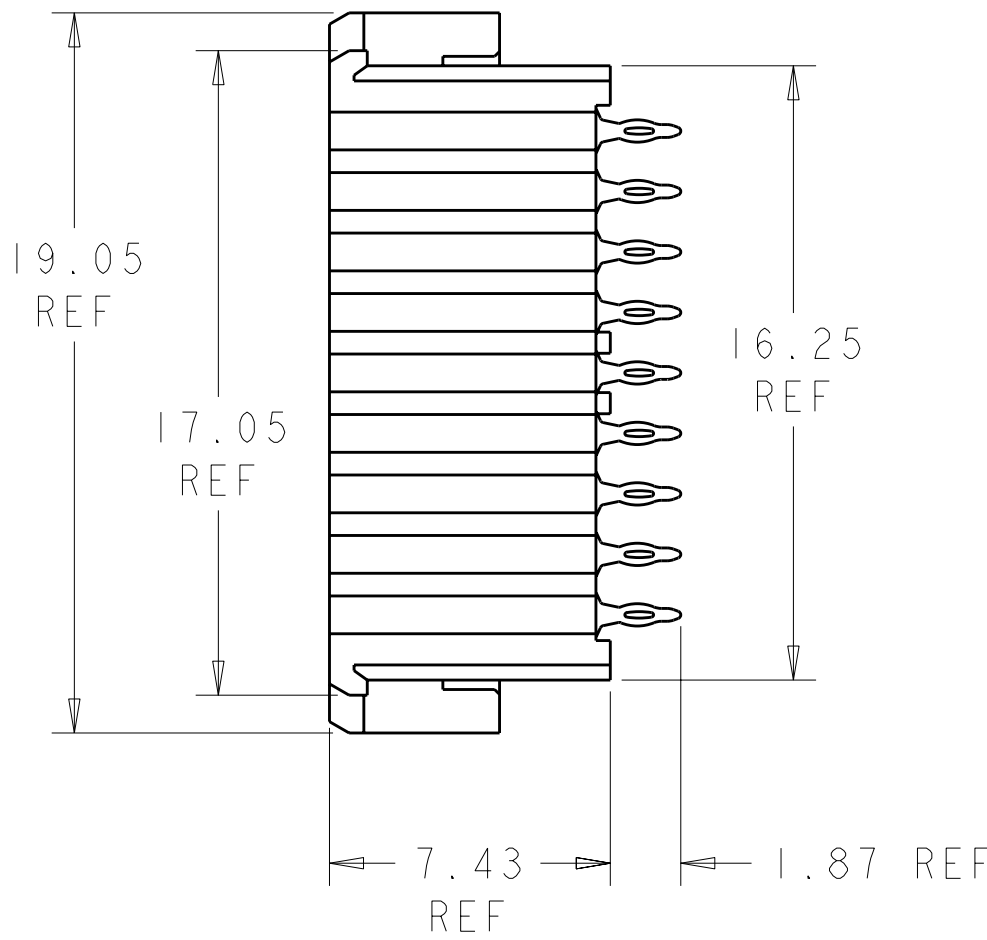
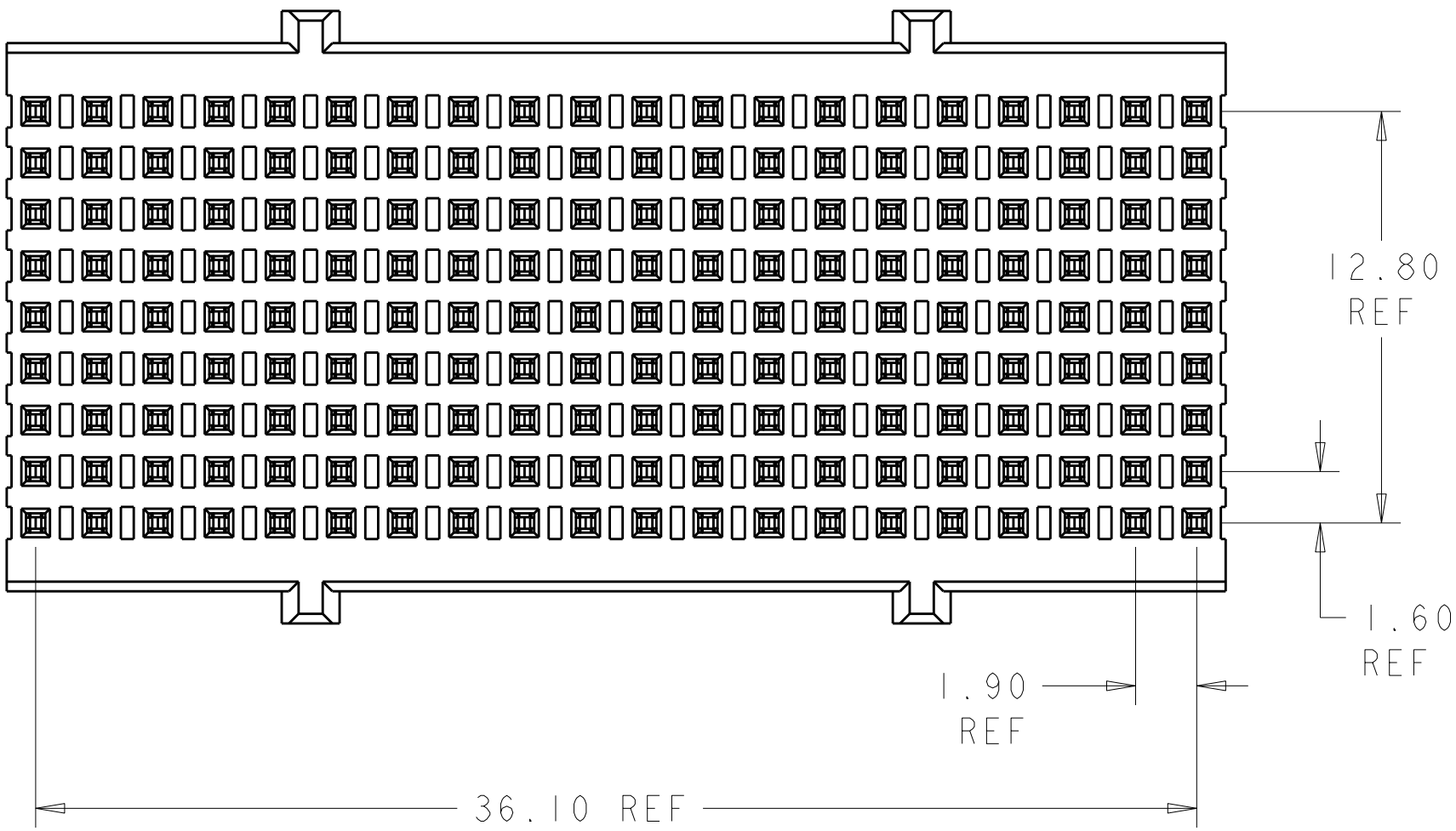
3 FINISH:
COMPLIANT PIN AREA: 0.38μm MIN BRIGHT TIN LEAD
OVER 1.27μm MIN NICKEL

4 FINISH:
COMPLIANT PIN AREA: 0.38μm MIN MATTE TIN
OVER 1.27μm MIN NICKEL

5 FINISH:
COMPLIANT PIN AREA: 0.38μm MIN GOLD
OVER 1.27μm MIN NICKEL

6 DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER.

7 REFERENCE APPLICATION SPEC 114-13267 FOR RECOMMENDED
PLATING THRU HOLE DIAMETER AND PLATING THICKNESS.



THIS PRODUCT HAS NOT COMPLETED VALIDATION TESTING

THIS PRINT IS
PRELIMINARY
UNQUALIFIED PRODUCT
CONTACT PRODUCT ENGINEERING
BEFORE USING THIS PRINT

DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED:		DWN D. BELACK 06JUL2009		Tyco Electronics Harrisburg, PA 17105-3608	
		9 PLC ±.13 1 PLC ±0.13 2 PLC ±0.05 3 PLC ±. 4 PLC ±. ANGLES ±2°		CHK A. TSANG		NAME Fortis Zd, 20-COLUMN, MINIBOX, VERTICAL, CENTER MODULE ASSEMBLY	
MATERIAL 		FINISH -		APVD M. McALONIS		PRODUCT SPEC 108-2409	
				APPLICATION SPEC 114-13267		SIZE A100779	
				WEIGHT ~6.85 g		DRAWING NO C=2000905	
				CUSTOMER DRAWING		RESTRICTED TO -	
						SCALE 5:1 SHEET 1 OF 1 REV 4	