

## 200 series — CMOS

|                                                                       |                                                                                |
|-----------------------------------------------------------------------|--------------------------------------------------------------------------------|
| FREQUENCY RANGE                                                       | 250 Hz TO 10.0 MHz                                                             |
| FREQUENCY ACCURACY @ 25°C                                             | ±0.0015%                                                                       |
| FREQUENCY STABILITY<br>VS. TEMPERATURE<br>OPERATING TEMPERATURE RANGE | SEE TABLE<br>BELOW                                                             |
| STORAGE TEMPERATURE                                                   | -55°C TO 125°C                                                                 |
| INPUT VOLTAGE $V_{DD}$                                                | +5 VDC TO +15 VDC (SEE NOTE BELOW)                                             |
| INPUT CURRENT                                                         | 5 mA MAX. @ 5 VDC<br>25 mA MAX. @ 15 VDC                                       |
| SYMMETRY                                                              | 60-40%                                                                         |
| RISE & FALL TIMES                                                     | 150 nS MAX. @ +5 VDC<br>50 nS MAX. @ +15 VDC } $R_L$ 200 K Ohms<br>$C_L$ 50 PF |
| "0" LEVEL                                                             | $V_{SS}+0.2$ VDC                                                               |
| "1" LEVEL                                                             | $V_{DD}-0.2$ VDC                                                               |
| AGING @ 25°C                                                          | 5 PPM/YR. MAX.                                                                 |
| FREQUENCY STABILITY VS. INPUT VOLTAGE                                 | ±5 PPM MAX. FOR 10% CHANGE<br>IN INPUT VOLTAGE                                 |

NOTE: Input voltage must be specified for C-MOS parts, minimum input voltage required depends upon output frequency and operating temperature range. Consult factory for your specific application.

## STABILITIES AVAILABLE

## TEMPERATURE RANGE

| P/N |                                              |
|-----|----------------------------------------------|
| 1   | ±.1%                                         |
| 2   | ±.05%                                        |
| 3   | ±.01%                                        |
| 4   | ±.005%                                       |
| 5   | ±.002% NOT AVAILABLE WITH<br>-55°C TO +125°C |

| P/N |                 |
|-----|-----------------|
| 1   | 0°C TO +70°C    |
| 2   | -30°C TO +85°C  |
| 3   | -55°C TO +125°C |

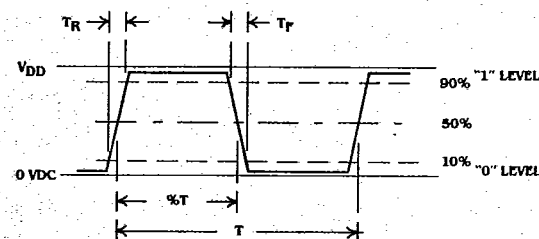
14 PIN PACKAGE  
(2 FOR  
TTL COMPATIBLE C-MOS)

±.002%

-30°C TO +85°C

SPECIFIED FREQUENCY

5 2 @ 20.0MHZ



C-MOS WAVEFORM

## IONS AVAILABLE

## 4 PIN PACKAGE

## OPTION 'M'

## OPTION 'T'

| PIN #     | FUNCTION |
|-----------|----------|
| 1         | OUTPUT   |
| 2         | CASE     |
| 8         | GND.     |
| 14        | B+       |
| 3 THRU 7  | NC       |
| 9 THRU 13 | NC       |

| PIN #     | FUNCTION |
|-----------|----------|
| 1         | N/C      |
| 7         | GND.     |
| 8         | OUTPUT   |
| 14        | B+       |
| 2 THRU 6  | MISSING  |
| 9 THRU 13 | MISSING  |

SAME AS M55310/8/11/15



ELECTRONICS

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