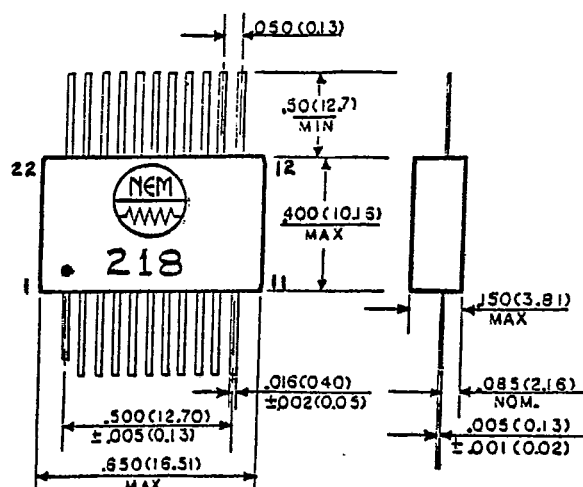


REV

CHANGE

APPR

DATE



| PIN | CONNECTION     | PIN | CONNECTION |
|-----|----------------|-----|------------|
| 1   | A OUTPUT       | 12  | A -50mA    |
| 2   | A +50mA        | 13  | NC         |
| 3   | +5VDC          | 14  | NC         |
| 4   | A INPUT        | 15  | NC         |
| 5   | A MODE CONTROL | 16  | NC         |
| 6   | GND            | 17  | -5VDC      |
| 7   | B MODE CONTROL | 18  | NC         |
| 8   | B INPUT        | 19  | NC         |
| 9   | NC             | 20  | NC         |
| 10  | B +50mA        | 21  | NC         |
| 11  | B OUTPUT       | 22  | B -50mA    |

7. MAY BE USED TO DRIVE TWO SPST SWITCHES, OR ONE SPDT SWITCH.

8. TO DRIVE SPDT SWITCH, MODE CONTROL PINS ARE USED FOR OPPOSITE LOGIC FROM SAME TTL INPUT :

| CHANNEL B         | LOGIC      | CHANNEL A         |
|-------------------|------------|-------------------|
| PINS 7-3 LOW OUT  | HIGH INPUT | LOW PINS 5-3 OUT  |
| PINS 7-6 HIGH OUT |            | HIGH PINS 5-6 OUT |
| PINS 7-3 HIGH OUT | LOW INPUT  | HIGH PINS 5-3 OUT |
| PINS 7-6 LOW OUT  |            | LOW PINS 5-6 OUT  |

## SPECIFICATIONS

1. PROVIDES TWO INDEPENDENT DRIVERS, A AND B.
2. MODE CONTROLS FOR EACH CHANNEL, TO PROGRAM INVERTING OR NON-INVERTING OPERATION.

3. LOGIC : TTL

LOW :  $0 \pm .7V$ .

HIGH :  $4 \pm 1.5V$ .

4. TRUE TTL COMPATIBILITY

5. SUPPLY VOLTAGES :

+5 VDC  $\pm 5\%$

-5 VDC  $\pm 5\%$

6. OUTPUT CURRENT 30 mA nom.

MAY BE INCREASED TO 50 mA.  
WITH PIN CONNECTIONS AS FOLLOWS :

### CHANNEL A

PINS 1 AND 2 (+50 mA)

PINS 12 AND 17 (-50 mA)

### CHANNEL B

PINS 10 AND 11 (+50 mA)

PINS 22 AND 17 (-50 mA)

7. SWITCHING SPEED :

60 nanosec. TYP.

80 nanosec. MAX.



## NEW ENGLAND MICROWAVE CORPORATION

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DRAWN BY *Phh*

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TOLERANCE UNLESS OTHERWISE SPECIFIED  
.XX  $\pm .01$   
.XXX  $\pm .005$

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## MODEL 218 DUAL DRIVER

INVERTING / NON-INVERTING

A