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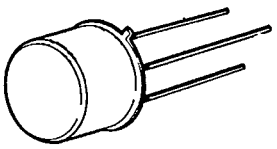
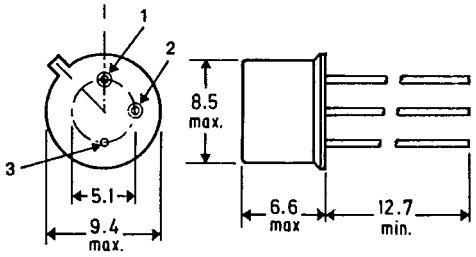


2N 6781
2N 6782

MECHANICAL DATA

Dimensions in mm

MOS POWER
N-Channel Enhancement Mode



APPLICATIONS

- FAST SWITCHING
- MOTOR CONTROLS
- POWER SUPPLIES

PIN 1 – Source PIN 2 – Gate PIN 3 Drain and Case

TO 39

ABSOLUTE MAXIMUM RATINGS (T_{CASE} = 25°C unless otherwise specified)

	Parameter	2N 6781	2N 6782
V_{DS}	Drain source voltage	60V	100V
V_{DGR}	Drain gate voltage (R_{GS} = 1 MΩ)	60V	100V
I_D @ T_C = 25°C	Continuous drain current	±3·5A	
I_D @ T_C = 100°C	Continuous drain current	±2·25A	
I_{DM}	Pulsed drain current (i)	±8A	
V_{GS}	Gate-source voltage	±40V	
P_D @ T_C = 25°C	Maximum power dissipation	15W	
P_D @ T_C = 100°C	Maximum power dissipation	6W	
Junction to case	Linear derating factor	0·12 W/°C	
Junction to ambient	Linear derating factor	0·005 W/°C	
T_J	Operating and	−55 to 150°C	
T_{stg}	storage temperature range		
Lead temperature	(1/16" from case for 10 secs.)	300°C	

(i) Pulse test: Pulse width ≤300μsec, duty cycle ≤2%

2N 6781 2N 6782

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ELECTRICAL CHARACTERISTICS (T_{CASE} = 25°C unless otherwise specified)

STATIC

Parameter		Type	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS}	Drain-Source Breakdown Voltage	2N6781	60*			V	V_{GS} = 0 I_D = 0.25 mA
		2N6782	100*			V	
$V_{GS(th)}$	Gate-Threshold Voltage	All	2* 1*		4.0*	V	$V_{DS} = V_{GS}$, I_D = 0.5 mA $V_{DS} = V_{GS}$, I_D = 0.5mA@T_A = 125°C
I_{GSSF}	Gate-Body Leakage Forward	All			100* 200*	nA	V_{GS} = 20V V_{GS} = 20V @ T_A = 125°C
I_{GSSR}	Gate-Body Leakage Reverse	All			−100*	nA	V_{GS} = −20V
I_{DSS}	Zero Gate Voltage Drain Current	All			0.25*	mA	V_{DS} = 0.8 Max. Rating, V_{GS} = 0
		All			1*	mA	V_{DS} = Max. Rating, V_{GS} = 0 T_C = 125°C
$I_{D(on)}$	On-State Drain Current[1]	2N6781	3.5			A	$V_{DS} > 2V_{DS(ON)}$, V_{GS} = 10V
		2N6782	3.5			A	$V_{DS} > 2V_{DS(ON)}$, V_{GS} = 10V
$V_{DS(on)}$	Static Drain-Source On-State Voltage[1]	2N6781			2.1*	V	V_{GS} = 10V , I_D = 3.5A
		2N6782			2.1*	V	V_{GS} = 10V , I_D = 3.5A
$R_{DS(on)}$	Static Drain-Source On-State Resistance[1]	2N6781			0.6*	Ω	V_{GS} = 10V , I_D = 2.25A
		2N6782			0.6*	Ω	V_{GS} = 10V , I_D = 2.25A
$R_{DS(on)}$	Static Drain-Source On-State Resistance[1]	2N6781			1.08*	Ω	V_{GS} =10V, I_D =2.25A T_C =125°C
		2N6782			1.08*	Ω	V_{GS} =10V, I_D =2.25A T_C = 125°C

DYNAMIC

g_{fs}	Forward Transductance[1]	All	1.0*		3.0*	S (℧)	$V_{DS} \geq 2\ V_{DS(ON)}$, I_D = 2.25A
C_{iss}	Input Capacitance	All	60*		200*	pF	V_{GS} = 0 , V_{DS} = 25V f = 1 MHz
C_{oss}	Output Capacitance	All	40*		100*	pF	
C_{rss}	Reverse Transfer Capacitance	All	10*		25*	pF	
$t_{d(on)}$	Turn-On Delay Time	All			15*	ns	V_{DD} =34V , $I_D \cong$ 2.25A R_g = 25Ω , R_L = 15Ω (MOS FET switching times are essentially independent of operating temperature)
t_r	Rise Time	All			25*	ns	
$t_{d(off)}$	Turn-Off Delay Time	All			25*	ns	
t_f	Fall Time	All			20*	ns	

THERMAL RESISTANCE

R_{thJC}	Junction-to-Case	All			8.33*	°C/W	
R_{thJA}	Junction-to-Ambient	All			170	°C/W	Free Air Operation

BODY-DRAIN DIODE RATINGS AND CHARACTERISTICS

I_S	Continuous Source Current (Body Diode)	2N6781			−3.5*	A	Modified MOS POWER symbol showing the intergal P-N junction rectifer.
		2N6782			−3.5*	A	
I_{SM}	Source Current[1] (Body Diode)	2N6781			−8	A	
		2N6782			−8	A	
V_{SD}	Diode Forward Voltage[1]	2N6781	−0.75*		−1.5*	V	T_C=25°C, I_S=−3.5A, V_{GS} = 0
		2N6782	−0.75*		−1.5*	V	T_C=25°C, I_S=−3.5A, V_{GS} = 0
t_{rr}	Reverse Recovery Time	All		200		ns	T_J = 150°C, $I_F = I_S$, dI_F/dt = 100 A/μs

1 Pulse Test: Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%
*JEDEC Registered Values

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