2SC3795, 2SC3795A

Silicon NPN triple diffusion planar type

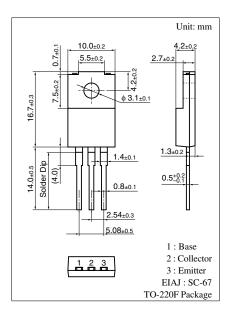
For high breakdown voltage high-speed switching

■ Features

- High-speed switching
- \bullet High collector to base voltage V_{CBO}
- \bullet Low collector to emitter saturation voltage $V_{\text{CE}(\text{sat})}$
- Full-pack package which can be installed to the heat sink with one screw

■ Absolute Maximum Ratings $T_C = 25$ °C

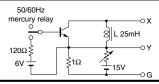
Parameter		Symbol	Rating	Unit	
Collector to base	2SC3795	V_{CBO}	800	V	
voltage	2SC3795A		900		
Collector to	2SC3795	V _{CES}	800	V	
emitter voltage	2SC3795A		900		
Collector to emitter voltage		V _{CEO}	500	V	
Emitter to base voltage		V_{EBO}	8	V	
Peak collector current		I_{CP}	10	A	
Collector current		I_{C}	5	A	
Base current		I_B	3	A	
Collector power	$T_C = 25^{\circ}C$	P_{C}	40	W	
dissipation	$T_a = 25^{\circ}C$		2		
Junction temperature		T _j	150	°C	
Storage temperature		T_{stg}	-55 to +150	°C	

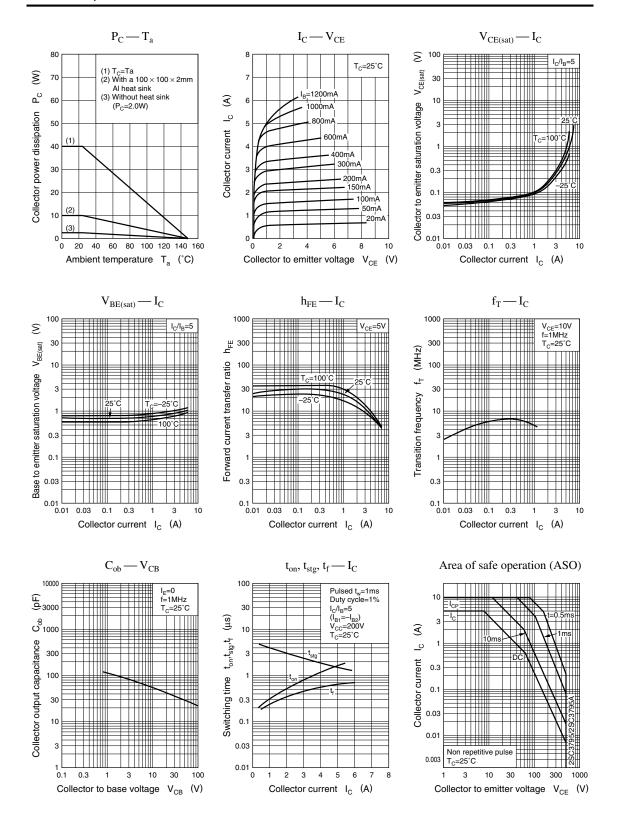


■ Electrical Characteristics $T_C = 25$ °C

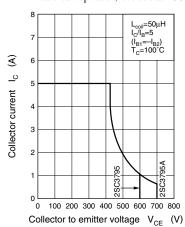
Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Collector cutoff	2SC3795	I_{CBO}	$V_{CB} = 800 \text{ V}, I_{E} = 0$			100	μΑ
current	2SC3795A		$V_{CB} = 900 \text{ V}, I_{E} = 0$			100	
Emitter cutoff current		I_{EBO}	$V_{EB} = 5 \text{ V}, I_{C} = 0$			100	μΑ
Collector to emitter voltage *		V _{CEO(sus)}	I _C = 0.2 A, L = 25 mH	500			V
Forward current transfer ratio		h _{FE1}	$V_{CE} = 5 \text{ V}, I_{C} = 0.1 \text{ A}$	15			
		h _{FE2}	$V_{CE} = 5 \text{ V}, I_{C} = 3 \text{ A}$	8			
Collector to emitter saturation voltage		V _{CE(sat)}	$I_C = 3 \text{ A}, I_B = 0.6 \text{ A}$			1	V
Base to emitter saturation voltage		V _{BE(sat)}	$I_C = 3 \text{ A}, I_B = 0.6 \text{ A}$			1.5	V
Transition frequency		f_T	$V_{CE} = 10 \text{ V}, I_{C} = 0.5 \text{ A}, f = 1 \text{ MHz}$		8		MHz
Turn-on time	2SC3795	t _{on}	$I_C = 3 \text{ A}, I_{B1} = 0.6 \text{ A}, I_{B2} = -0.6 \text{ A},$			1	μs
	2SC3795A		$V_{CC} = 200 \text{ V}$			1.2	
Storage time		t _{stg}				3	μs
Fall time	2SC3795	$t_{\rm f}$				1	μs
	2SC3795A					1.2	



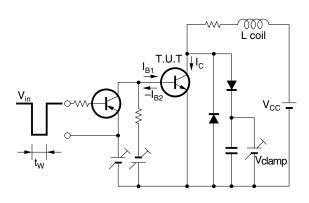


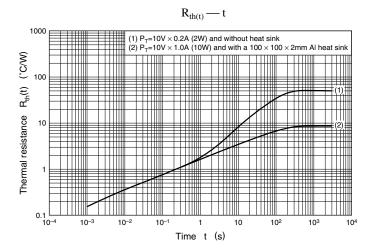


Area of safe operation, reverse bias ASO



Reverse bias ASO measuring circuit





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