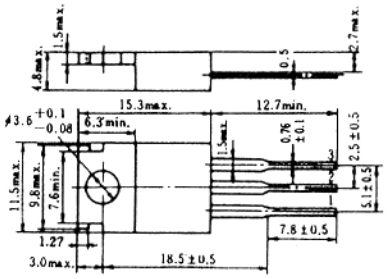
HITACHI

2SD1248Ⓚ

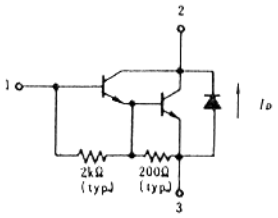
SILICON NPN TRIPLE DIFFUSED

LOW FREQUENCY POWER AMPLIFIER



1. Base
2. Collector (Flange)
3. Emitter

(Dimensions in mm)



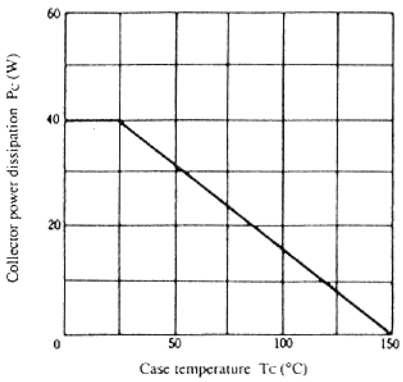
(JEDEC TO-220AB)

■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Item	Symbol	2SD1248Ⓚ	Unit
Collector to base voltage	V_{CBO}	120	V
Collector to emitter voltage	V_{CEO}	120	V
Emitter to base voltage	V_{EBO}	7	V
Collector current	I_C	8	A
Collector peak current	$i_{C(peak)}$	12	A
C to E diode forward current	I_D*	8	A
Collector power dissipation	P_C*	40	W
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	−55 to +150	°C

* Value at T_C = 25°C.

MAXIMUM COLLECTOR DISSIPATION CURVE



■ ELECTRICAL CHARACTERISTICS (Ta=25°C)

Item	Symbol	Test Condition	min.	typ.	max.	Unit
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	I_C = 25mA, R_{BE} = ∞	120	—	—	V
Emitter to base breakdown voltage	$V_{(BR)EBO}$	I_E = 100mA, I_C = 0	7	—	—	V
Collector cutoff current	I_{CBO}	V_{CB} = 120V, I_E = 0	—	—	100	μA
	I_{CEO}	V_{CE} = 100V, R_{BE} = ∞	—	—	10	μA
DC current transfer ratio	h_{FE}	V_{CE} = 3V, I_C = 4A*	1000	—	20000	
Collector to emitter saturation voltage 1	$V_{CE(sat)1}$	I_C = 4A, I_B = 8mA*	—	—	1.5	V
Base to emitter saturation voltage 1	$V_{BE(sat)1}$	I_C = 4A, I_B = 8mA*	—	—	2.0	V
Collector to emitter saturation voltage 2	$V_{CE(sat)2}$	I_C = 8A, I_B = 80mA*	—	—	3.0	V
Base to emitter saturation voltage 2	$V_{BE(sat)2}$	I_C = 8A, I_B = 80mA*	—	—	3.5	V
C to E diode forward voltage	V_D	I_D = 8A*	—	—	3.0	V

* Pulse Test.

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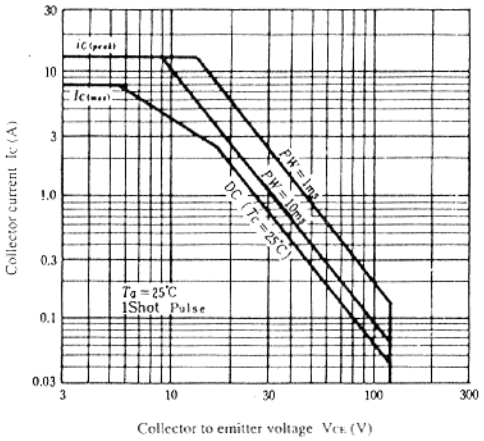
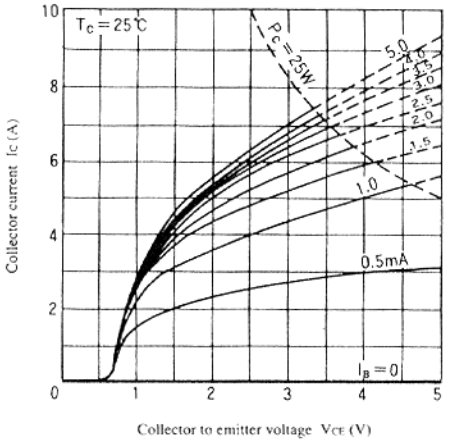
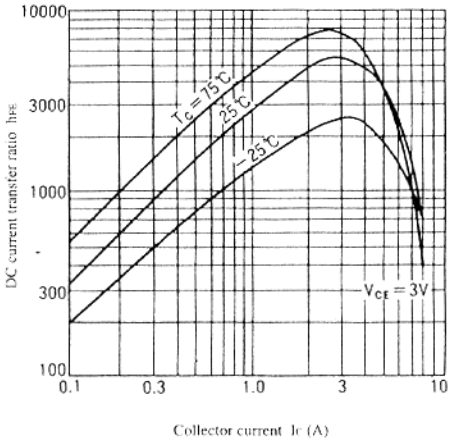
AREA OF SAFE OPERATION



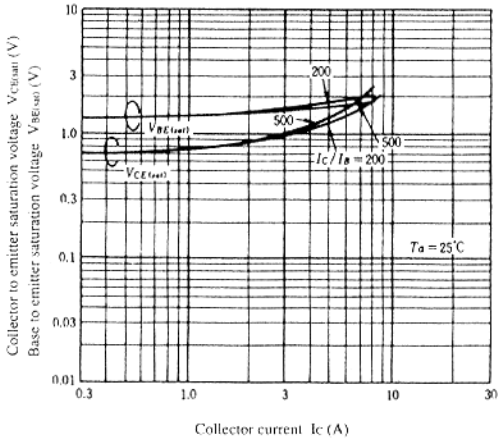
TYPICAL OUTPUT CHARACTERISTICS



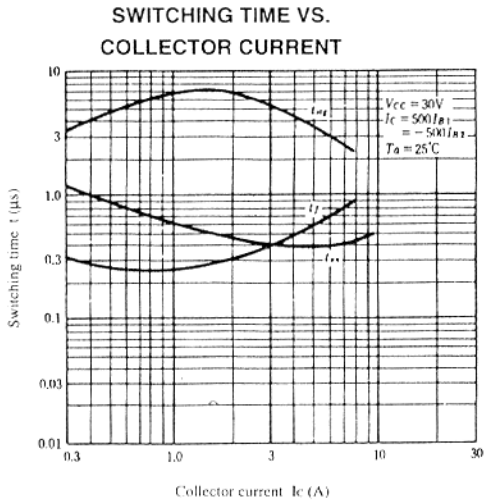
DC CURRENT TRANSFER RATIO VS. COLLECTOR CURRENT



SATURATION VOLTAGE VS. COLLECTOR CURRENT



SWITCHING TIME VS. COLLECTOR CURRENT



TRANSIENT THERMAL RESISTANCE