# 2SD1446

# Silicon NPN triple diffusion planar type Darlington

# For power amplification

## ■ Features

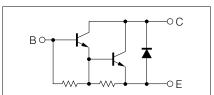
- High forward current transfer ratio h<sub>FE</sub>
- $\bullet$  High collector to base voltage  $V_{CBO}$
- Full-pack package which can be installed to the heat sink with one screw

# ■ Absolute Maximum Ratings $T_C = 25$ °C

Parameter		Symbol	Rating	Unit
Collector to base voltage		$V_{CBO}$	500	V
Collector to emitter voltage		$V_{CEO}$	400	V
Emitter to base voltage		$V_{EBO}$	5	V
Peak collector current		$I_{CP}$	10	A
Collector current		$I_{C}$	6	A
Collector power	$T_C = 25^{\circ}C$	$P_{C}$	40	W
dissipation	$T_a = 25^{\circ}C$		2	
Junction temperature		T <sub>j</sub>	150	°C
Storage temperature		$T_{stg}$	-55 to +150	°C

# Unit: mm 4.2±0.2 5.5±0.2 5.5±0.2 2.7±0.2 2.7±0.2 2.7±0.2 2.7±0.2 2.7±0.2 2.7±0.2 2.7±0.2 3.1±0.1 3.1±0.1 3.1±0.1 4.2±0.2 3.1±0.1 4.2±0.2 3.1±0.1 4.2±0.2 4.2±0.2 3.1±0.1 4.2±0.2 4.2±0.2 4.2±0.2 4.2±0.2 4.2±0.2 5.5±0.2 5.5±0.2 5.5±0.2 6.3±0.3 5.08±0.5 1: Base 2: Collector 3: Emitter EIAJ: SC-67 TO-220F Package

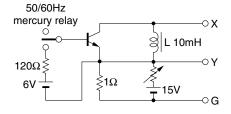
## **Internal Connection**



# ■ Electrical Characteristics $T_C = 25$ °C

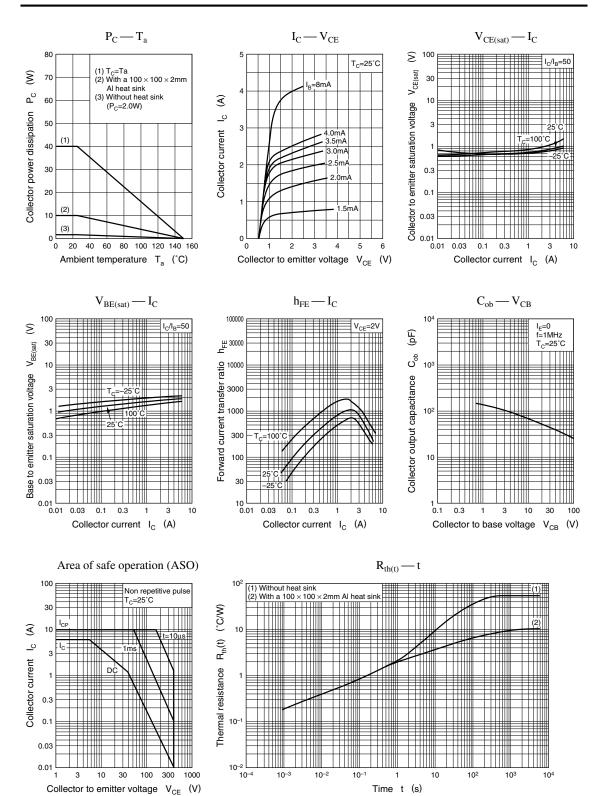
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector cutoff current	$I_{CBO}$	$V_{CB} = 350 \text{ V}, I_E = 0$			100	μΑ
Collector to emitter voltage *	V <sub>CEO(sus)</sub>	$I_C = 2 \text{ A}, L = 10 \text{ mH}$	400			V
Emitter to base voltage	$V_{EBO}$	$I_E = 0.1 \text{ A}, I_C = 0$	5			V
Forward current transfer ratio	h <sub>FE</sub>	$V_{CE} = 2 V, I_{C} = 2 A$	500			
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 3 \text{ A}, I_B = 0.06 \text{ A}$			1.5	V
Base to emitter saturation voltage	V <sub>BE(sat)</sub>	$I_C = 3 \text{ A}, I_B = 0.06 \text{ A}$			2.5	V
Transition frequency	$f_T$	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ A}, f = 1 \text{ MHz}$		15		MHz

Note) \*: V<sub>CEO(sus)</sub> Test circuit



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2SD1446 Power Transistors



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