

# 2SK2114, 2SK2115

## Silicon N Channel MOS FET

### Application

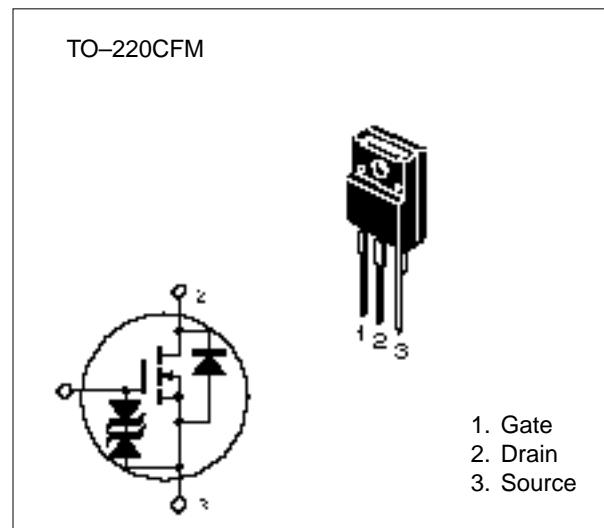
High speed power switching

### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for Switching regulator

**Table 1 Ordering Information**

Type No.	V <sub>DSS</sub>
2SK2114	450 V
2SK2115	500 V



**Table 2 Absolute Maximum Ratings (Ta = 25°C)**

Item	Symbol	Ratings	Unit
Drain to source voltage	2SK2114      V <sub>DSS</sub>	450	V
	2SK2115      V <sub>DSS</sub>	500	
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	I <sub>D</sub>	5	A
Drain peak current	I <sub>D(pulse)</sub> *	20	A
Body-drain diode reverse drain current	I <sub>DR</sub>	5	A
Channel dissipation	P <sub>ch</sub> **	35	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

\* PW ≤ 10 µs, duty cycle ≤ 1 %

\*\* Value at T<sub>c</sub> = 25 °C

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**Table 3 Electrical Characteristics (Ta = 25°C)**

Item		Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	2SK2114	V <sub>(BR)DSS</sub>	450	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
	2SK2115		500				
Gate to source breakdown voltage		V <sub>(BR)GSS</sub>	±30	—	—	V	I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0
Gate to source leak current		I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	2SK2114	I <sub>DSS</sub>	—	—	250	μA	V <sub>DS</sub> = 360 V, V <sub>GS</sub> = 0
	2SK2115						V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage		V <sub>GS(off)</sub>	2.0	—	3.0	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V
Static drain to source on state resistance	2SK2114	R <sub>DS(on)</sub>	—	1.0	1.4	Ω	I <sub>D</sub> = 2.5 A, V <sub>GS</sub> = 10 V *
	2SK2115		—	1.2	1.5		
Forward transfer admittance		y <sub>fs</sub>	2.5	4.0	—	S	I <sub>D</sub> = 2.5 A V <sub>DS</sub> = 10 V *
Input capacitance		C <sub>iss</sub>	—	640	—	pF	V <sub>DS</sub> = 10 V
Output capacitance		C <sub>oss</sub>	—	160	—	pF	V <sub>GS</sub> = 0
Reverse transfer capacitance		C <sub>rss</sub>	—	20	—	pF	f = 1 MHz
Turn-on delay time		t <sub>d(on)</sub>	—	10	—	ns	I <sub>D</sub> = 2.5 A
Rise time		t <sub>r</sub>	—	25	—	ns	V <sub>GS</sub> = 10 V
Turn-off delay time		t <sub>d(off)</sub>	—	50	—	ns	R <sub>L</sub> = 12 Ω
Fall time		t <sub>f</sub>	—	30	—	ns	
Body-drain diode forward voltage		V <sub>DF</sub>	—	0.95	—	V	I <sub>F</sub> = 5 A, V <sub>GS</sub> = 0
Body-drain diode reverse recovery time		t <sub>rr</sub>	—	300	—	ns	I <sub>F</sub> = 5 A, V <sub>GS</sub> = 0, dI <sub>F</sub> / dt = 100 A / μs

\* Pulse Test

■ See characteristics curve of 2SK1155, 2SK1156.

