

MOS FIELD EFFECT TRANSISTOR

2SK2415, 2SK2415-Z

SWITCHING

N-CHANNEL POWER MOS FET

INDUSTRIAL USE

DESCRIPTION

The 2SK2415 is N-Channel MOS Field Effect Transistor designed for high voltage switching applications.

FEATURES

- Low On-Resistance
 $R_{DS(on)1} = 0.10 \Omega$ MAX. (@ $V_{GS} = 10 \text{ V}$, $I_D = 4.0 \text{ A}$)
 $R_{DS(on)2} = 0.15 \Omega$ MAX. (@ $V_{GS} = 4 \text{ V}$, $I_D = 4.0 \text{ A}$)
- Low C_{iss} $C_{iss} = 570 \text{ pF}$ TYP.

QUALITY GRADE

Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

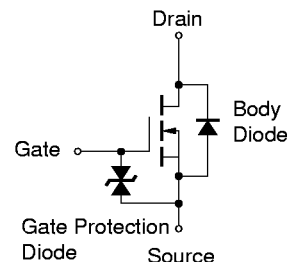
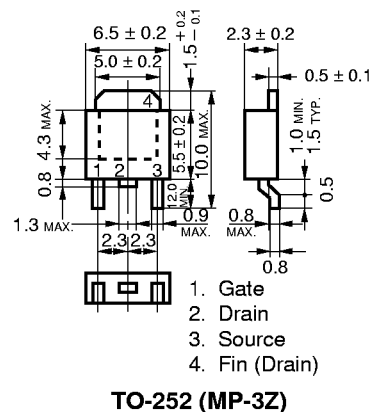
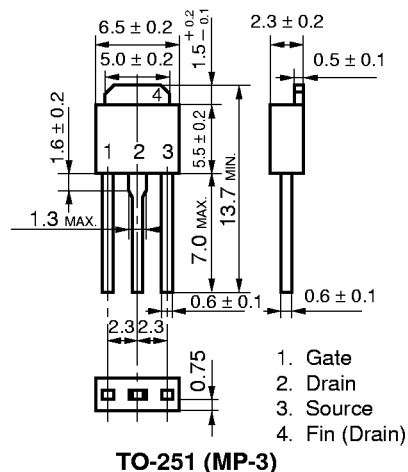
Drain to Source Voltage	V_{DSS}	60	V
Gate to Source Voltage	V_{GSS}	± 20	V
Drain Current (DC)	$I_{D(DC)}$	± 8.0	A
Drain Current (pulse)*	$I_{D(pulse)}$	± 32	A
Total Power Dissipation ($T_c = 25^\circ\text{C}$)	P_{T1}	20	W
Total Power Dissipation ($T_A = 25^\circ\text{C}$)	P_{T2}	1.0	W
Channel Temperature	T_{ch}	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to $+150$	$^\circ\text{C}$
Single Avalanche Current**	I_{AS}	8.0	A
Single Avalanche Energy**	E_{AS}	6.4	mJ

* $PW \leq 10 \mu\text{s}$, Duty Cycle $\leq 1\%$

** Starting $T_{ch} = 25^\circ\text{C}$, $R_G = 25 \Omega$, $V_{GS} = 20 \text{ V} \rightarrow 0$

PACKAGE DIMENSIONS

(in millimeters)

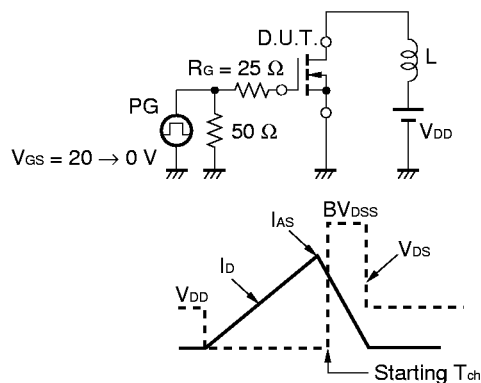


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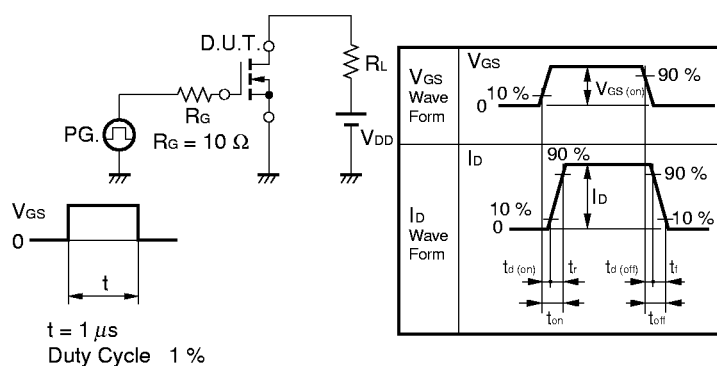
ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-State Resistance	R _{DS(on)1}		0.07	0.10	Ω	V _{GS} = 10 V, I _D = 4.0 A
Drain to Source On-State Resistance	R _{DS(on)2}		0.10	0.15	Ω	V _{GS} = 4 V, I _D = 4.0 A
Gate to Source Cutoff Voltage	V _{GS(off)}	1.0	1.6	2.0	V	V _{DS} = 10 V, I _D = 1 mA
Forward Transfer Admittance	y _{fs}	5.0	8.4		S	V _{DS} = 10 V, I _D = 4.0 A
Drain Leakage Current	I _{DSS}			10	μA	V _{DS} = 60 V, V _{GS} = 0
Gate to Source Leakage Current	I _{GSS}			±10	μA	V _{GS} = ±20 V, V _{DS} = 0
Input Capacitance	C _{iss}		570		pF	V _{DS} = 10 V
Output Capacitance	C _{oss}		290		pF	V _{GS} = 0
Reverse Transfer Capacitance	C _{rss}		75		pF	f = 1 MHz
Turn-On Delay Time	t _{d(on)}		5		ns	I _D = 4.0 A
Rise Time	t _r		60		ns	V _{GS(on)} = 10 V
Turn-Off Delay Time	t _{d(off)}		75		ns	V _{DD} = 30 V
Fall Time	t _f		40		ns	R _G = 10 Ω
Total Gate Charge	Q _G		21		nC	I _D = 8.0 A
Gate to Source Charge	Q _{GS}		2.0		nC	V _{DD} = 48 V
Gate to Drain Charge	Q _{GD}		6.5		nC	V _{GS} = 10 V
Body Diode Forward Voltage	V _{F(S-D)}		1.0		V	I _F = 8.0 A, V _{GS} = 0
Reverse Recovery Time	t _{rr}		85		ns	I _F = 8.0 A, V _{GS} = 0
Reverse Recovery Charge	Q _{rr}		200		nC	di/dt = 100 A/μs

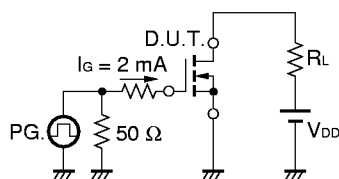
Test Circuit 1 Avalanche Capability



Test Circuit 2 Switching Time

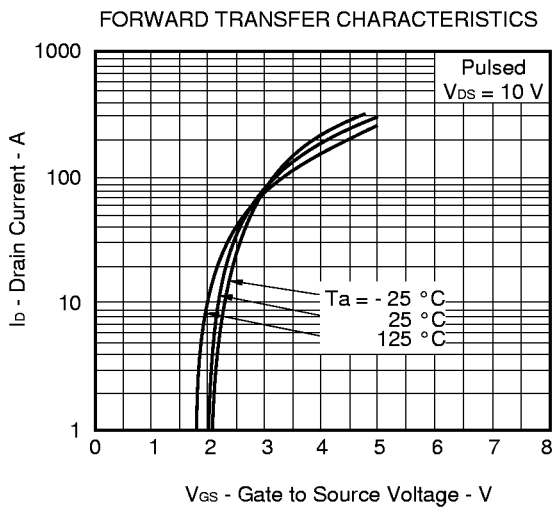
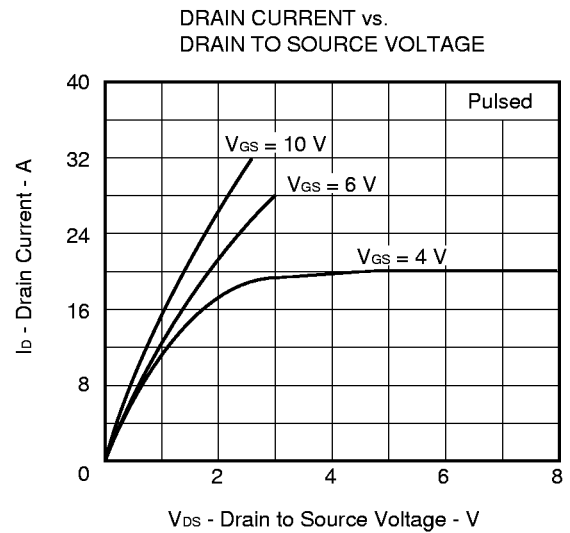
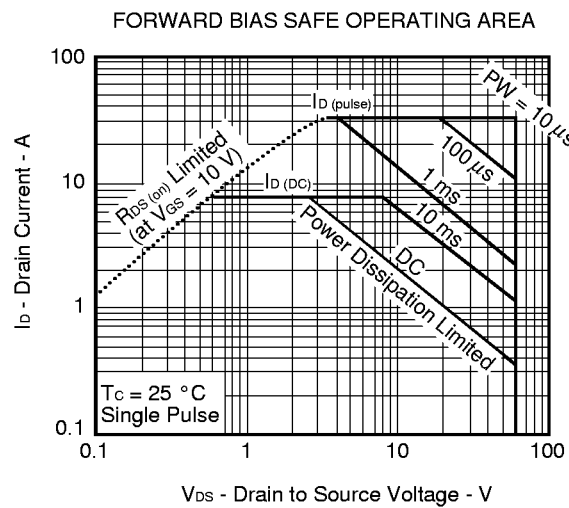
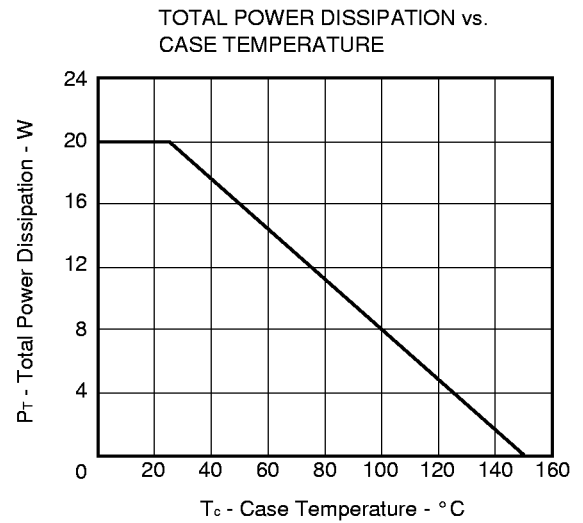
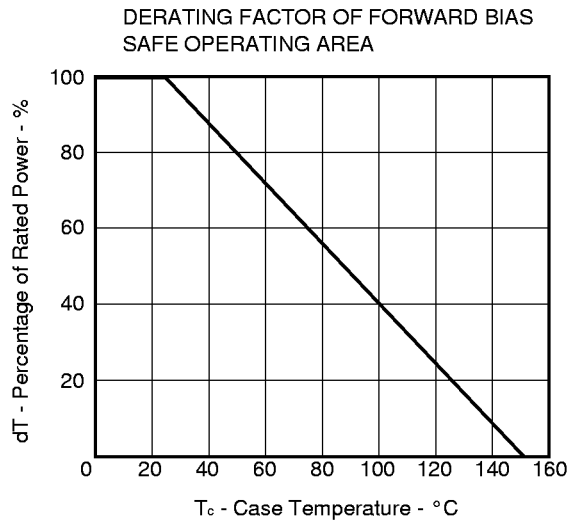


Test Circuit 3 Gate Charge

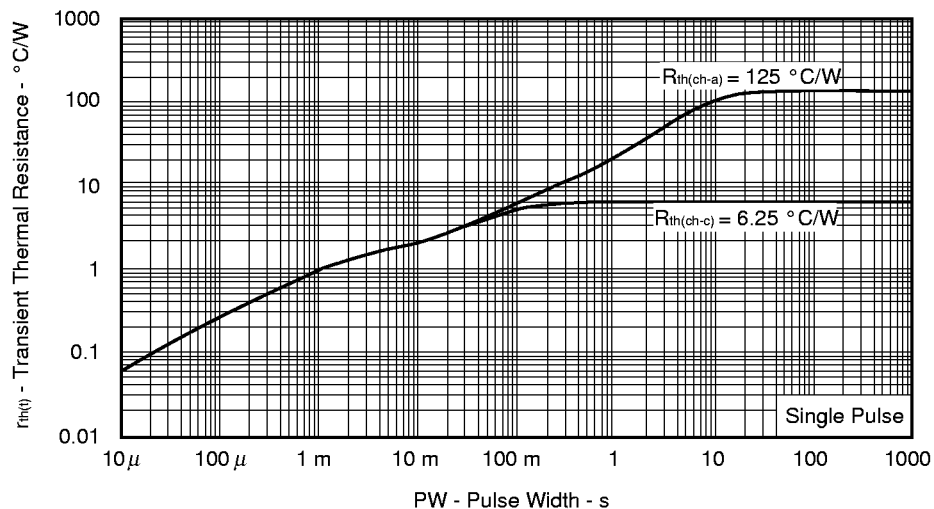


The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

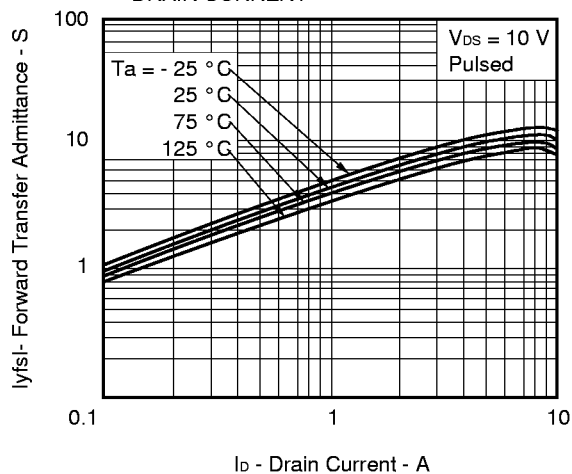
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)



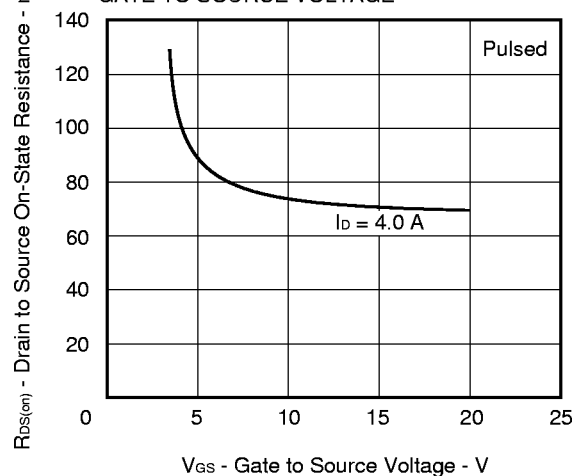
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



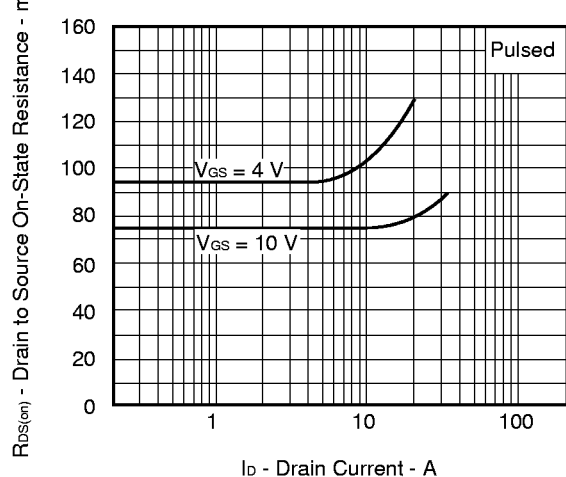
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE

