

9097250 TOSHIBA (DISCRETE/OPTO)

99D 16743

DT-39-13



SEMICONDUCTOR

TECHNICAL DATA

TOSHIBA FIELD EFFECT TRANSISTOR

2 S K 6 9 4

SILICON N CHANNEL MOS TYPE

(7-MOS1)

INDUSTRIAL APPLICATIONS

Unit in mm

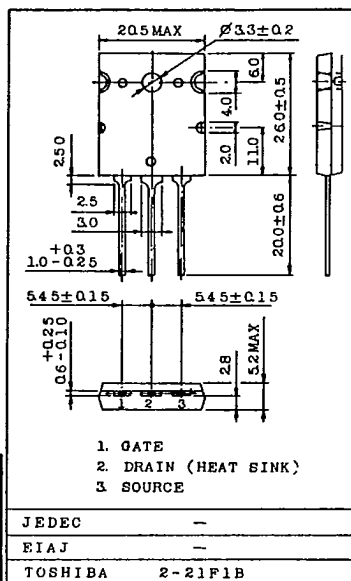
HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS.
CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR
DRIVE APPLICATIONS.

FEATURES:

- Low Drain-Source ON Resistance : $R_{DS(ON)}=0.40\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}|=9.0S$ (Typ.)
- Low Leakage Current : $I_{GSS}=\pm 100nA$ (Max.) @ $V_{GS}=\pm 20V$
 $I_{DSS}=300\mu A$ (Max.) @ $V_{DS}=500V$
- Enhancement-Mode : $V_{th}=2.0\sim 4.0V$ @ $V_{DS}=10V, I_D=1mA$

MAXIMUM RATINGS ($T_a=25^\circ C$)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	V_{DSX}	500	V
Drain-Gate Voltage ($R_{GS}=20k\Omega$)	V_{DGR}	500	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current	DC ($T_c=25^\circ C$)	I_D	A
	Pulse	I_{DP}	
Drain Power Dissipation ($T_c=25^\circ C$)	P_D	150	W
Channel Temperature	T_{ch}	150	$^\circ C$
Storage Temperature Range	T_{stg}	$-55\sim 150$	$^\circ C$



Weight : 9.75g

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Junction to Case	$R_{th(j-c)}$	0.83	$^\circ C/W$
Thermal Resistance, Junction to Ambient	$R_{th(j-a)}$	30	$^\circ C/W$
Maximum Lead Temperature for Soldering Purposes (1.6mm from case for 10 seconds)	T_L	300	$^\circ C$

TOSHIBA CORPORATION

9097250 TOSHIBA (DISCRETE/OPTO)

99D 16744 DT-39-13

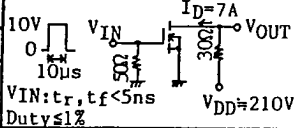


SEMICONDUCTOR

TECHNICAL DATA

2 S K 6 9 4

ELECTRICAL CHARACTERISTICS (Ta=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current	IGSS	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Drain Cut-off Current	IDSS	V _{DS} =500V, V _{GS} =0V	-	-	300	μA
Drain-Source Breakdown Voltage	V(BR)DSS	I _D =10mA, V _{GS} =0V	500	-	-	V
Gate Threshold Voltage	V _{th}	V _{DS} =10V, I _D =1mA	2.0	-	4.0	V
Forward Transfer Admittance	Y _{fs}	V _{DS} =10V, I _D =7A	6.0	9.0	-	S
Drain-Source ON Resistance	R _{DS(ON)}	I _D =7A, V _{GS} =10V	-	0.40	0.50	Ω
Drain-Source ON Voltage	V _{DS(ON)}	I _D =12A, V _{GS} =10V	-	5.3	7.0	V
Input Capacitance	C _{iss}	V _{DS} =10V, V _{GS} =0V, f=1MHz	-	2300	3600	pF
Reverse Transfer Capacitance	C _{rss}		-	450	680	
Output Capacitance	C _{oss}		-	1000	1400	
Switching Time	Rise Time		-	70	140	ns
	Turn-on Time		-	100	200	
	Fall Time		-	75	150	
	Turn-off Time		-	350	700	
Total Gate Charge (Gate-Source Plus Gate-Drain)	Q _g	I _D =12A, V _{GS} =10V V _{DD} =400V	-	82	110	nC
Gate-Source Charge	Q _{gs}		-	47	-	
Gate-Drain ("Miller") Charge	Q _{gd}		-	35	-	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I _{DR}	--	-	-	12	A
Pulse Drain Reverse Current	I _{DRP}	--	-	-	48	A
Diode Forward Voltage	V _{DSF}	I _{DR} =12A, V _{GS} =0V	-	-	1.7	V
Reverse Recovery Time	t _{rr}	I _{DR} =12A	-	400	-	ns
Reverse Recovered Charge	Q _{rr}	dI _{DR} /dt=100A/μs	-	4.0	-	μC

TOSHIBA CORPORATION

GT1A9A