

3SK180

# High-Frequency General-Purpose Amplifier Applications

## **Applications**

· FM tuners and VHF tuners.

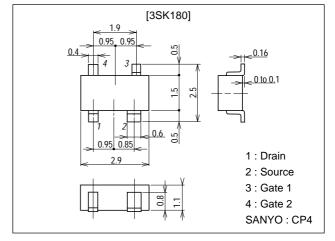
#### **Features**

- · High power gain and low noise figure.
- · High forward transfer admittance.

## **Package Dimensions**

unit:mm

2046A



## **Specifications**

### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DS</sub>		15	V
Gate1-to-Source Voltage	V <sub>G1S</sub>		±7	V
Gate2-to-Source Voltage	V <sub>G2S</sub>		±7	V
Drain Current	I <sub>D</sub>		30	mA
Allowable Power Dissipation	P <sub>D</sub>		200	mW
Channel Temperature	Tch		125	°C
Storage Temperature	Tstg		-55 to +125	°C

#### Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Voltage	V <sub>DS</sub>	V <sub>G1S</sub> =-4V, V <sub>G2S</sub> =0V, I <sub>DS</sub> =100μA	15			V
Gate1-to-Source Breakdown Voltage	V <sub>(BR)G1SS</sub>	$I_{G1}=10\mu A, V_{DS}=0, V_{G2S}=0 V$	±7			V
Gate2-to-Source Breakdown Voltage	V(BR)G2SS	$I_{G2}=10\mu A, V_{DS}=0, V_{G1S}=0 V$	±7			V
Gate1-to-Source Cutoff Voltage	VG1S(off)	V <sub>DS</sub> =10V, V <sub>G2S</sub> =4V, I <sub>D</sub> =100μA			-3	V
Gate2-to-Source Cutoff Voltage	V <sub>G2S(off)</sub>	$V_{DS}=10V, V_{G1S}=0V, I_{D}=100\mu A$			-2.5	V
Gate1-to-Source Leakage Current	I <sub>G1SS</sub>	V <sub>G1S</sub> =±5V, V <sub>G2S</sub> =V <sub>DS</sub> =0V			±50	nA
Gate2-to-Source Leakage Current	I <sub>G2SS</sub>	$V_{G2S}=\pm5V$ , $V_{G1S}=V_{DS}=0V$			±50	nA
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =10V, V <sub>G1S</sub> =0, V <sub>G2S</sub> =4V	2.5*		24*	mA

\*: The 3SK180 is classified by I<sub>DSS</sub> as follows: (unit: mA)

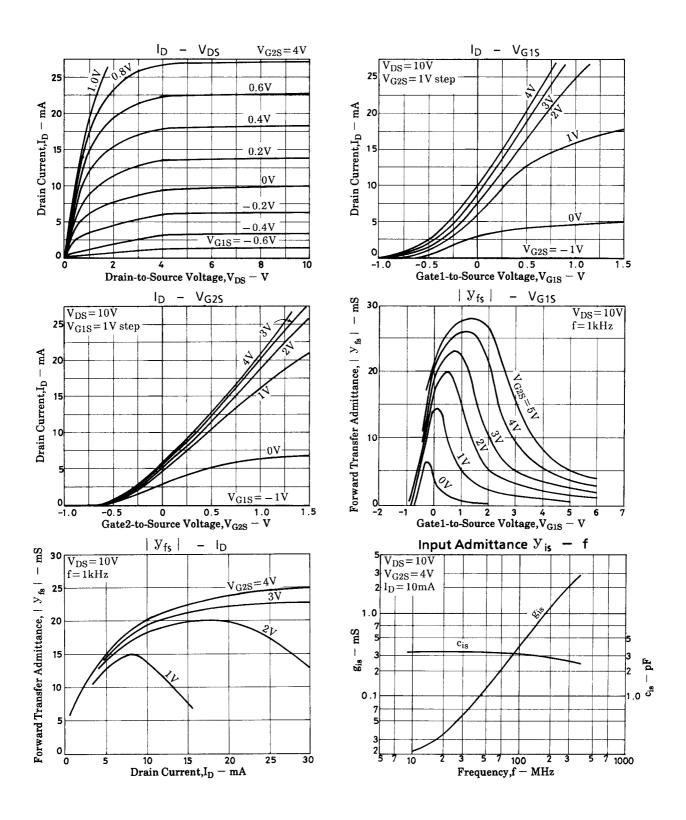
Marking : DJ I<sub>DSS</sub> rank : 4, 5, 6 2.5 4 6.0 5.0 5 12.0 10.0 6 24.0

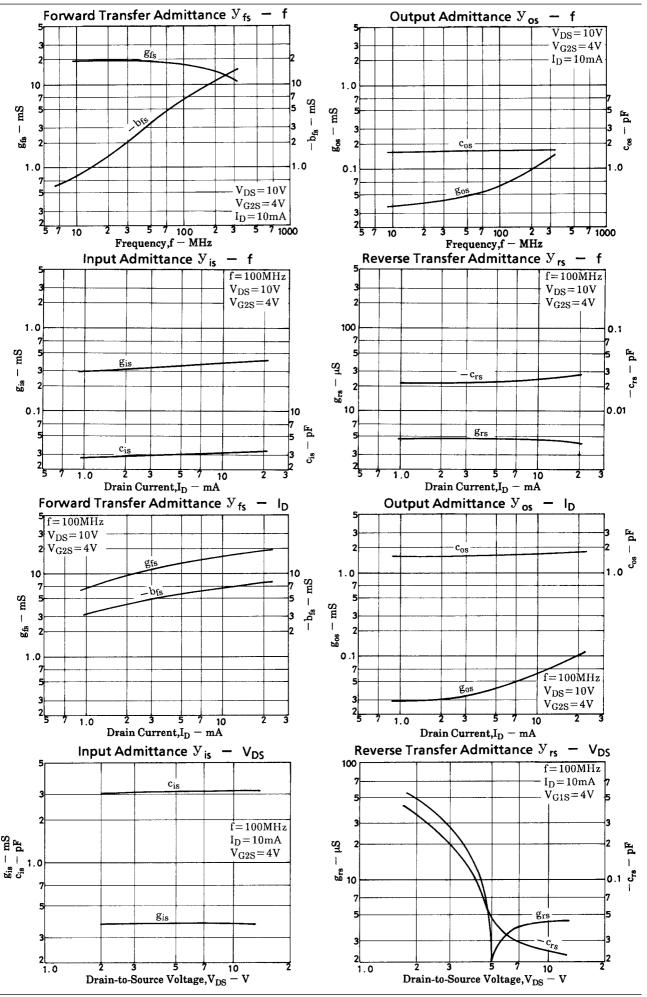
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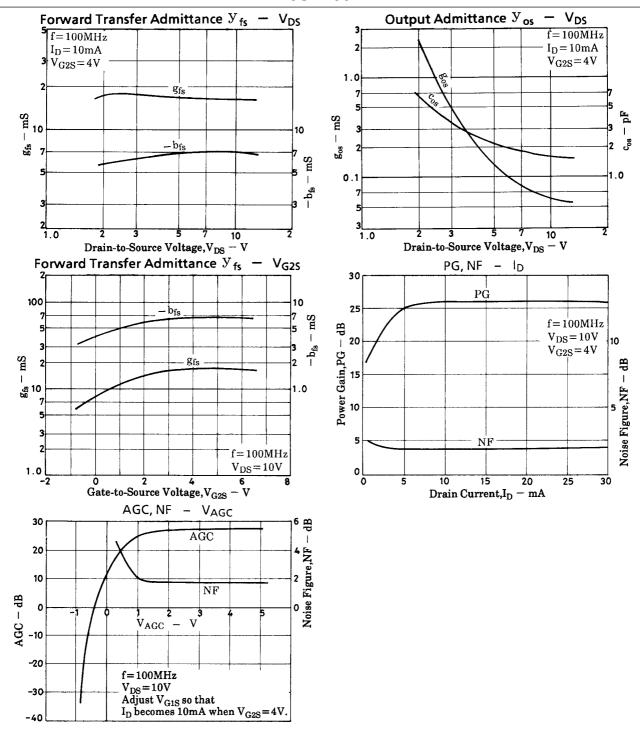
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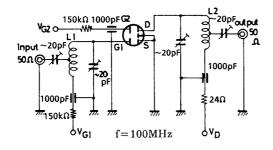
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Forward Transfer Admittance	yfs	f=1kHz, I <sub>D</sub> =10mA, V <sub>DS</sub> =10V, V <sub>G2S</sub> =4V		20		mS
Input Capacitance	Ciss	$V_{DS}$ =10V, f=1MHz, $V_{G1S}$ =0V, $V_{G2S}$ =4V		3.0		pF
Reverse Transfer Capacitance	Crss	$V_{DS}$ =10V, f=1MHz, $V_{G1S}$ =0V, $V_{G2S}$ =4V		0.02	0.05	pF
Power Gain	PG	V <sub>DS</sub> =10V, I <sub>D</sub> =10mA	22	28		dB
Noise Figure	NF	f=100MHz, V <sub>G2S</sub> =4V		1.8	3.0	dB







**PG, NF Specified Test Circuit** 



 $L1:1mm \emptyset \ plated \ wire \ 10mm \emptyset \ 4T, tap:1T \ from \ gate \ side$   $L2:1mm \emptyset \ plated \ wire \ 10mm \emptyset \ 6T, tap:1T \ from \ drain \ side$ 

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