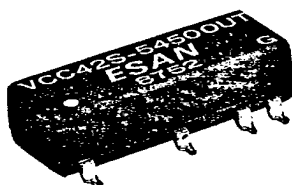


- TTL Compatible
- 5 Tap Output
- 14 Pin DIL Package

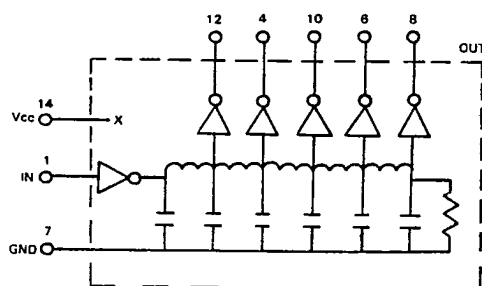


Specifications:

- Delay tolerance : $\pm 5\%$ or 2ns whichever is greater
- Rise time : 4ns max.
- Temp. coefficient : 100 ppm/ $^{\circ}\text{C}$
- Operating Temp. Range: -0°C to 70°C (-55°C to $+125^{\circ}\text{C}$ on request)
- Minimum Pulse Width : 40% of total delay
- Supply Voltage : $5\text{Vcc} \pm 5\%$
- Logic 1 Input current : 50 μA max.
- Logic 0 Input current : -2 mA max.
- Logic 1 Output : 2.75V min
- Logic 0 Output : 0.4V max.
- Logic 1 Fan out : 20/TAP max.
- Logic 0 Fan out : 10/TAP max.
- Power Dissipation : 375 mW max.

Test Conditions:

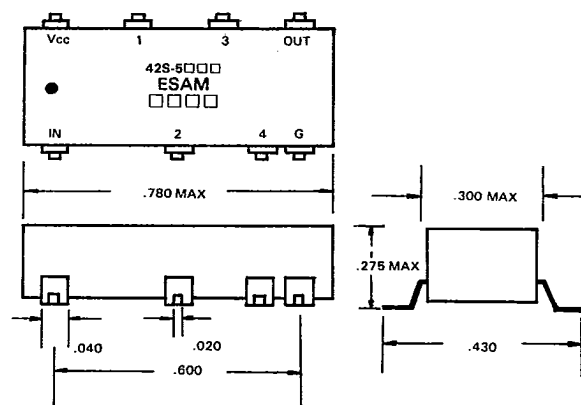
- Input pulse voltage : 3.2V
- Input pulse width : $1.2 \times \text{total delay}$
- Input pulse spacing : $2 \times \text{total delay}$
- Input pulse rise time: 2 ns
- Rise time : 0.75V to 2.4V
- Time delay measured : @ 1.5V level
- All measurement : @ $\text{Vcc} = 5.0\text{V}$, $\text{TA} = 25^{\circ}\text{C}$



Features:

- Schottky TTL interface
- Low power schottky available named "42SL-5xxx" series
- Both Leading and Trailing edge available under request named "42SB-5xxx" series

PART NO.	TOTAL DELAY (ns)	TAP TO TAP (ns)
42S-5250	25	5
42S-5500	50	10
42S-5750	75	15
42S-5101	100	20
42S-5125	125	25
42S-5151	150	30
42S-5201	200	40
42S-5251	250	50
42S-5301	300	60
42S-5401	400	80
42S-5501	500	100



Unit: Inch