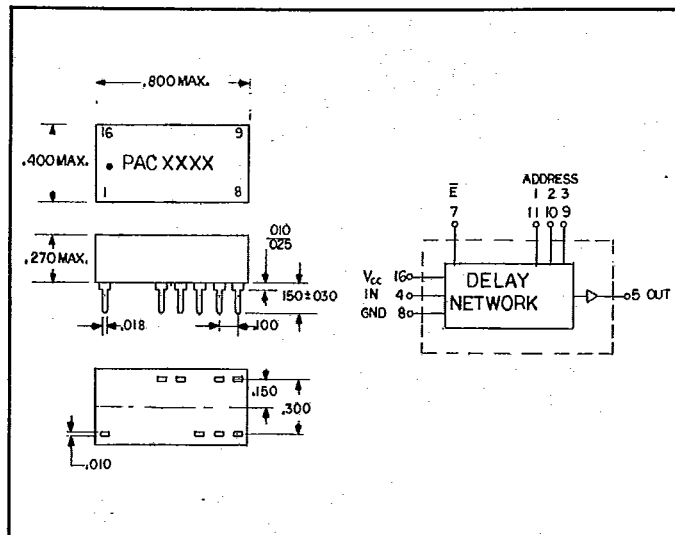


DELAY MODULES, TTL, PROGRAMMABLE

3 BITS 16 PIN

T-47-17



- TTL and DTL compatible
- All delays digitally programmable
- Completely interfaced
- Low Profile

Part Number	Incremental Delay Step ns	Programmable Delay Range Low ns ± 1 High ns
4300	.5 $\pm$.2 ns	7 10.5 $\pm$ 1ns
4301	1 $\pm$.3 ns	7 14 $\pm$ 1ns
4302	2 $\pm$.4 ns	7 21 $\pm$ 5%
4303	3 $\pm$.5 ns	7 28 $\pm$ 5%
4304	4 $\pm$.5 ns	7 35 $\pm$ 5%
4305	5 $\pm$ 10%	7 42 $\pm$ 5%
4306	6 $\pm$ 10%	7 49 $\pm$ 5%
4307	7 $\pm$ 10%	7 56 $\pm$ 5%
4308	8 $\pm$ 10%	7 63 $\pm$ 5%
4309	9 $\pm$ 10%	7 70 $\pm$ 5%
4310	10 $\pm$ 10%	7 77 $\pm$ 5%
4311	15 $\pm$ 8%	7 112 $\pm$ 5%
4312	20 $\pm$ 7%	7 147 $\pm$ 5%
4313	40 $\pm$ 5%	7 287 $\pm$ 5%
4314	50 $\pm$ 5%	7 357 $\pm$ 5%

TRUTH TABLE

Enable	Address (Bit No.)			Delay Out	
	3	2	1		
0	0	0	0	T_0	1 = High 0 = Low Ø = Don't Care T_0 = Reference or inherent delay of circuit T_1 to T_7 = Multiplier of incremental delay.
0	0	0	1	T_1	
0	0	1	0	T_2	
0	0	1	1	T_3	
0	1	0	0	T_4	
0	1	0	1	T_5	
0	1	1	0	T_6	
0	1	1	1	T_7	
1	Ø	Ø	Ø	0	

OPERATING SPECIFICATIONS

Vcc supply voltage:	4.75 to 5.25 VDC
Logic 1 input:	
Voltage	2V min; 5.5V max.
Current	2.4V = 50ua max. 5.5V = 1ma max.
Logic 0 input:	
Voltage	.8V max.
Current	-2ma max.
Logic 1 Voltage out:	2.4V min.
Logic 0 Voltage out:	.4V max.
Operating temperature range:	0° to 70°C
Storage temperature:	-55 to +125°C.

TEST CONDITIONS

Input pulse width:	Minimum of 50% of Delay
Input pulse rise time:	3ns
Input pulse voltage:	3.2V
Vcc supply voltage:	5.0 VDC
Icc supply current:	40 ma typ.
All measurements made at 25°C.	

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