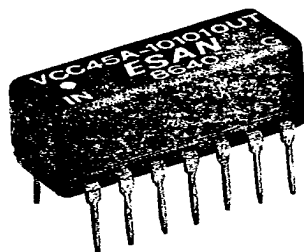


## Active Delay Line (TTL Interface)

## 45A Series

- 10 Tap Output
- DIL Package
- Auto-Insertable



## Specifications:

- Delay tolerance :  $\pm 5\%$  or 2ns  
whichever is greater
- Rise time : 4ns max.
- Temp. coefficient : 100 ppm/ $^{\circ}\text{C}$
- Operating Temp. Range :  $-0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$
- Minimum Pulse Width : 40% of total delay
- Supply Voltage : 5Vdc  $\pm 5\%$
- Logic 1 Input current : 50  $\mu\text{A}$  max.
- Logic 0 Input current :  $-2$  mA max.
- Logic 1 Output : 2.75V min.
- Logic 0 Output : 0.4V max.
- Logic 1 Fan out : 20/TAP max.
- Logic 0 Fan out : 10/TAP max.
- Power Dissipation : 780 mW max.

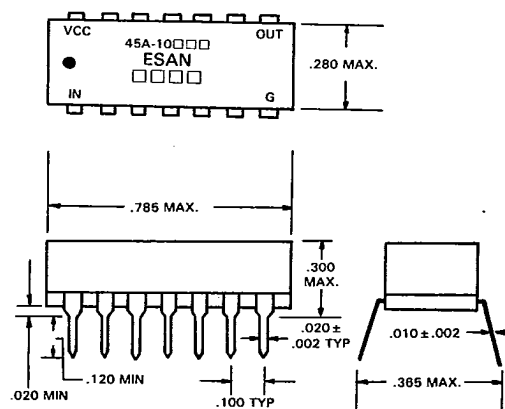
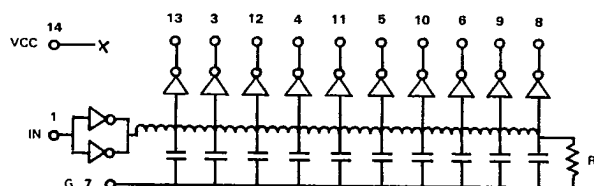
## Test Conditions:

- Input pulse voltage : 3.2V
- Input pulse width :  $1.2 \times$  total delay
- Input pulse spacing :  $2 \times$  total delay
- Input pulse rise time : 2 ns
- Rise time : 0.75V to 2.4V
- Time delay measured : @ 1.5V level
- All measurement : @  $V_{CC} = 5.0\text{V}$ ,  $T_A = 25^{\circ}\text{C}$

## Features:

- Schottky TTL interface
- Low power schottky available named "45AL-10xxx" series
- Both Leading and Trailing edge available under request named "45B-10xxx" series

| PART NO.  | TOTAL DELAY (ns) | DELAY (ns) |
|-----------|------------------|------------|
| 45A-10500 | 50               | 5          |
| 45A-10101 | 100              | 10         |
| 45A-10151 | 150              | 15         |
| 45A-10201 | 200              | 20         |
| 45A-10251 | 250              | 25         |
| 45A-10301 | 300              | 30         |
| 45A-10351 | 350              | 35         |
| 45A-10401 | 400              | 40         |
| 45A-10451 | 450              | 45         |
| 45A-10501 | 500              | 50         |



Unit: Inch