

Product Preview

4 Megabit 128K x 32 Bit Fast SRAM Multi-Chip Module

The 46226 is a 4,096,000 bit static random access memory organized as 131,072 words of 8, 16, 24 or 32 bits. The module is constructed using four 6226 static RAM die mounted in a multilayer co-fired hermetic ceramic sealed package.

- High Density 4.0 Megabit SRAM (128K x 32)
- High Speed CMOS SRAMS
- Fast Access Time — Military 25 ns (Max)
- Available in 68 pin hermetic package (1.7 inch x 0.900 inch)
- Single 5.0 V ($\pm 10\%$) Power Supply
- Two Module Controls \bar{E} , \bar{E} for Automatic Power Down
 \bar{G} for Fast Access to Data

Pin Names and Function	
A ₀ — A ₁₆	Address Inputs
\bar{W}	Write Enable
E ₀ — E ₃ , \bar{E}_0 — \bar{E}_3	Chip Enable
\bar{G}	Output Enable
I/O ₀ — I/O ₃₁	Data I/O
V _{CC}	+ 5.0 volt Power Supply
V _{SS}	Ground

Truth Table							
E	\bar{E}	\bar{G}	\bar{W}	Mode	V _{CC} Currents	I/O Pin	Cycle
L	X	X	X	Not Selected	I _{ISB1} , I _{ISB2}	HI-Z	—
X	H	X	X	Not Selected	I _{ISB1} , I _{ISB2}	HI-Z	—
H	L	H	H	Read	I _{CCA}	HI-Z	—
H	L	L	H	Read	I _{CCA}	DOUT	Read Cycle
H	L	X	L	Write	I _{CCA}	DIN	Write Cycle

X = Don't Care

CAPACITANCE:

(f = 1.0 MHz, dV = 3.0 V, T_A = 25°C, Periodically Sampled Rather Than 100% Tested)

Characteristic	Symbol	Typ	Max	Unit
Input Capacitance All Inputs Except \bar{E} , E and I/O	C _{in}	16	24	pF
	C _{in}	20	28	pF
I/O Capacitance DQ (I/O)	C _{I/O}	20	28	pF

Military 46226



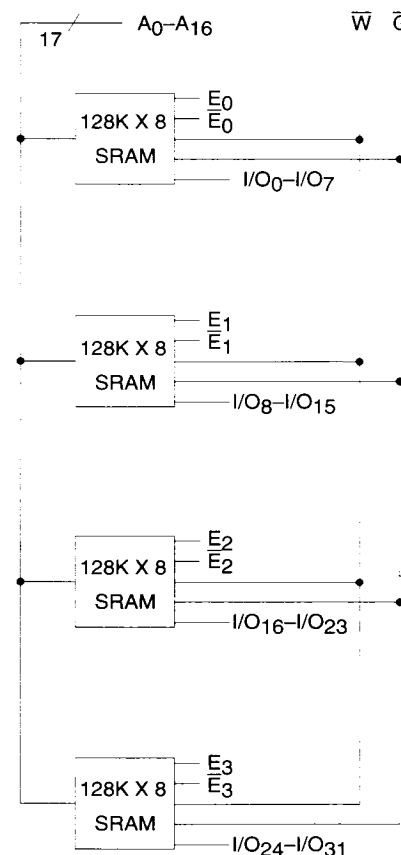
SRAM MULTI-CHIP MODULE

AVAILABLE AS

- 1) JAN: N/A
 - 2) SMD: N/A
 - 3) 883: Planned
- X = CASE OUTLINE AS FOLLOWS:
PACKAGE:

DIL: 46226-XX/BXAJC
(Form Leads) DIL: 46226-XX/BYAJC
XX = Speed in ns (25, 30, 35, 45)

BLOCK DIAGRAM



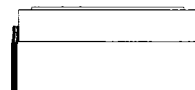
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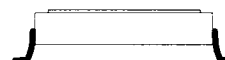
MOTOROLA

PACKAGE OFFERING


68 PIN DUAL-IN-LINE SIDEBRAZE



68 PIN DUAL-IN-LINE SIDEBRAZE
(FORMED LEAD OPTION)



FOR MORE INFORMATION CONTACT **DENNIS MAHAR** (602) 897-3541
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