

Cub 486/386DX (SMM)

Notebook/Green PC

Chipset Specifications

Process technology used	1.0µ CMOS	Programmable wait states	Yes
Support SMI#, SMIADS#, SUSP#, SUSA# interface		On chip cache controller	Yes
CPU's supported	Intel 80486DX, 80486SX, 80386DX AMD Am386DXLV Am486DXLV Cyrix Cx486DLC Cx486S2 Texas Instruments TI486DLC	Support 8kx8 and 8kx9 Tag RAM	Yes
CPU speed supported	8/16/20/25/33/40/50 MHz	Cache update scheme	Write back
Coprocessor supported in 386 system (No external PALS required)	Intel 80387/ Cyrix/IIT/ ULSI/WEITEK/3167	Cache organization	Direct mapped
Coprocessors supported in 486 system (No external PALS required)	WEITEK 4167	Data cache size	32KB-512KB
AT clock generation	Async/Sync	Non-cacheable support	Yes
Programmable AT bus clock	Yes	Each non-cacheable region size	512KB-32MB
Hardware/Software turbo switching	Yes	Zero wait state cache read hit	Yes
Page Mode DRAM control	Yes	Zero wait state cache write hit	Yes
DRAM type supported	256K/1M/4M	DRAM Burst mode support	Yes
Max physical memory	64MB	External TTL component	12 ~ 15
Mixing DRAMS	Yes	SRAM Burst mode support	Yes
Hidden refresh	Yes	2-1-1-1, 2-2-2-2, 3-1-1-1, 3-2-2-2	
Shadow RAM range maximum size/block size	256 KB/64KB	SRAM speed required @ 50MHz	20ns
Supported Local Bus	Yes	SRAM speed required @ 40MHz	20ns
PQFP package	208	SRAM speed required @ 33MHz	20ns
Power Saving Mode:	Run/Doze/Sleep/ Suspend/Turbo	SRAM speed required @ 25MHz	35ns
Slow Refresh during Suspend Mode	Yes	Fast gate A20	Yes
Peripheral Activity monitor	Yes	Fast reset	Yes
System Idle detection	Yes	22cm x 22cm half baby AT board	Yes
System and Peripheral power control	Yes		

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