

T-49-17-34

Cheetah 486SX/486DX non-cache Chipset Specifications

Process technology used	1.0μ CMOS	22cm x 22cm half baby AT board	Yes
CPU's supported	Intel 80486, 80486SX AMD Am386DX, Am486DX Cyrix 486DLC	Hidden refresh	Yes
CPU speed supported	16/20/25/33/40/50 MHz	Shadow RAM range maximum size/block size	256KB/64KB
Coprocessors supported (No external PALS required)	WEITEK 4167	Programmable wait states	Yes
AT clock generation	Async	DRAM Burst mode support 3-2-2-2, 4-3-3-3, 5-4-4-4	Yes
Hardware/Software turbo switching	Yes	Local Bus support	Yes
Page Mode DRAM control	Yes	PQFP package	160
DRAM type supported	256K/1M/4M	External TTL component	Less than 10
Max physical memory	64MB	DRAM speed	80ns
Mixing DRAMS	Yes	Fast gate A20	Yes
		Fast reset	Yes

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