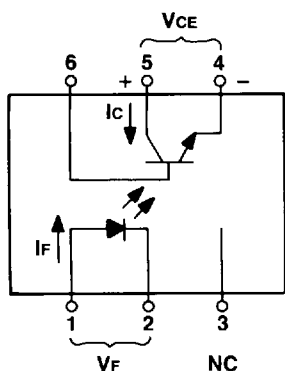
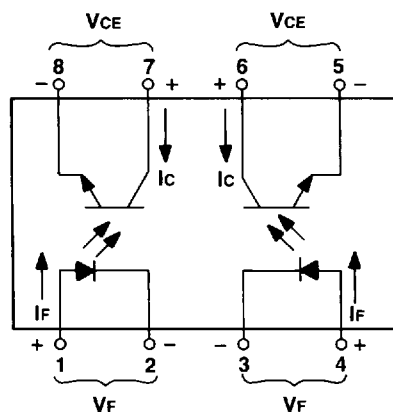


UNIPOLARITY IRED INPUT, TRANSISTOR OUTPUT

Single Channel



Base Lead to No. 6 Pin	Specified Input Current (I_F) mA	Current Transfer Ratio (CTR) min	Sustained Voltage [$V_{CE(SUS)}$] V min	(I_{CEO}) ¹ nA max	Isolation Current ² (I _{ISO}) nA max	Pulse Time (T_r, T_f) max	Device Code Number
none	1.0	1.0	17	100	100	5.0 μ sec	4W
none	1.0	1.0	17	100	100	5.0 μ sec	4L
yes	2.0	0.6	60	100	100	5.0 μ sec	4A
none	2.0	0.6	12	100	100	5.0 μ sec	4J
yes	1.0	0.3	60	100	100	5.0 μ sec	4F
none	0.5	0.3	12	100	100	5.0 μ sec	4M
none	0.16	—	30	100	100	20 msec	4U ³



Dual Channel

Specified Input Current (I_F) mA	Current Transfer Ratio (CTR) min	Sustained Voltage [$V_{CE(SUS)}$] V min	(I_{CEO}) ¹ nA max	Isolation Current ² (I _{ISO}) nA max	Pulse Time (T_r, T_f) μ sec max	Propagation Delay μ sec max	Device Code Number
2.0	0.6	60	100	100	50	—	2C
2.0	1.8	60	100	100	—	50	2K
1.0	1.0	60	100	100	5.0	—	2J

Applications:

- Isolate high voltage transients
- Replace relays and transformers
- Eliminate ground loop feed-throughs
- Maintain floating grounds
- CMOS TTL logic interface

¹ Collector-Emitter Leakage Current

² Specified at (V_{ISO}) = 2500V

³ UL Recognized (File No. E86340).

Approvals can be obtained for other devices.