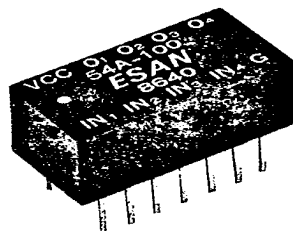


- Quad TTL Compatible
- Quad In/Out Delay
- 14 Pin Dip Package



Specifications:

- Delay tolerance : $\pm 5\%$ or 2ns
whichever is greater
- Rise time : 4ns max.
- Temp. coefficient : 100 ppm/°C
- Operating Temp. Range : -0°C to 70°C
 -55°C to $+125^{\circ}\text{C}$
on request)
- Minimum Pulse Width : 40% of total delay
- Supply Voltage : $5\text{V}_{\text{DC}} \pm 5\%$
- Logic 1 Input current : 50 μA max.
- Logic 0 Input current : -2 mA max.
- Logic 1 Output : 2.75V min.
- Logic 0 Output : 0.4V max.
- Logic 1 Fan out : 20/TAP max.
- Logic 0 Fan out : 10/TAP max.
- Power Dissipation : 375 mW max.

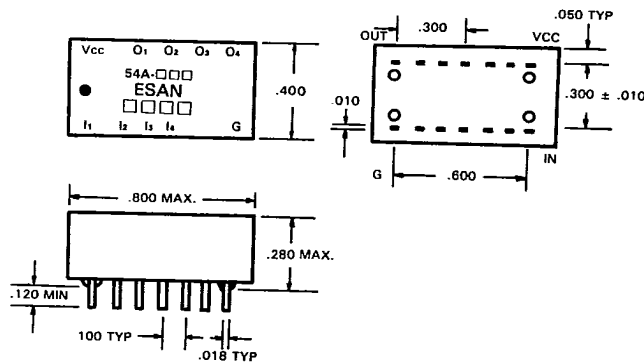
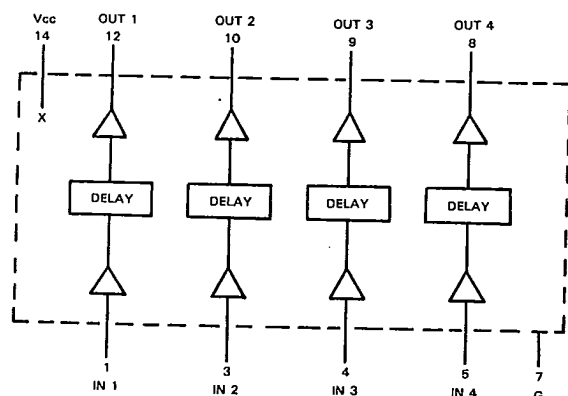
Test Conditions:

- Input pulse voltage : 3.2V
- Input pulse width : $1.2 \times \text{total delay}$
- Input pulse spacing : $2 \times \text{total delay}$
- Input pulse rise time : 2 ns
- Rise time : 0.75V to 2.4V
- Time delay measured : @ 1.5V level
- All measurement : @ $V_{\text{CC}} = 5.0\text{V}$, $T_A = 25^{\circ}\text{C}$

Features:

- Schottky TTL interface
- Low power schottky available named "54AL- xxx" series
- Both Leading and Trailing edge available under request named "54B-xxx" series
- Ceramic IC meet MIL-STD 883 level B available named "54AM- xxx series"

PART NO.	TOTAL DELAY (ns)
54A-005	5
54A-010	10
54A-015	15
54A-020	20
54A-025	25
54A-030	30
54A-035	35
54A-040	40
54A-045	45
54A-050	50



Unit: Inch