								ı	REVISI	ONS										
LTR						DESCF	RIPTIO	V			DATE (YR-MO-DA) APP				APPF	ROVED				
А	Add	case ou	utline N	. Make	chang	es to 1.	2.2, 1.3	3, and fi	gure 1.	- ro				98-1	2-17		R. MONNIN			
В	Add	case ou	utline 2.	Make	change	es to 1.	2.2, 1.3	, table l	l, and fig	gure 2.	- ro			99-1	0-21			R. M	ИІИИС	
THE ORIGINAL	L FIRS	T SHEE	T OF T	THIS DI	RAWIN	IG HAS	S BEEN	REPL	ACED.											
	L FIRS	T SHEE	ET OF T	ΓHIS DI	RAWIN	IG HAS	S BEEN	REPL	ACED.			ı	ı						1	
REV	L FIRS	T SHEE	ET OF T	THIS DI	RAWIN	IG HAS	S BEEN	REPL	ACED.											
REV SHEET	L FIRS	T SHEE	ET OF T	THIS DI	RAWIN	NG HAS	S BEEN	REPL	ACED.											
REV SHEET REV	L FIRST	T SHEE	ET OF T	THIS DI	RAWIN	NG HAS	S BEEN	REPL	ACED.											
REV SHEET REV SHEET		T SHEE	T OF T			NG HAS														
REV SHEET REV SHEET REV STATUS		T SHEE	ET OF T	REV	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	NG HAS	В	В	В	В	В	В	В	В	В	В	В	В	В	В
REV SHEET REV SHEET REV STATUS OF SHEETS		T SHEE	ET OF T	REV	/ EET						B 5	B 6	B 7	B 8	B 9	B 10	B 11	B 12	B 13	B 14
REV SHEET REV SHEET REV STATUS OF SHEETS PMIC N/A	NDAF	RD	ET OF T	REV SHE PRE JOS	/ EET SEPH /	D BY A. KER	B 1	В	В	В	5	6	7 SE S		9 Y CE	10 NTEF	11	12 UMB	13	
REV SHEET REV SHEET REV STATUS OF SHEETS PMIC N/A STA MICRO DRA THIS DRAWIT FOR U DEPA	NDAI OCIRG AWIN NG IS A ISE BY RTMEN	RD CUIT IG AVAILA	BLE	REV SHE PRE JOS CHE CH	ZEET EPAREISEPH /	D BY A. KER BY S E. BE	B 1 BY	В	В	B 4	D CROC	efen CIRCU	7 SE S COL	8 UPPL	9 Y CE JS, O	NTEF HIO	11 R COL 43216	UMB	US	
REV SHEET REV SHEET REV STATUS OF SHEETS PMIC N/A STA MICRO DRA THIS DRAWII FOR U	NDAF OCIRG AWIN NG IS A ISE BY RTMEN NCIES	RD CUIT IG AVAILA ALL NTS OF THI	BLE	REV SHE JOS CHE CH	PROVE	D BY A. KER BY S E. BE D BY A. FRY	B 1 BY	B 2	В	B 4	D CROC	efen CIRCU	7 SE S COL	8 UPPL UMBI	9 Y CE JS, O	NTEF HIO	11 R COL 43216	UMB	US	
REV SHEET REV SHEET REV STATUS OF SHEETS PMIC N/A STA MICRO DR/ THIS DRAWII FOR U DEPA AND AGEI DEPARTMEN	NDAF OCIRG AWIN NG IS A ISE BY RTMEN NCIES	RD CUIT IG AVAILA 'ALL NTS OF THI DEFEN	BLE	REV SHE PRE JOS CHE CH	PROVECHAEL	D BY A. KER BY S E. BE D BY A. FRY	B 1 BY SORE	B 2	В	B 4	D CROC	EFEN CIRCUATOR	7 SE S COL	UPPL UMBI LINEA	9 Y CE JS, O	NTEF HIO	11 R COL 43216 LT NE	UMB S	US TIVE DN	
REV SHEET REV SHEET REV STATUS OF SHEETS PMIC N/A STA MICRO DR/ THIS DRAWII FOR U DEPA AND AGEI DEPARTMEN	NDAF OCIRO AWIN NG IS A ISE BY RTMEN NCIES NT OF	RD CUIT IG AVAILA 'ALL NTS OF THI DEFEN	BLE	REV SHE PRE JOS CHE CH	PROVECHAEL	D BY A. KER BY S.E. BE D BY A. FRY APPRO 89-0	B 1 BY SORE	B 2	В	B 4	D CROC GULA	EFEN CIRCUATOR	7 SE S COL JIT, L	UPPL UMBI LINEA ED, M	9 Y CE JS, O	NTEF HIO	11 R COL 43216 LT NE	UMB	US TIVE DN	

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1. SCOPE

- 1.1 Scope. This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.
 - 1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:

Drawing number	Device type (see 1.2.1)	Case outline (see 1.2.2)	Lead finish (see 1.2.3)
*	*	*	*
*	*	*	*
*	*	*	*
*	*	*	*
5962-88747	<u>01</u>	<u>N</u>	<u>X</u>

1.2.1 <u>Device type(s)</u>. The device type(s) identify the circuit function as follows:

Device type	Generic number	<u>Circuit function</u>		
01	7912 A	1.0 A negative regulator, fixed 12-volt		

1.2.2 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style
N	See figure 1	3	Surface mount
Т	See figure 1	3	TO-257, flange mounted case, glass sealed
U	See figure 1	3	TO-257, flange mounted case with an isolated tab, glass sealed
Χ	See figure 1	3	TO-39, can
Υ	See figure 1	2	TO-3, can
Z	MBFM4-P2	2	TO-66, can
2	CQCC1-N20	20	Square leadless chip carrier

- 1.2.3 Lead finish. The lead finish is as specified in MIL-PRF-38535, appendix A.
- 1.3 Absolute maximum ratings.

Input voltage:

Operating or input shorted to ground-35 V dc Transient......-50 V dc <u>1</u>/ Storage temperature range.....-65°C to +150°C Lead temperature (soldering, 10 seconds).....+300°C Power dissipation (P_D):

 $T_{C} = +25^{\circ}C$: Cases X and 2 2 W

The 50-volt input rating referes to the ability of the regulator to withstand high line or transient conditions without damage. Since the regulator's maximum current capability is reduced, the output may fall out of regulation at high input voltages under nominal loading.

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1.3 Absolute maximum ratings - Continued.

Power dissipation (P_D):

 $T_A = +25^{\circ}C$:

Thermal resistance, junction-to-case (θ_{JC}):

 Cases N and T
 3.5°C/W

 Case U
 4.2°C/W

 Case X
 15°C/W

 Case Y
 3°C/W

 Case Z
 6°C/W

 Case 2
 See MIL-STD-1835

Thermal resistance, junction-to-ambient (θ_{JA}) :

1.4 Recommended operating conditions.

Input voltage range (V_{IN}) -14.5 V dc to -27 V dc Ambient operating temperature range (T_A) -55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 <u>Government specification, standards, and handbooks</u>. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DoDISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

STANDARDS

DEPARTMENT OF DEFENSE

MIL-STD-883 - Test Method Standard Microcircuits.

MIL-STD-973 - Configuration Management.

MIL-STD-1835 - Interface Standard For Microcircuit Case Outlines.

2/ The device is protected by a thermal shutdown circuit which is designed to turn off the output transistor whenever the device junction temperature is in excess of +150°C.

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HANDBOOKS

DEPARTMENT OF DEFENSE

MIL-HDBK-103 - List of Standard Microcircuit Drawings (SMD's).

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Unless otherwise indicated, copies of the specification, standards, and handbooks are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 <u>Order of precedence</u>. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

- 3.1 <u>Item requirements</u>. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used.
- 3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535, appendix A and herein.
 - 3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.2 herein and figure 1.
 - 3.2.2 Terminal connections. The terminal connections shall be specified on figure 2.
- 3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.
- 3.4 <u>Electrical test requirements</u>. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.
- 3.5 <u>Marking</u>. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in MIL-HDBK-103 (see 6.6 herein). For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device.
- 3.6 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DSCC-VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.
- 3.7 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.
- 3.8 Notification of change. Notification of change to DSCC-VA shall be required in accordance with MIL-PRF-38535, appendix A.
- 3.9 <u>Verification and review</u>. DSCC, DSCC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

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TABLE I.	Electrical	performance	characteristics.

Test	Symbol	Conditions $\underline{1}/$ -55°C \leq T _A \leq +125°C unless otherwise specified	Group A subgroups	Device type	Lim	nits	Unit
					Min	Max	1
Output voltage	V _{OUT}	T _A = +25°C	1	01	-11.88	-12.12	٧
		V _{IN} = -14.5 V to -27 V <u>2</u> /	1,2,3		-11.64	-12.36	
Line regulation 3/4/	V _{RLINE}	V _{IN} = -14.5 V to -27 V	1	01		20	mV
			2,3			50	
		V _{IN} = -16 V to -22 V	1			10	
			2,3			30	
Load regulation 3/	V _{RLOAD}	l _O = 5.0 mA to 1.5 A <u>5</u> /	1	01		32	mV
			2,3			60	=
		l _O = 250 mA to 750 mA <u>5</u> /	1			16	
			2,3			30	
		I _O = 5 mA to 500 mA <u>6</u> /	1			30	
			2,3			60	=
Standby current drain	Isco		1	01		3.5	mA
			2,3			4.0	1
Standby current drain change with line	ΔI_{SCD} (line)	V _{IN} = -14.5 V to -27 V	1,2,3	01		0.8	mA
Standby current drain change with load	Δl _{SCD} (load)	l _O = 5.0 mA to 1000 mA	1,2,3	01		0.5	mA
Dropout voltage	VDO	$I_{O} = 1.0 \text{ A}, \ \underline{5}/$ $\Delta V_{OUT} = 100 \text{ mV}$	1,2,3	01		1.8	V
		$I_0 = 500 \text{ mA}, \ \underline{6}/$ $\Delta V_{OUT} = 100 \text{ mV}$				1.8	
Peak output current 2/	I _{O(PK)}	T _A = +25°C <u>5</u> /	1	01	1.5	3.3	Α
		T _A = +25°C <u>6</u> /	1		0.5	1.7	1

See footnotes at end of table.

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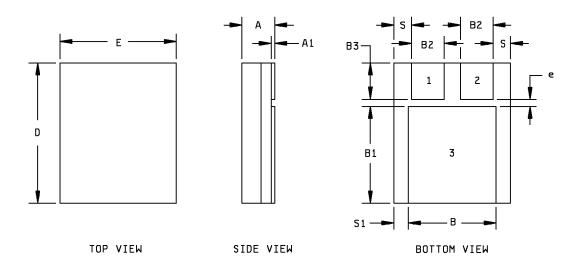
TABLE I. <u>Electrical performance characteristics</u> - Continued.

Test	Symbol	Conditions $\underline{1}/$ -55°C \leq T _A \leq +125°C unless otherwise specified	Group A subgroups	Device type	Lim	its	Unit
					Min	Max	
Short circuit current 7/	los	V _{IN} = -35 V <u>5</u> /	1	01		1.2	A
			2,3			2.8	
		V _{IN} = -35 V <u>6</u> /	1			0.7	
			2,3			2.0	
Ripple rejection	ΔV _{IN} / ΔV _{OUT}	f = 120 Hz, ΔV _{IN} = 10 V	4	01	56		dB
			5,6 <u>8</u> /		53		
Output noise voltage <u>8</u> /	No	f = 10 Hz to 100 kHz, T _A = +25°C	7	01		40	μV / Vrms
Long term stability <u>8</u> /	ΔV _{OUT} /	t = 1,000 hours, T _A = +25°C	7	01		120	mV

- 1/ Unless otherwise specified, V_{IN} = -19 V and I_O = 500 mA for cases N, T, U, Y, and Z. V_{IN} = -19 V and I_O = 100 mA for cases X and 2. Maximum test current for cases X and 2 is 500 mA.
- $\underline{2}/$ For cases X and 2: $I_O = 5$ mA to 500 mA, $P \le 2$ W. For case Y: $I_O = 5$ mA to 1.0 A, $P \le 20$ W. For cases N, T, U, and Z: $I_O = 5$ mA to 1.0 A, $P \le 15$ W.
- 3/ All measurements except output noise voltage and ripple rejection are made at constant junction temperature and with low duty cycle.
- 4/ Minimum load current for full line regulation is 5.0 mA.
- $\underline{5}/$ For cases N, T, U, Y, and Z only.
- 6/ For cases X and 2 only.
- $\underline{7}/$ Short circuit protection is only assured up to V_{IN} = -35 V.
- 8/ Guaranteed, if not tested, to the limits specified.

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Case outline N



Symbol	Inc	hes	Millime	eters
	Min	Мах	Min	Max
Α	.130	.150	3.30	3.81
A1	.010	.020	0.25	0.51
В	.370	.380	9.40	9.65
B1	.410	.420	10.41	10.67
B2	.135	.145	3.43	3.68
B3	.152	.162	3.86	4.11
D	.620	.630	15.75	16.00
E	.445	.455	11.30	11.55
е	.030		0.76	
S	.045	.055	1.14	1.40
S1	.032	.042	0.81	1.07

<u>PINOUT</u>

- 1 GROUND
- 2 OUTPUT
- 3 INPUT

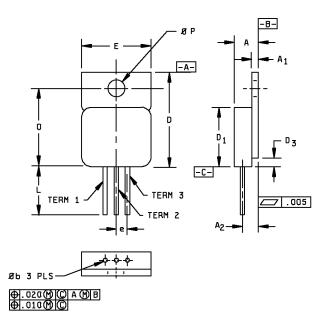
NOTE:

The U.S. government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.

FIGURE 1. Case outlines.

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Case outlines T and U



Letter	Inches		Millim	neters
	Min	Max	Min	Max
Α	.190	.200	4.83	5.08
A1	.035	.045	0.89	1.14
A 2	.120	BSC	3.05	BSC
φb	.025	.035	0.64	0.89
D	.645	.665	16.38	16.89
D1	.410	.430	10.41	10.92
D3	.000	.065	0.00	1.65
е	.100	BSC 2.54 BSC		BSC
Е	.410	.422	10.41	10.71
L	.500	.750	12.70	19.05
0	.527	.537	13.39	16.64
φP	.140	.150	3.56	3.81

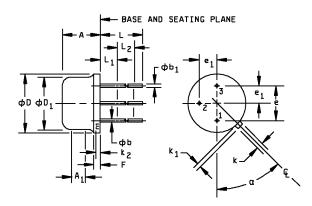
NOTE:

The U.S. government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.

FIGURE 1. <u>Case outlines</u>. - Continued.

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Case outline X



Letter	Inc	hes	Millimeters		Notes
	Min	Max	Min	Max	
Α	.165	.185	4.19	4.70	
A1	.100) TP	2.54	. T.P.	
φb	.016	.019	0.41	0.48	2
φb1	.016	.021	0.41	0.53	2
φD	.335	.370	8.51	9.40	
φD1	.305	.335	7.75	8.51	
е	.200	T.P.	5.08 T.P.		4
e1	.100	T.P.	2.54 T.P.		4
F		.050		1.27	
k	.028	.034	0.71	0.86	
k1	.029	.045	0.74	1.14	3
k2	.009	.041	0.23	1.04	
L	.500		12.70		
L1		.050		1.27	
L2	.250		6.35		
α	45°	T.P.	45°	T.P.	

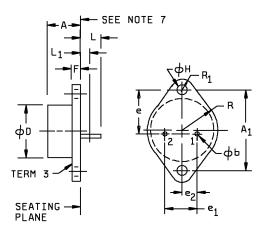
NOTES:

- The U.S. government preferred system of measurement is the metric SI system. However, since this item was
 originally designed using inch-pound units of measurement, in the event of conflict between the metric and
 inch-pound units, the inch-pound units shall take precedence.
- 2. (All leads) \$\phi\$ applies between the L1 and L2. \$\phi\$1 applies between the L2 and .500 inch (12.70 mm) from the reference plane. Diameter is uncontrolled in L1 and beyond .500 inch (12.70 mm) from the reference plane.
- 3. Measured from the maximum diameter of the product.
- 4. Leads having a maximum diameter of .019 inch (0.48 mm) measured in gauging plane .054 inch (1.37 mm) + .001 inch (0.03 mm) .000 inch (0.00 mm) below the base plane of the product shall be within .007 inch (0.18 mm) of their true-position relative to a maximum width tab.
- 5. The product may be measured by direct methods or by gauge.

FIGURE 1. Case outlines - Continued.

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Case outline Y



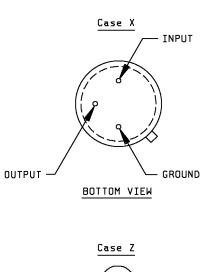
Letter	Inc	hes	Millimeters		Notes
	Min	Max	Min	Max	
Α	.250	.450	6.35	11.43	
A1	1.177	1.197	29.90	30.40	
фЬ	.038	.043	0.97	1.09	2, 6
φD		.875		22.22	
e	.655	.675	16.64	17.14	4
e1	.420	.440	10.67	11.16	4
e2	.205	.225	5.21	5.72	
F	.060	.135	1.52	3.43	
φН	.151	.161	3.84	4.09	4, 5
L	.312	.500	7.92	12.70	3
L1		.050		1.27	2, 3
R	.495	.525	12.57	13.34	
R1	.131	.188	3.33	4.78	

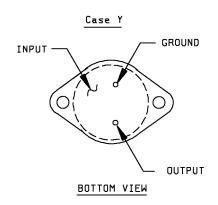
NOTES:

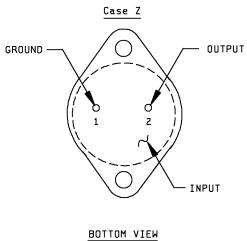
- 1. The U.S. government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.
- 2. (Two leads) \$\phi\$ applies between the L1 and .500 inch (12.70 mm) from the seating plane. Diameter is uncontrolled in L1 and beyond .500 inch (12.70 mm) from the seating plane.
- 3. Two leads.
- 4. Two holes.
- 5. Two holes located at true position within diameter .010 inch (0.25 mm).
- 6. Leads having a maximum diameter of .043 inch (1.09 mm) measured in gauging plane .054 inch (1.37 mm) + .001 inch (0.03 mm) .000 inch (0.00 mm) below the seating plane shall be located at true-position within diameter .014 inch (0.36 mm)
- 7. The mounting surface of the header shall be flat to convex within .003 inch (0.08 mm) inside a .930 inch (23.62 mm) diameter circle on the center of the header and flat to convex within .006 inch (0.15 mm) overall.

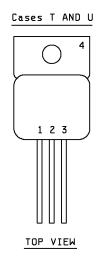
FIGURE 1. Case outlines - Continued.

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Case I	<u>Case U</u>
1 - GROUND 2 - INPUT 3 - OUTPUT 4 - INPUT	1 - GROUND 2 - INPUT 3 - OUTPUT 4 - NC
T 1111 O1	T 110

NOTE: For case N see figure 1.

FIGURE 2. <u>Terminal connections</u>.

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Device type	01
Case outline	2
Terminal	Terminal symbol
number	
1	NC
2	V _{IN}
3	NC
4	Vout
5	Vout
6	NC
7	V _{OUT}
8	NC
9	NC
10	NC
11	NC
12	NC
13	NC
14	NC
15	GROUND
16	NC
17	GROUND
18	NC
19	NC
20	V _{IN}

NC = No connection

FIGURE 2. <u>Terminal connections</u>. – Continued.

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4. QUALITY ASSURANCE PROVISIONS

- 4.1 <u>Sampling and inspection</u>. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.
- 4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:
 - a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
 - (2) $T_A = +125^{\circ}C$, minimum.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
- 4.3 <u>Quality conformance inspection</u>. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.
 - 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.
 - 4.3.2 Groups C and D inspections.
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
 - (2) $T_A = +125$ °C, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.
 - 5. PACKAGING
 - 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1*,2,3,4
Group A test requirements (method 5005)	1,2,3,4,5**,6**,7**
Groups C and D end-point electrical parameters (method 5005)	1

^{*} PDA applies to subgroup 1.

6. NOTES

- 6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.
- 6.2 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- 6.3 <u>Configuration control of SMD's</u>. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-993 using DD Form 1692, Engineering Change Proposal.
- 6.4 <u>Record of users</u>. Military and industrial users shall inform Defense Supply Center Columbus when a system application requires configuration control and the applicable SMD. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0525.
- 6.5 <u>Comments</u>. Comments on this drawing should be directed to DSCC-VA, Columbus, Ohio 43216-5000, or telephone (614) 692-0694.
- 6.6 <u>Approved sources of supply</u>. Approved sources of supply are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DSCC-VA.

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^{**} Subgroups 5, 6, and 7 if not tested, shall be guaranteed to the limits specified in table I.

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DATE: 99-10-21

Approved sources of supply for SMD 5962-88747 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535.

Standard microcircuit drawing PIN <u>1</u> /	Vendor CAGE number	Vendor similar PIN <u>2</u> /
5962-8874701NA	69210	OM1912NM/883B
5962-8874701TA	U3158	IP7912AG/883B
	69210	OM7912AH/883B
5962-8874701UA	U3158	IP7912AIG/883B
	27851	FM912S7
	34333	SG7912AIG/883B
	69210	OM7912AIH/883B
5962-8874701XA	U3158	IP79M12AH/883B
	34333	SG7912AT/883B
5962-8874701YA	U3158	IP7912AK/883B
	34333	SG7912AK/883B
	69210	OM1912NK/883B
5962-8874701ZA	U3158	IP7912AR/883B
	34333	SG7912AR/883B

STANDARD MICROCIRCUIT DRAWING BULLETIN - CONTINUED

Standard microcircuit drawing PIN <u>1</u> /	Vendor CAGE number	Vendor similar PIN <u>2</u> /
5962-88747012A	34333	SG7912AL/883B
	69210	OM1912N2/883B

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- <u>2</u>/ <u>Caution</u>. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number	Vendor name and address
U3158	Semelab PLC Coventry Road, Lutterworth Leicestershire LE174JB United Kingdom Point of contact: Martinez & Associates 19 Palham Island Road Wayland, MA 01778
27851	Film Microelectronics, Incorporated 530 Turnpike Street North Andover, MA 01845
34333	Linfinity Microelectronics Incorporated 11861 Western Avenue Garden Grove, CA 92841
69210	Omnirel Corporation 205 Crawford Street Leominster, MA 01453-2353

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.