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PMIC N/A STANDARDIZED MILITARY DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE AMSC N/A	PREPARED BY <i>Tim H. Noh</i> CHECKED BY <i>Ray Monnin</i> APPROVED BY <i>[Signature]</i> DRAWING APPROVAL DATE 25 APRIL 1989 REVISION LEVEL	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444 MICROCIRCUITS, DIGITAL ADVANCED CMOS, 8-INPUT MULTIPLEXER, TTL COMPATIBLE INPUTS, THREE-STATE, MONOLITHIC SILICON <table style="width: 100%;"> <tr> <td style="width: 15%;">SIZE A</td> <td style="width: 35%;">CAGE CODE 67268</td> <td style="width: 50%;">5962-89599</td> </tr> </table>	SIZE A	CAGE CODE 67268	5962-89599
SIZE A	CAGE CODE 67268	5962-89599			
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• U.S. GOVERNMENT PRINTING OFFICE: 1987 — 748-129/60911

5962-E1302

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:

5962-89599	01	E	X
Drawing number	Device type (1.2.1)	Case outline (1.2.2)	Lead finish per MIL-M-38510

1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	54ACT251	8-input multiplexer with three state outputs, TTL compatible inputs

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
E	D-2 (16-lead, .840" x .310" x .200"), dual-in-line package
F	F-5 (16-lead, .440" x .285" x .085"), flat package
2	C-2 (20-terminal, .358" x .358" x .100"), square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage range (V_{CC})	1/ - - - - -	-0.5 V dc to +6.0 V dc
DC input voltage	1/ - - - - -	-0.5 V dc to V_{CC} + 0.5 V dc
DC output voltage	1/ - - - - -	-0.5 V dc to V_{CC} + 0.5 V dc
DC input diode current	- - - - -	±20 mA
DC output diode current (per pin)	- - - - -	±50 mA
DC V_{CC} or GND current	- - - - -	±100 mA
Storage temperature range	- - - - -	-65°C to +150°C
Maximum power dissipation (P_D)	- - - - -	500 mW
Lead temperature (soldering, 10 seconds)	- - - - -	+300°C
Thermal resistance, junction-to-case (θ_{JC})	- - - - -	See MIL-M-38510, appendix C
Junction temperature (T_J)	2/ - - - - -	+175°C

1.4 Recommended operating conditions.

Supply voltage (V_{CC})	- - - - -	4.5 V dc to 5.5 V dc
Input voltage	- - - - -	0.0 V dc to V_{CC}
Output voltage	- - - - -	0.0 V dc to V_{CC}
Case operating temperature range (T_C)	- - - - -	-55°C to +125°C
Input rise or fall times:		
V_{CC} = 4.5	- - - - -	0.0 to 24 ns (10-90%, 10ns/V)
V_{CC} = 5.5 V	- - - - -	0.0 to 20 ns (10-90%, 8ns/V)

1/ Unless otherwise specified, all voltages are referenced to GND.

2/ Maximum junction temperature shall not be exceeded except for allowable short duration burn-in screening conditions in accordance with method 5004 of MIL-STD-883.

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2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Test circuits and switching waveforms. The test circuits and switching waveforms shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full case operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified		Group A subgroups	Limits		Unit
					Min	Max	
High level output voltage	V _{OH}	V _{IN} = 2.0 V or 0.8 V, I _{OH} = -50 μA <u>1/</u>	V _{CC} = 4.5 V	1, 2, 3	4.4		V
			V _{CC} = 5.5 V		5.4		
		V _{IN} = 2.0 V or 0.8 V, I _{OH} = -24 mA <u>1/</u>	V _{CC} = 4.5 V		3.7		
			V _{CC} = 5.5 V		4.7		
		V _{IN} = 2.0 V or 0.8 V, I _{OH} = -50 mA <u>1/</u>	V _{CC} = 5.5 V		3.85		
Low level output voltage	V _{OL}	V _{IN} = 2.0 V or 0.8 V, I _{OL} = 50 μA <u>1/</u>	V _{CC} = 4.5 V	1, 2, 3		0.1	V
			V _{CC} = 5.5 V			0.1	
		V _{IN} = 2.0 V or 0.8 V, I _{OL} = 24 mA <u>1/</u>	V _{CC} = 4.5 V			0.5	
			V _{CC} = 5.5 V			0.5	
		V _{IN} = 2.0 V or 0.8 V, I _{OL} = 50 mA <u>1/</u>	V _{CC} = 5.5 V			1.65	
High level input voltage	V _{IH}	<u>2/</u>	V _{CC} = 4.5 V	1, 2, 3	2.0		V
			V _{CC} = 5.5 V		2.0		
Low level input voltage	V _{IL}	<u>2/</u>	V _{CC} = 4.5 V	1, 2, 3		0.8	V
			V _{CC} = 5.5 V			0.8	
Input leakage current	I _{IL}	V _{IN} = 0.0 V	V _{CC} = 5.5 V	1, 2, 3		-1.0	μA
	I _{IH}	V _{IN} = 5.5 V				1.0	

See footnotes at end of table.

**STANDARDIZED
MILITARY DRAWING**

DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO 45444

SIZE
A

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TABLE 1. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Maximum I _{CC} /TTL inputs High	ΔI _{CC}	V _{CC} = 5.5 V, V _{IH} = V _{CC} - 2.1 V, V _{IL} = 0.0 V	1, 2, 3		1.6	mA
Quiescent current	I _{CCH}	V _{IN} = V _{CC} or GND, V _{CC} = 5.5 V	1, 2, 3		160	μA
	I _{CCL}				160	
	I _{CCZ}				160	
Off state output leakage current	I _{OZH}	V _{IN} = V _{CC} or GND, V _{CC} = 5.5 V V _{OUT} = 5.5 V or 0.0 V	1, 2, 3		10.0	μA
	I _{OZL}				-10.0	
Input capacitance	C _{IN}	See 4.3.1c	4		8.0	pF
Power dissipation capacitance	C _{PD}	See 4.3.1c 3/	4		110	pF
Functional tests		Tested at V _{CC} = 4.5 V and repeated at V _{CC} = 5.5 V, see 4.3.1d	7, 8			
Propagation delay time, In to Z	t _{PHL1}	C _L = 50 pF, R _L = 500Ω, V _{CC} = 4.5 V See figure 3 4/	9 10, 11	1.0 1.0	11.5 13.5	ns
	t _{PLH1}		9 10, 11	1.0 1.0	11.5 13.5	
Propagation delay time, In to Z	t _{PHL2}		9 10, 11	1.0 1.0	12.5 15.0	ns
	t _{PLH2}		9 10, 11	1.0 1.0	12.0 14.0	
Propagation delay time, Sn to Z	t _{PHL3}		9 10, 11	1.0 1.0	14.5 18.0	ns
	t _{PLH3}		9 10, 11	1.0 1.0	14.5 18.0	

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Propagation delay time, S _n to Z	t _{PHL4}	C _L = 50 pF, R _L = 500Ω, V _{CC} = 4.5 V See figure 3 4/	9	1.0	15.5	ns
			10, 11	1.0	19.5	
	t _{PLH4}		9	1.0	14.5	
			10, 11	1.0	18.5	
Output enable time, OE to Z	t _{pZH1}		9	1.0	8.5	ns
			10, 11	1.0	10.0	
	t _{pZL1}		9	1.0	8.0	
			10, 11	1.0	9.5	
Output disable time, OE to Z	t _{pHZ1}		9	1.0	11.0	ns
			10, 11	1.0	12.5	
	t _{pLZ1}		9	1.0	8.5	
			10, 11	1.0	9.5	
Output enable time, OE to Z	t _{pZH2}		9	1.0	8.5	ns
			10, 11	1.0	10.0	
	t _{pZL2}		9	1.0	8.5	
			10, 11	1.0	10.0	
Output disable time, OE to Z	t _{pHZ2}		9	1.0	12.0	ns
			10, 11	1.0	13.5	
	t _{pLZ2}		9	1.0	7.5	
			10, 11	1.0	8.5	

1/ V_{OH} and V_{OL} tests shall be tested at V_{CC} = 4.5 V. V_{OH} and V_{OL} are guaranteed, if not tested, for V_{CC} = 5.5 V. Limits shown apply to operation at V_{CC} = 5.0 V ± 0.5 V. Transmission driving tests are performed at V_{CC} = 5.5 V with a 2 ms duration maximum.

2/ V_{IH} and V_{IL} tests are not required, and shall be applied as forcing functions for V_{OH} and V_{OL} tests.

3/ Power dissipation capacitance (C_{PD}), determines the dynamic power consumption, P_D = (C_{PD} + C_L) (V_{CC} × V_{CC}) f + I_{CC} (V_{CC}), and the dynamic current consumption (I_S) is, I_S = (C_{PD} + C_L) V_{CC} f + I_{CC}.

4/ AC limits at V_{CC} = 5.5 V are equal to limits at V_{CC} = 4.5 V and guaranteed by testing at V_{CC} = 4.5 V. Minimum ac guaranteed for V_{CC} = 5.5 V by guardbanding V_{CC} = 4.5 V limits to 1.5 ns (minimum).

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Case outlines	E and F	2
Terminal number	Terminal symbol	
1	I3	NC
2	I2	I3
3	I1	I2
4	I0	I1
5	Z	I0
6	Z	NC
7	OE	Z
8	GND	Z
9	S2	OE
10	S1	GND
11	S0	NC
12	I7	S2
13	I6	S1
14	I5	S0
15	I4	I7
16	VCC	NC
17	---	I6
18	---	I5
19	---	I4
20	---	VCC

NC = No connection

FIGURE 1. Terminal connections.

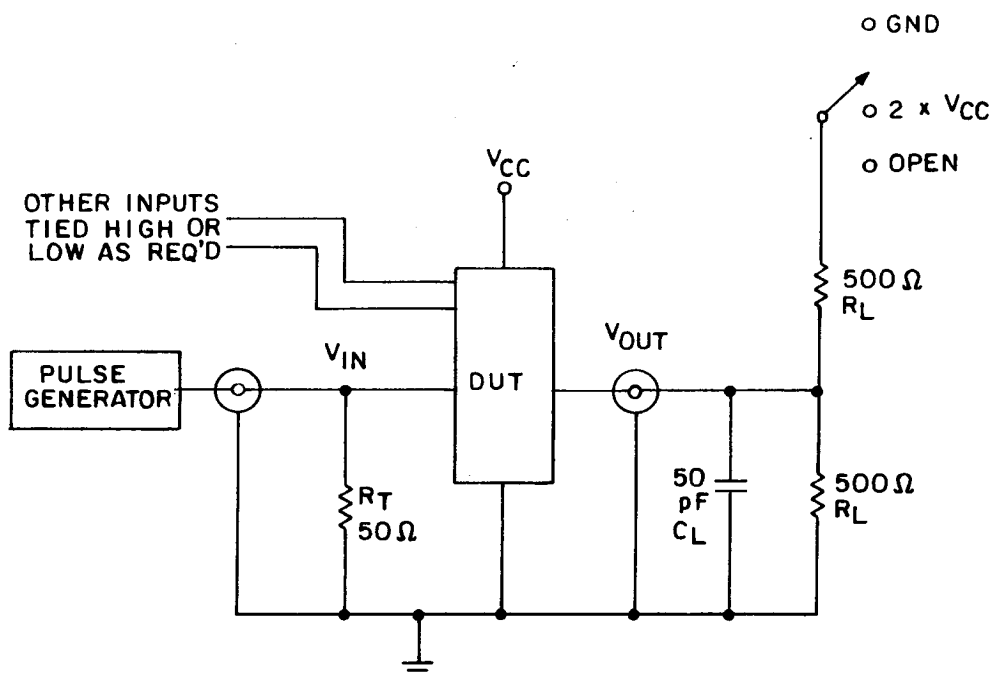
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Inputs				Outputs	
$\overline{OE}$	S2	S1	S0	Z	Z
H	X	X	X	Z	Z
L	L	L	L	<u>Y0</u>	10
L	L	L	H	<u>Y1</u>	11
L	L	H	L	<u>Y2</u>	12
L	L	H	H	<u>Y3</u>	13
L	H	L	L	<u>Y4</u>	14
L	H	L	H	<u>Y5</u>	15
L	H	H	L	<u>Y6</u>	16
L	H	H	H	<u>Y7</u>	17

H = High voltage level
 L = Low voltage level
 X = Irrelevant
 Z = High impedance state

FIGURE 2. Truth table.

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Preferred method

GND = t_{pZH} , t_{pHZ}
 $2 \times V_{CC}$ = t_{pLZ} , t_{pZL}
 Open = t_{pLH} , t_{pHL}

Alternate method

Open = t_{pHZ} , t_{pZH} , t_{pLH} , t_{pHL}
 $2 \times V_{CC}$ = t_{pLZ} , t_{pZL}

NOTES:

1. C_L includes probe and jig capacitance.
2. R_T = termination resistance and should be equal to Z_{OUT} of the pulse generator.
3. $t_r = t_f = 2.5\ \text{ns}$ (10% to 90%) unless otherwise specified.

FIGURE 3. Test circuit and switching waveforms.

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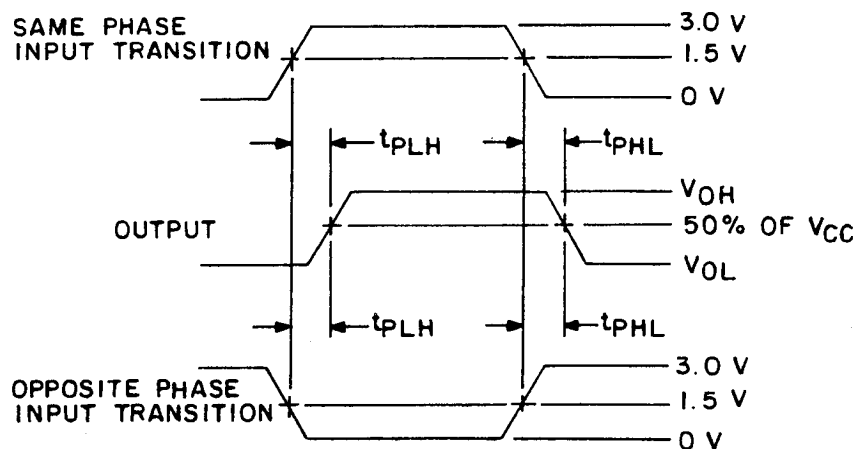
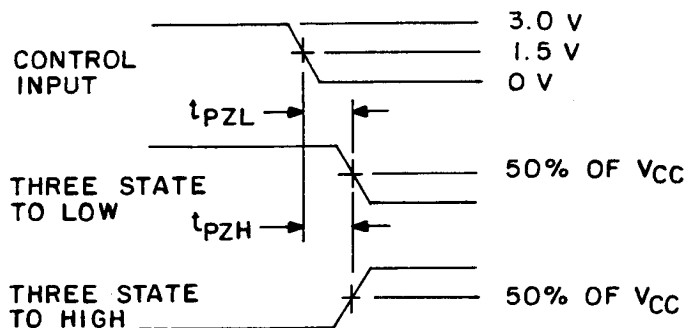


FIGURE 3. Test circuit and switching waveforms - Continued.

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ENABLE TIMES



DISABLE TIMES

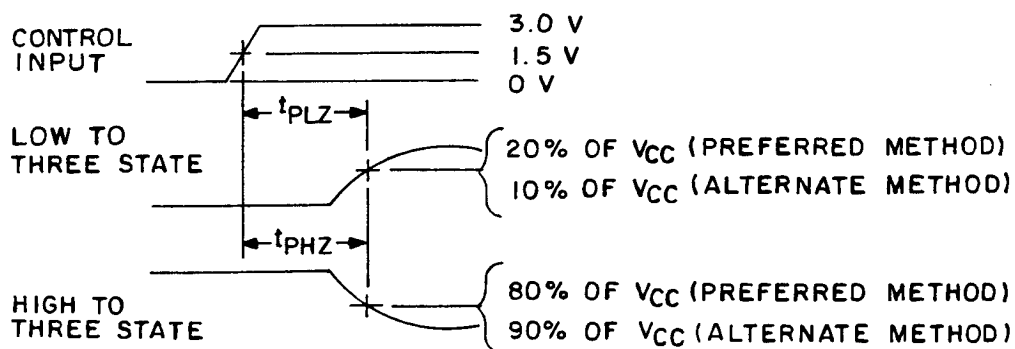


FIGURE 3. Test circuit and switching waveforms - Continued.

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3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2) $T_A = +125^{\circ}\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 5 and 6 in table I, method 5005 of MIL-STD-883, shall be omitted.

c. Subgroup 4 (C_{IN} and C_{PD} measurements) shall be measured only for the initial test and after process or design changes which may affect capacitance. Test all applicable pins on 5 devices with zero failures.

d. Subgroups 7 and 8 tests shall verify the truth table as specified on figure 2 herein.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test condition, method 1005 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2) $T_A = +125^{\circ}\text{C}$, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*,2,3,7,8,9, 10,11
Group A test requirements (method 5005)	1,2,3,4,7,8, 9,10,11
Groups C and D end-point electrical parameters (method 5005)	1,2,3

* PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved sources of supply. Approved sources of supply are listed herein. Additional sources will be added as they become available. The vendors listed herein have agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1/</u>
5962-8959901EX	27014	54ACT251DMQB
5962-8959901FX	27014	54ACT251FMQB
5962-89599012X	27014	54ACT251LMQB

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE
number

27014

Vendor name
and address

National Semiconductor
333 Western Avenue
South Portland, ME 04106

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