

SERIES 603 5-TAPS 14 PIN FAST LOGIC DELAY MODULES

- 14 Pin DIP and Surface Mount Packages • TTL and DTL Compatible
- 5 Equally Spaced Taps

Specifications:

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 100 μ A Max
- Logic 0 Input Current : -1.6 mA Max
- Logic 1 Volt Out : 2.5V Min
- Logic 0 Volt Out : 0.5V Max
- Logic 1 Fan-Out : 25 Max
- Logic 0 Fan-Out : 12.5 Max
- Output Rise Time⁽²⁾ : 2NSEC TYP
- Operating Temp Range : 0°C to 70°C
- Temperature Coefficient : 100 PPM/°C

Input Test Conditions:

- Input Pulse Voltage : 3.2V
- Input Rise Time : ≤ 2 NS
- Pulse Width : 40% of Total Delay
- Supply Current ICCL* : 32 mA
- ICCH* : 7 mA

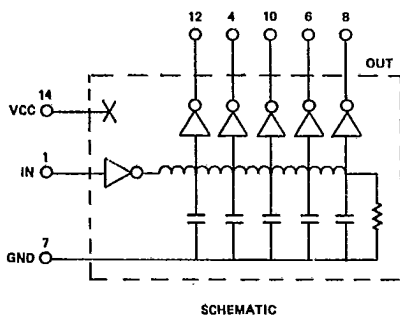
Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number			Total Delay Nanosecond (1)	Tap To Tap Delay NSEC (1)
Type D	Type W	Type J		
603-4D*	603-4W*	603-4J*	4 $\pm$ 1	1 $\pm$ 0.5
603-5D*	603-5W*	603-5J*	5 $\pm$ 1	1.2 $\pm$ 0.5
603-6D*	603-6W*	603-6J*	6 $\pm$ 1	1.5 $\pm$ 0.5
603-8D*	603-8W*	603-8J*	8 $\pm$ 2	2 $\pm$ 0.5
603-10D*	603-10W*	603-10J*	10 $\pm$ 2	2.5 $\pm$ 0.5
603-12D*	603-12W*	603-12J*	12 $\pm$ 2	3 $\pm$ 0.5
603-16D*	603-16W*	603-16J*	16 $\pm$ 2	4 $\pm$ 0.6
603-25D	603-25W	603-25J	25 $\pm$ 2	5 $\pm$ 0.8
603-30D	603-30W	603-30J	30 $\pm$ 2	6 $\pm$ 1
603-35D	603-35W	603-35J	35 $\pm$ 2	7 $\pm$ 1
603-40D	603-40W	603-40J	40 $\pm$ 2	8 $\pm$ 1
603-45D	603-45W	603-45J	45 $\pm$ 2.2	9 $\pm$ 1
603-50D	603-50W	603-50J	50 $\pm$ 2.5	10 $\pm$ 2
603-60D	603-60W	603-60J	60 $\pm$ 3	12 $\pm$ 2
603-75D	603-75W	603-75J	75 $\pm$ 3.5	15 $\pm$ 2
603-100D	603-100W	603-100J	100 $\pm$ 5.0	20 $\pm$ 2
603-125D	603-125W	603-125J	125 $\pm$ 5.5	25 $\pm$ 2
603-150D	603-150W	603-150J	150 $\pm$ 6.0	30 $\pm$ 3
603-175D	603-175W	603-175J	175 $\pm$ 8.7	35 $\pm$ 3.5
603-200D	603-200W	603-200J	200 $\pm$ 10	40 $\pm$ 4
603-250D	603-250W	603-250J	250 $\pm$ 12.5	50 $\pm$ 5
603-300D	603-300W	603-300J	300 $\pm$ 15	60 $\pm$ 6
603-350D	603-350W	603-350J	350 $\pm$ 17.5	70 $\pm$ 7
603-400D	603-400W	603-400J	400 $\pm$ 20	80 $\pm$ 8
603-500D	603-500W	603-500J	500 $\pm$ 25	100 $\pm$ 10

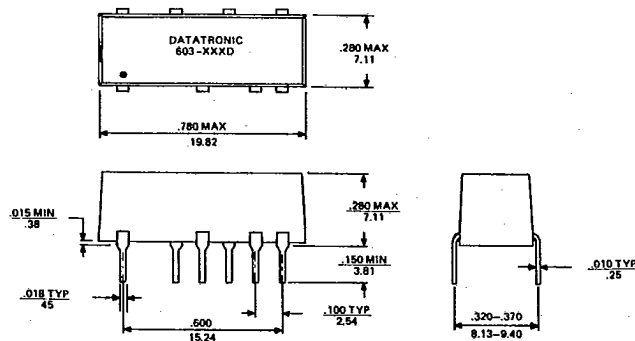
Note: (1) Measured at 1.5v level leading edge.

(2) Measured from 0.75v to 2.4v.

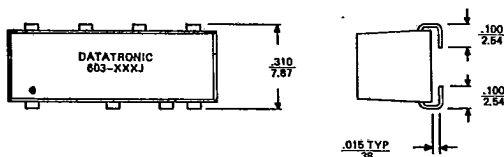
*Time delay measured with respect to 1st tap.



TYPE D



TYPE J



TYPE W

