

SERIES 612 14 PIN TRIPLE FAST LOGIC DELAY MODULES

- 14 Pin DIP Auto-Insertable • Three Independent Equal Delay Units Per Package
- TTL and DTL Compatible

Specifications:

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 100 μ A Max
- Logic 0 Input Current : -1.6mA Max
- Logic 1 Volt Out : 2.5V Min
- Logic 0 Volt Out : 0.5V Max
- Logic 1 Fan-Out : 25 Max
- Logic 0 Fan-Out : 12.5 Max
- Output Rise Time⁽²⁾ : 2NSEC TYP.
- Operating Temp Range : 0°C to 70°C
- Temperature Coefficient : 100 PPM/°C

Input Test Conditions:

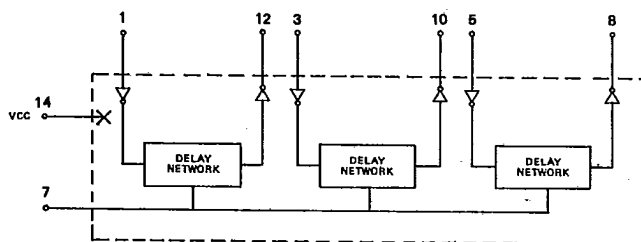
- Input Pulse Voltage : 3.2V
- Input Rise Time : ≤ 2 NS
- Pulse Width : 40% of Total Delay
- Supply Current ICCL* : 74mA
- ICCH* : 14mA



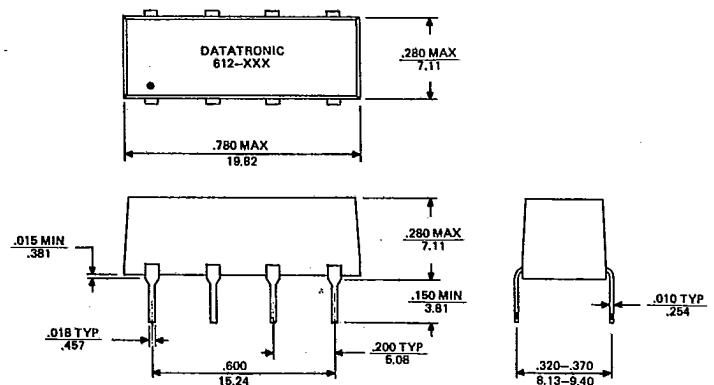
Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number	Total Delay Nanosecond (1)
612-5	5 $\pm$ 1
612-6	6 $\pm$ 1
612-8	8 $\pm$ 2
612-10	10 $\pm$ 2
612-12	12 $\pm$ 2
612-16	16 $\pm$ 2
612-25	25 $\pm$ 2
612-30	30 $\pm$ 2
612-35	35 $\pm$ 2
612-40	40 $\pm$ 2
612-45	45 $\pm$ 2.2
612-50	50 $\pm$ 2.5
612-60	60 $\pm$ 3
612-75	75 $\pm$ 3.5
612-100	100 $\pm$ 5.0
612-125	125 $\pm$ 5.5
612-150	150 $\pm$ 6.0
612-175	175 $\pm$ 8.7
612-200	200 $\pm$ 10
612-250	250 $\pm$ 12.5

Note: (1) Measured at 1.5v level leading edge.
(2) Measured from 0.75v to 2.4v.



SCHEMATIC



INCHES .XXX $\pm$.010
MILLIMETERS .XX $\pm$.25