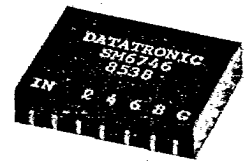


SERIES 627 10-TAPS 30 PIN H-CMOS DIGITAL DELAY MODULES

- 30 Pin Chip Carrier • Designed For Surface Mounting • H-Cmos Logic
- 10 Equally Spaced Taps

Specifications:

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Voltage : 3.2v
- Logic 0 Input Voltage : 0.9v
- Logic 1 Output Voltage : 4.5v
- Logic 0 Output Voltage : 0.1v
- Input Current : 1 μ A Max
- Supply Current : ICCH = 5mA, ICCL = 20 μ A
- Fan-Out : 10 LSTTL Loads Min
- Rise-Time : 8NS.TYP. (measured from 0.8v to 2.0v)
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 300 PPM/°C

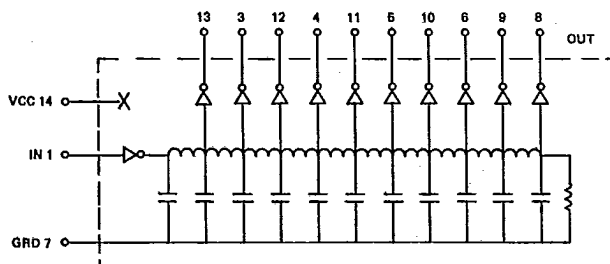


Electrical Specifications at 25°C (Measured with no Loads on Taps)

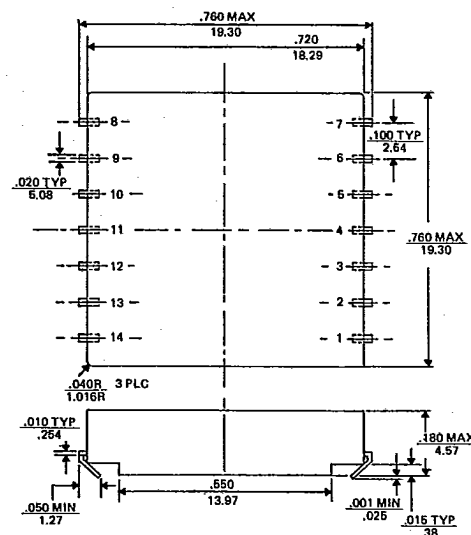
Part Number	Total Delay Nanosecond (1)	Tap to Tap Delay NSEC (1)
627-10*	9.0 $\pm$ 1.0	1.0 $\pm$ 0.4
627-20*	18.0 $\pm$ 2.0	2.0 $\pm$ 0.5
627-25*	22.5 $\pm$ 2.0	2.5 $\pm$ 0.7
627-50*	45.0 $\pm$ 2.0	5.0 $\pm$ 1.0
627-60*	54.0 $\pm$ 3.0	6.0 $\pm$ 1.0
627-70*	63.0 $\pm$ 3.5	7.0 $\pm$ 1.0
627-75*	67.5 $\pm$ 3.5	7.5 $\pm$ 1.0
627-100	100.0 $\pm$ 5.0	10.0 $\pm$ 2.0
627-125	125.0 $\pm$ 6.0	12.5 $\pm$ 2.0
627-150	150.0 $\pm$ 7.5	15.0 $\pm$ 2.0
627-200	200.0 $\pm$ 10.0	20.0 $\pm$ 2.0
627-250	250.0 $\pm$ 12.5	25.0 $\pm$ 2.5

Note: (1) Measured at 1.5v level leading edge.

*Time Delay measured with respect to 1st Tap.



SCHEMATIC



INCHES .XXX $\pm$.010
MILLIMETERS .XX $\pm$.25