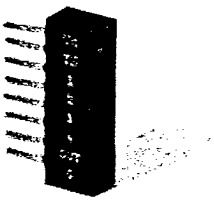
SERIES 630 5-TAPS 8 PIN SIP H-CMOS DIGITAL DELAY MODULES

- 8 Pin Single-In-Line Package • H-Cmos Logic
- 5 Equally Spaced Taps



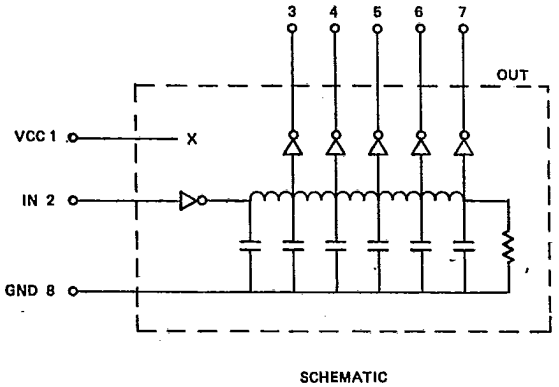
Specifications:

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Voltage : 3.2v
- Logic 0 Input Voltage : 0.9v
- Logic 1 Output Voltage : 4.5v
- Logic 0 Output Voltage : 0.1v
- Input Current : 1μA Max
- Supply Current : ICCH = 5mA, ICCL = 20μA
- Fan-Out : 10 LSTTL Loads Min
- Rise-Time : 8NS TYP. (measured from 0.8v to 2.0v)
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 300 PPM/°C

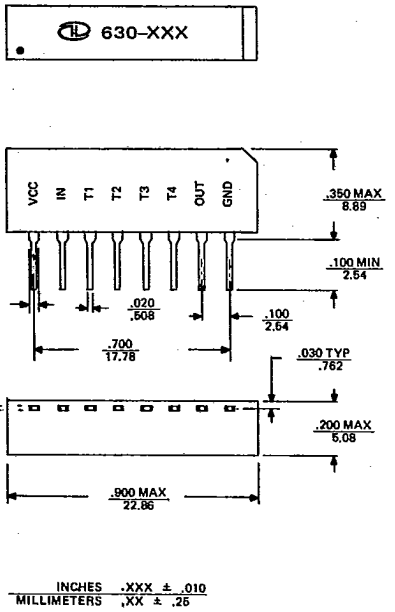
Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number	Total Delay Nanosecond (1)	Tap To Tap Delay NSEC (1)
630–4*	4±1	1±0.5
630–5*	5±1	1.2±0.5
630–6*	6±1	1.5±0.5
630–8*	8±2	2±0.5
630–10*	10±2	2.5±0.5
630–12*	12±2	3±0.5
630–16*	16±2	4±0.6
630–25	25±2	5±0.8
630–30	30±2	6±1
630–35	35±2	7±1
630–40	40±2	8±1
630–45	45±2.2	9±1
630–50	50±2.5	10±2
630–60	60±3	12±2
630–75	75±3.5	15±2
630–100	100±5.0	20±2
630–125	125±5.5	25±2
630–150	150±6.0	30±3
630–175	175±8.7	35±3.5
630–200	200±10	40±4
630–250	250±12.5	50±5

Note: (1) Measured at 1.5v level leading edge.
*Time Delay measured with respect to 1st Tap.



SCHEMATIC



INCHES .XXX ± .010
MILLIMETERS .XX ± .25

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