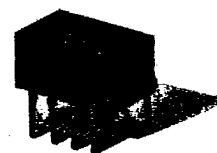


SERIES 633 8 PIN DUAL H-CMOS DIGITAL DELAY MODULES

- 8 Pin DIP Auto-Insertable
- Two Independent Equal Delay Units Per Package
- H-Cmos Logic

Specifications:

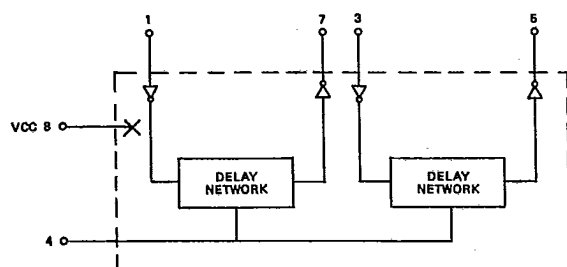
- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Voltage : 3.2v
- Logic 0 Input Voltage : 0.9v
- Logic 1 Output Voltage : 4.5v
- Logic 0 Output Voltage : 0.1v
- Input Current : 1 μ A Max
- Supply Current : ICCH = 5mA, ICCL = 20 μ A
- Fan-Out : 10 LSTTL Loads Min
- Rise-Time : 8NS TYP. (measured from 0.8v to 2.0v)
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 300 PPM/°C



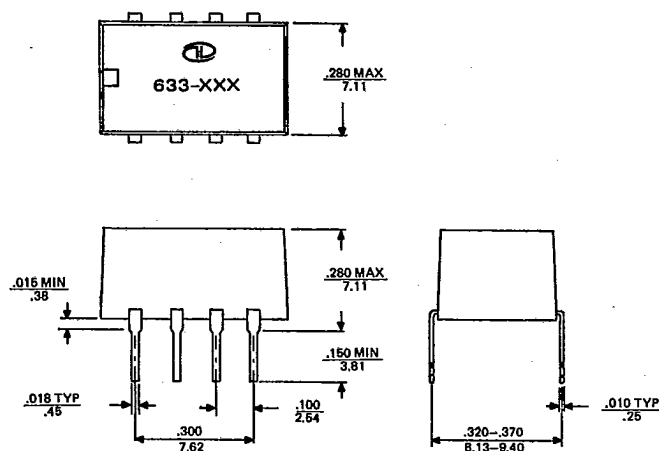
Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number	Total Delay Nanosecond (1)
633-5	5 $\pm$ 1
633-6	6 $\pm$ 1
633-8	8 $\pm$ 2
633-10	10 $\pm$ 2
633-12	12 $\pm$ 2
633-16	16 $\pm$ 2
633-25	25 $\pm$ 2
633-30	30 $\pm$ 2
633-35	35 $\pm$ 2
633-40	40 $\pm$ 2
633-45	45 $\pm$ 2.2
633-50	50 $\pm$ 2.5
633-60	60 $\pm$ 3
633-75	75 $\pm$ 3.5
633-100	100 $\pm$ 5.0
633-125	125 $\pm$ 5.5
633-150	150 $\pm$ 6.0
633-175	175 $\pm$ 8.7
633-200	200 $\pm$ 10
633-250	250 $\pm$ 12.5

Note: (1) Measured at 1.5v level leading edge.



SCHEMATIC



INCHES .XXX $\pm$.010
MILLIMETERS .XX $\pm$.25