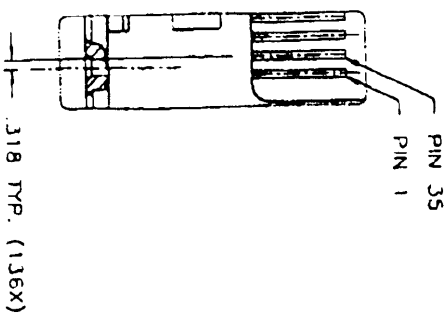


PRODUCT NUMBER	VOLTAGE KEY	DIM A	BOARD OFFSET DIM C	PACKAGING
72267-001	5.0V	1.0	0.3	TRAY-STANDARD PACKAGE 1000
72267-011	3.3V	2.1	0.3	
72267-002	5.0V	1.0	0.6	
72267-012	3.3V	2.1	0.6	
72267-003	5.0V	1.0	0.0	TAPE AND REEL
72267-013	3.3V	2.1	0.0	
72267-001T	5.0V	1.0	0.3	
72267-011T	3.3V	2.1	0.3	
72267-002T	5.0V	1.0	0.6	TRAY-STANDARD PACKAGE 200
72267-012T	3.3V	2.1	0.6	
72267-003S	5.0V	1.0	0.0	
72267-013S	3.3V	2.1	0.0	
72267-002TL	5.0V	1.0	0.6	TAPE AND REEL



DETAIL B
SCALE 4/1

NOTE:
1. MATERIAL

CONTACT: BeCu 0.061
HOUSING: LCP UC94V-0 (COLOR: BLACK)

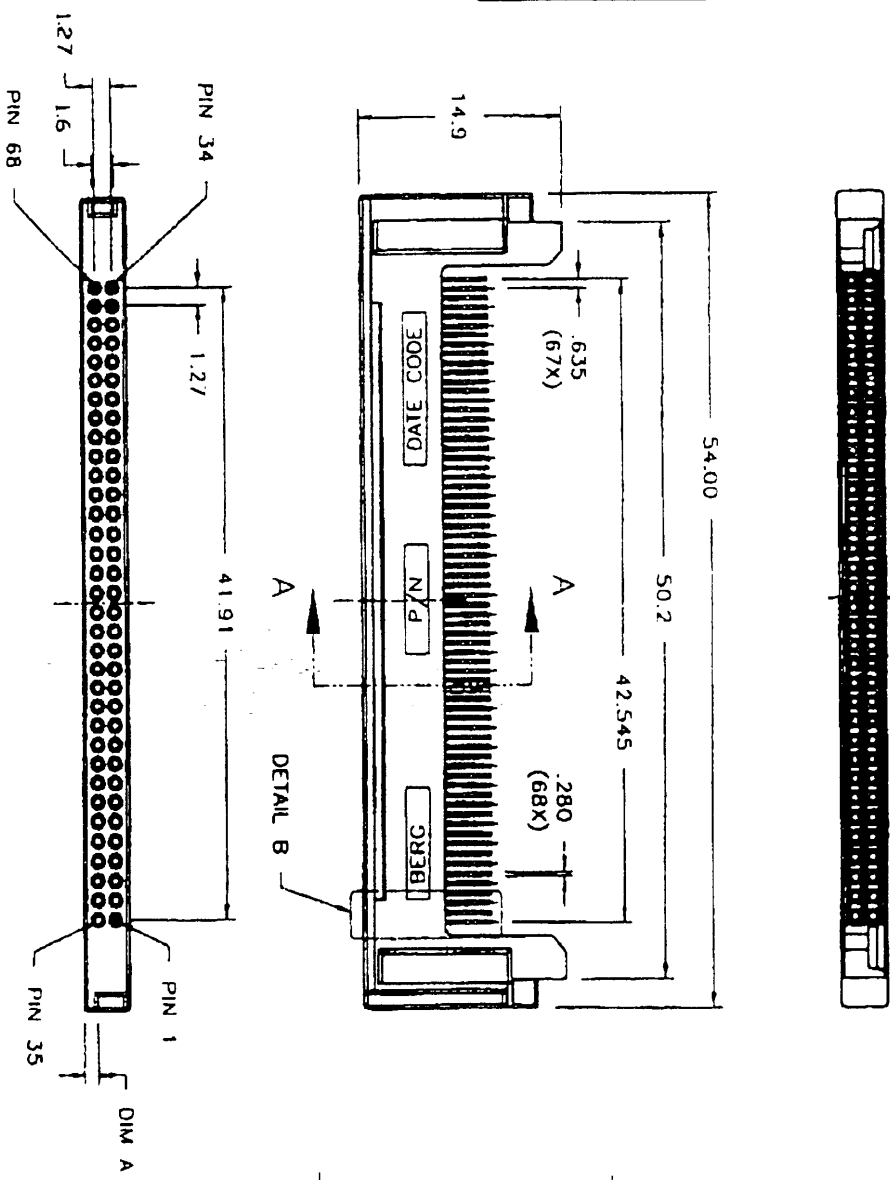
FINISH
CONTACT: ALL OVER--- Ni UNDER 0.5um THK.

SELECTIVE--- Au 0.1um THK.

SOLDER TAIL--- TIN LEAD 2.5um THK.

OVER Pd-Ni 0.5um THK.

MAXIMUM PCB BOW ON THIS

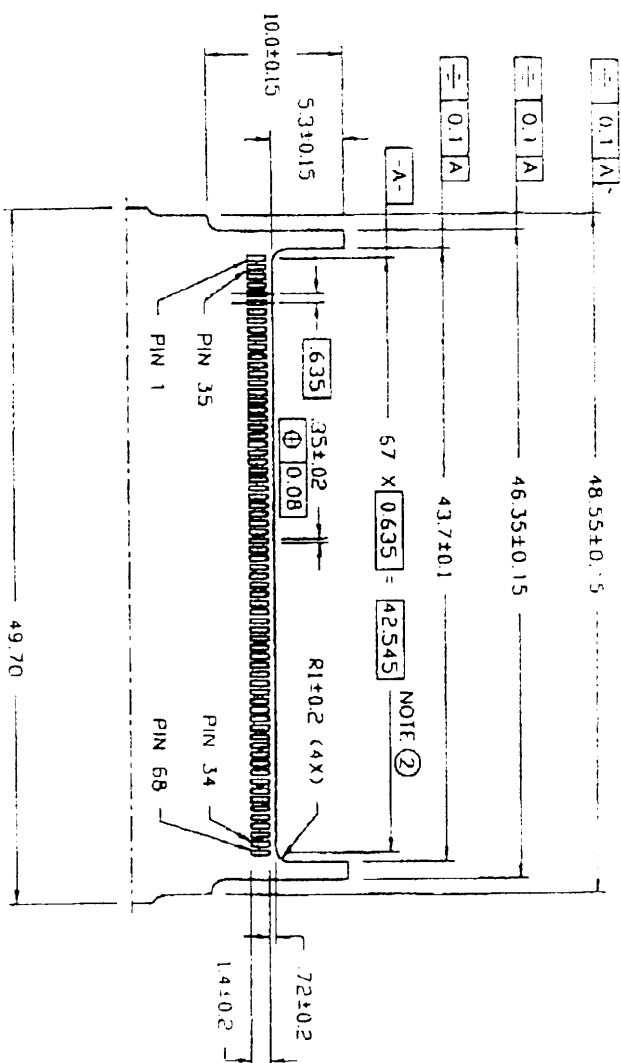
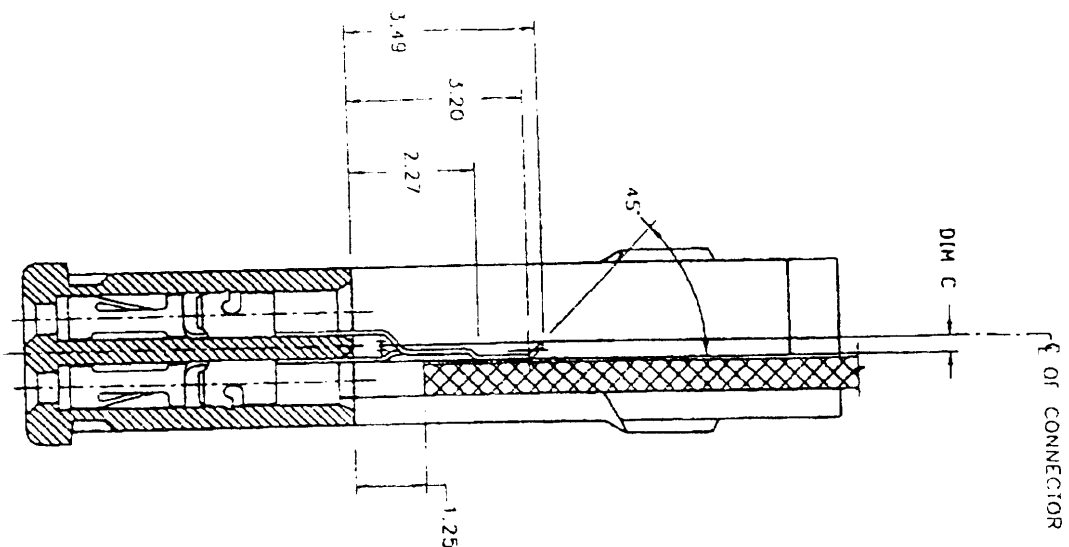


part code				tolerances unless otherwise specified		CUSTOMER COPY		BERG ELECTRONICS	
lit	eca no	dr	dole	.x ±.3		projection		title	
D	V61823	RDS	12/17/96	XX ±.13				PCMCIA RECEPTACLE	
E	V70189	RHC	2/16/97	XXX ±.051				V2 68PIN SMTL ASSY	
F	V70461	RHC	4/25/97	0' ±.2'		mm		product family PCMCIA	
G	V71007	RHC	7/25/97	dr		DINGRAM		6/20/96	
H	V71695	RHC	12/18/97	eng		CHONG TI		6/20/96	
J	V81211	RHC	6/16/98	cht		C.TI		7/23/96	
K	V90056	CCC	1/18/99	opnd		C.TI		7/23/96	
sheet	revision	K	K					scale 2.1	
								A4	
								72267	
								sheet 1 of 2	



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SECTION A--A
SCALE 8/1



RECOMMENDED P.C.B. LAYOUT

matl code		tolerances unless otherwise specified		CUSTOMER COPY		BERG ELECTRONICS	
ly	ecn no	dr	date	.X ± .J .XX ± .13 .XXX ± .051 0 ± .Z		title PCMCIA RECEPTACLE V2 68PIN SMIL ASSY	
K				linear angles 0 ± .Z		protection 	
				0.5H/ENTER 11/25/96 ENGR CHONG TI 11/25/96 C.M. 11/25/96 C.M. 11/25/96		size dwg no A4 72267	
sheet	revision			mm sec/cut		code -	
inset	sheet					sheet 2 of 2	