

54F/74F242

Quad Bus Transceiver with TRI-STATE® Outputs

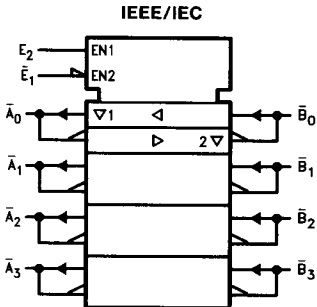
General Description

The 'F242 is a quad bus transmitter/receiver designed for 4-line asynchronous 2-way data communications between data busses.

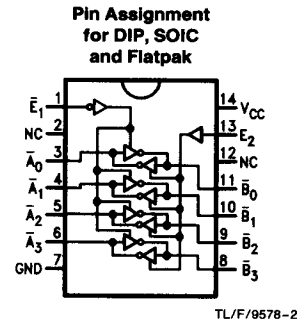
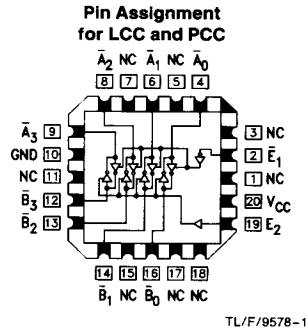
Features

- 2-way asynchronous data bus communication
- Input clamp diodes limit high-speed termination effects

Logic Symbol



Connection Diagrams



Truth Table

Inputs		Inputs/Outputs	
$\bar{E}_1$	E_2	A_n	B_n
L	L	Input	$B = \bar{A}$
L	H	N/A	N/A
H	L	Z	Z
H	H	$A = \bar{B}$	Input

H = HIGH Voltage Level

L = LOW Voltage Level

Z = High Impedance

N/A = Not Allowed Due to Excessive Currents