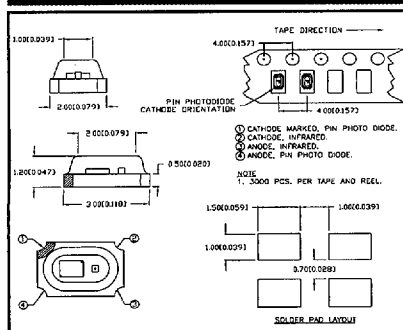


PHOTO REFLECTIVE ASSEMBLIES

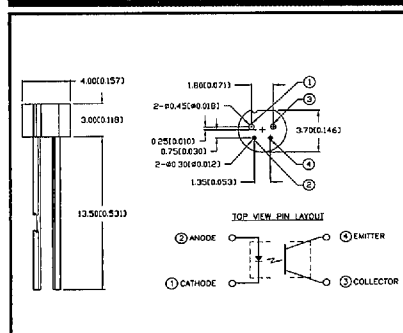
OED-HPI-CL-323



Package Style	Limits of Safe Operation			
	V _R (V)	P _D (mW)	I _f (mA)	t _{opr} (C°)
SMT Ceramic	8.0	150	800	-25 ~ + 80

Input			Output		
V _f @ 20mA	P _o (mW)	I _R (μA)	I _L (μA)	I _{CEO} (μA)	Type
1.6	2.5	1.8	20	—	Pin Photo Diode

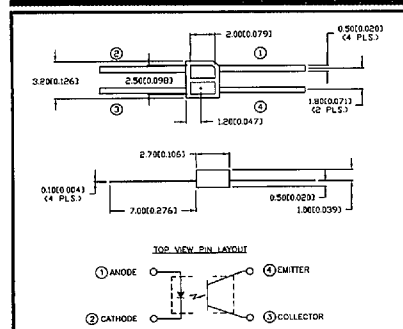
OED-SR-2BC



Package Style	Limits of Safe Operation			
	V _R (V)	P _D (mW)	I _f (mA)	t _{opr} (C°)
Flat Top Radial Leads	5.0	75	50	-20 ~ + 90

Input			Output		
V _f @ 20mA	P _o (mW)	I _R (μA)	I _L (μA)	I _{CEO} (μA)	Type
1.2	2.5	1.0	100	0.1	Photo Transistor

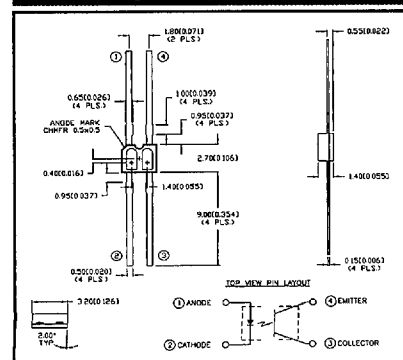
OED-SR-201



Package Style	Limits of Safe Operation			
	V _R (V)	P _D (mW)	I _f (mA)	t _{opr} (C°)
SMT Axial	5.0	60	50	-20 ~ + 85

Input			Output		
V _f @ 20mA	P _o (mW)	I _R (μA)	I _L (μA)	I _{CEO} (μA)	Type
1.7	3.5	5.0	23	100	Photo Transistor

OED-SR-105



Package Style	Limits of Safe Operation			
	V _R (V)	P _D (mW)	I _f (mA)	t _{opr} (C°)
SMT Axial	5.0	75	50	-20 ~ + 85

Input			Output		
V _f @ 20mA	P _o (mW)	I _R (μA)	I _L (μA)	I _{CEO} (μA)	Type
1.3	3.5	1.3	90	0.2	Photo Transistor