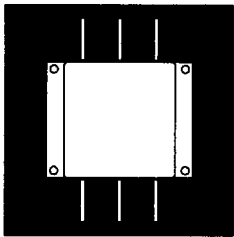


INSULATED GATE BIPOLAR TRANSISTOR (IGBT) IN A HERMETIC PACKAGE



1000 Volt, 50 Amp, N-Channel IGBT
In A Hermetic Metal Package

FEATURES

- Isolated IGBTs In A Hermetic Package
- High Input Impedance
- Low On-Voltage
- High Current Capability
- High Switching Speed
- Low Tail Current
- Available With Free Wheeling Diodes
- Available Screened To MIL-S-19500, TX, TXV And S Levels

DESCRIPTION

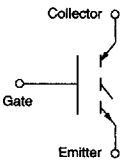
This IGBT power transistor features the high switching speeds of a power MOSFET and the low on-resistance of a bipolar transistor. It is ideally suited for high power switching applications such as frequency converters for 3Ø motors, UPS and high power SMPS.

MAXIMUM RATINGS @ 25°C Unless Specified Otherwise

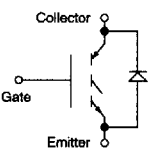
PART NUMBER	I _C (Cont.) @ 90°C, A	V _{BR} (CES) V	V _{CE} (sat) (Typ.) V	T _r (Typ.) ns	θ _{JC} °C/W	P _D W	T _J °C
OM6533SF	50	1000	4.0	300	.50	250	150
OM6534SF	50	1000	4.0	300	.50	250	150

2.1

SCHEMATICS

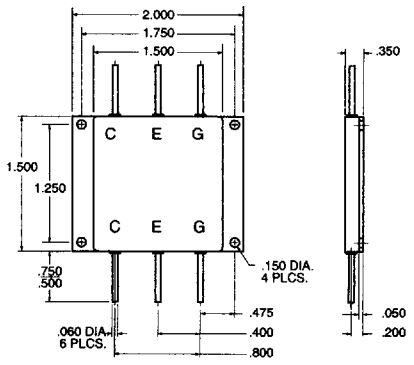


OM6533SF



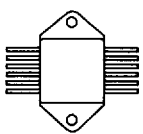
OM6534SF
(with Diode)

MECHANICAL OUTLINE



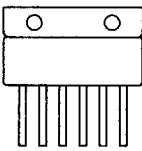
Contact factory for pinout confirmation.

PACKAGE OPTIONS



MOD PAK

NOTE: IGBTs are also available in Z-Tab, single and quad pak styles. Please call the factory for more information.



6 PIN S/P

PRELIMINARY DATA: OM6533SF

IGBT CHARACTERISTICS

Parameter - OFF	Min.	Typ.	Max.	Units	Test Conditions
$V_{BR(ES)}$ Collector Emitter Breakdown Voltage	1000			V	$V_{CE} = 0$ $I_C = 1 \text{ mA}$
I_{CES} Zero Gate Voltage Drain Current			1.0 4.0	mA mA	$V_{CE} = \text{Max. Rat.}, V_{GE} = 0$ $T_C = 125^\circ\text{C}$
I_{GES} Gate Emitter Leakage Current			± 100	nA	$V_{GE} = \pm 20 \text{ V}$ $V_{CE} = 0 \text{ V}$
Parameter - ON					
$V_{GE(th)}$ Gate Threshold Voltage	4.5		6.5	V	$V_{CE} = V_{GE}, I_C = 4 \text{ mA}$
$V_{CE(sat)}$ Collector Emitter Saturation Voltage		3.0		V	$V_{GE} = 15 \text{ V}, I_C = 50 \text{ A}$ $T_C = 25^\circ\text{C}$
$V_{CE(sat)}$ Collector Emitter Saturation Voltage		4.0	4.5	V	$V_{GE} = 15 \text{ V}, I_C = 50 \text{ A}$ $T_C = 150^\circ\text{C}$
Dynamic					
g_{fs} Forward Transconductance	22			S	$V_{CE} = 20 \text{ V}, I_C = 50 \text{ A}$
C_{ies} Input Capacitance		8000		pF	$V_{GE} = 0$
C_{oes} Output Capacitance			640	pF	$V_{CE} = 25 \text{ V}$
C_{res} Reverse Transfer Capacitance			250	pF	$f = 1 \text{ MHz}$
Switching-Resistive Load					
$T_{d(on)}$ Turn-On Time		100		nS	$V_{CC} = 600 \text{ V}, I_C = 50 \text{ A}$
t_r Rise Time		300		nS	$V_{GE} = 15 \text{ V}, R_g = 3.3 \Omega$ $T_J = 125^\circ\text{C}$
$T_{d(off)}$ Turn-Off Delay Time		250		nS	
t_f Fall Time		300		nS	
Switching-Inductive Load					
$T_{d(off)}$ Turn-Off Delay Time		250		nS	$V_{CE(sat)} = 600 \text{ V}, I_C = 50 \text{ A}$
t_f Fall Time		200		nS	$V_{GE} = 15 \text{ V}, R_g = 3.3 \Omega$
E_{off} Turn-Off Losses			4.0	mWs	$L = 1 \text{ mH}, T_J = 125^\circ\text{C}$

PRELIMINARY DATA: OM6534SF

IGBT CHARACTERISTICS

Parameter - OFF (see Note 1)	Min.	Typ.	Max.	Units	Test Conditions
$V_{BR(ES)}$ Collector Emitter Breakdown Voltage	1000			V	$V_{CE} = 0$ $I_C = 1 \text{ mA}$
I_{CES} Zero Gate Voltage Drain Current			1.0 4.0	mA mA	$V_{CE} = \text{Max. Rat.}, V_{GE} = 0$ $T_C = 125^\circ\text{C}$
I_{GES} Gate Emitter Leakage Current			± 100	nA	$V_{GE} = \pm 20 \text{ V}$ $V_{CE} = 0 \text{ V}$
Parameter - ON					
$V_{GE(th)}$ Gate Threshold Voltage	4.5		6.5	V	$V_{CE} = V_{GE}, I_C = 4 \text{ mA}$
$V_{CE(sat)}$ Collector Emitter Saturation Voltage		3.0		V	$V_{GE} = 15 \text{ V}, I_C = 50 \text{ A}$ $T_C = 25^\circ\text{C}$
$V_{CE(sat)}$ Collector Emitter Saturation Voltage		4.0	4.5	V	$V_{GE} = 15 \text{ V}, I_C = 50 \text{ A}$ $T_C = 150^\circ\text{C}$
Dynamic					
g_{fs} Forward Transconductance	22			S	$V_{CE} = 20 \text{ V}, I_C = 50 \text{ A}$
C_{ies} Input Capacitance		8000		pF	$V_{GE} = 0$
C_{oes} Output Capacitance			640	pF	$V_{CE} = 25 \text{ V}$
C_{res} Reverse Transfer Capacitance			250	pF	$f = 1 \text{ MHz}$
Switching-Resistive Load					
$T_{d(on)}$ Turn-On Time		100		nS	$V_{CC} = 600 \text{ V}, I_C = 50 \text{ A}$
t_r Rise Time		300		nS	$V_{GE} = 15 \text{ V}, R_g = 3.3 \Omega$ $T_J = 125^\circ\text{C}$
$T_{d(off)}$ Turn-Off Delay Time		250		nS	
t_f Fall Time		300		nS	
Switching-Inductive Load					
$T_{d(off)}$ Turn-Off Delay Time		250		nS	$V_{CE(sat)} = 600 \text{ V}, I_C = 50 \text{ A}$
t_f Fall Time		200		nS	$V_{GE} = 15 \text{ V}, R_g = 3.3 \Omega$
E_{off} Turn-Off Losses			4.0	mWs	$L = 1 \text{ mH}, T_J = 125^\circ\text{C}$

DIODE CHARACTERISTICS

V_f Maximum Forward Voltage			1.8	V	$I_f = 50 \text{ A}, T_C = 25^\circ\text{C}$
I_r Maximum Reverse Current			1.6	V	$I_f = 50 \text{ A}, T_C = 125^\circ\text{C}$
			.5	mA	$V_R = 1000 \text{ V}, T_C = 25^\circ\text{C}$
			4.0	mA	$V_R = 1000 \text{ V}, T_C = 125^\circ\text{C}$
t_r Reverse Recovery Time			200	nS	$I_f = 50 \text{ A}, dI/dt = -800 \text{ A}/\mu\text{S}$ $V_R = 600 \text{ V}, T_J = 125^\circ\text{C}, V_{GE} = 0$

Note 1: Limited by diode I_r characteristic.