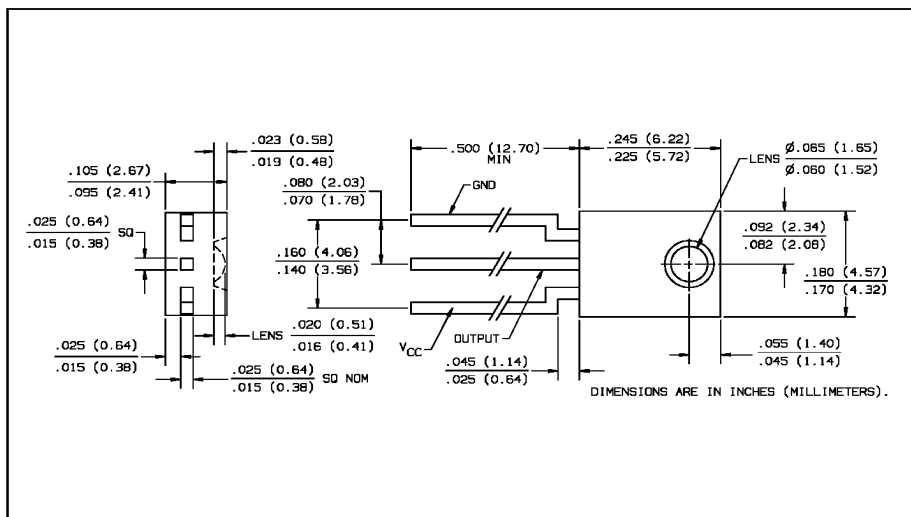
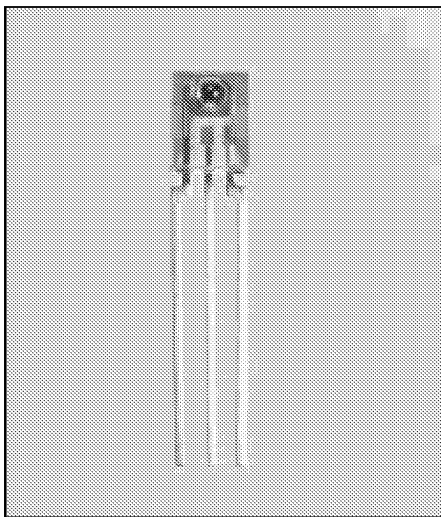


Photologic® Sensors

Types OPL535, OPL535-OC, OPL536, OPL536-OC



Features

- Four output options
- High noise immunity
- Direct TTL/LSTTL CMOS interface
- Low cost plastic side-looking package
- Mechanically and spectrally matched to the OP145 and OP245 series LED's
- Data rates to 250 kBaud
- Low power consumption

Description

The OPL535, OPL535-OC, OPL536, OPL536-OC contain a monolithic integrated circuit which incorporates a photodiode, amplifier, voltage regulator, Schmitt trigger and an NPN output transistor on a single silicon chip. The OPL535 and OPL536 include a 10 K Ω pull-up resistor (R_L) from output to V_{CC} . The OPL535-OC and OPL536-OC have an open-collector output. These devices exhibit very stable performance over supply voltages ranging from 4.5 V to 16 V and a wide range of irradiance levels. The Photologic® chip is encapsulated in a molded plastic package which has a recessed integral lens for enhanced optical coupling combined with mechanical protection.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

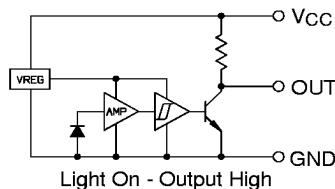
Supply Voltage, V_{CC}	18 V
Storage Temperature Range	-40°C to $+100^\circ\text{C}$
Operating Temperature Range	-40°C to $+85^\circ\text{C}$
Lead Soldering Temperature Range [1/16 inch (1.6 mm) from case for 5 sec. with soldering iron]	240°C
Power Dissipation	90 mW
Voltage at Output Lead ⁽⁴⁾	35 V
Sinking Current	50 mA

Notes:

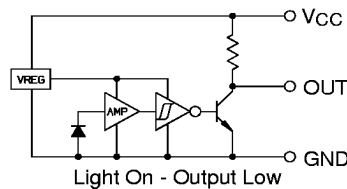
- (1) Derate linearly 2.67 mW/ $^\circ\text{C}$ above 70°C .
- (2) RMA flux is recommended. Duration can be extended to 10 sec. maximum when flow soldering. Max 20 grams force may be applied to the leads when soldering.
- (3) Irradiance measurements are made with $\lambda_i = 935\text{ nm}$.
- (4) OC versions only. For I_{CC} on pull-up versions add $V_{CC}/10\text{ k}\Omega$.

Schematics

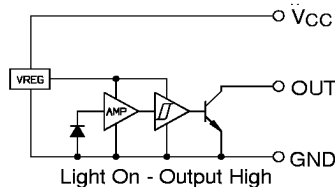
OPL535 Buffer/Pull-up Resistor



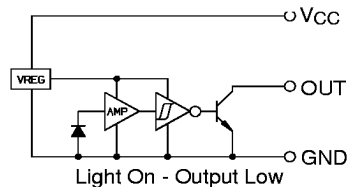
OPL536 Inverter/Pull-up Resistor



OPL535-OC Buffer/OC



OPL536-OC Inverter/OC



Types OPL535, OPL535-OC, OPL536, OPL536-OC

Electrical Characteristics (-40° C to +85° C unless otherwise noted) $V_{CC} = 4.5 \text{ V to } 16 \text{ V}$

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
V _{CC}	Operating Supply Voltage	4.5		16.0	V	
	Peak-to-Peak V _{CC} Ripple Necessary to Cause False Triggering of Output			2	V	f = DC to 50 MHz
I _{CC}	Supply Current ⁽⁴⁾		2.7	5.0	mA	E _e = 0 or 1 mW/cm ²
E _{eT} (+)	Positive-Going Threshold Irradiance ⁽³⁾					
	OPL535, OPL535-OC, OPL536, OPL536-OC	0.12		0.38	mW/cm ²	T _A = 25° C
	OPL535A, OPL535-OCA, OPL536A, OPL536-OCA	0.12		0.28	mW/cm ²	T _A = 25° C
	OPL535B, OPL535-OCB, OPL536B, OPL536-OCB	0.23		0.38	mW/cm ²	T _A = 25° C
E _{eT} (+)/E _{eT} (-)	Hysteresis Ratio	1.20		1.80		
ΔE _{eT} (+)(ΔT)	Temperature Coefficient 					

PHOTOLOGIC®
SENSORS

Optek reserves the right to make changes at any time in order to improve design and to supply the best product possible.

Optek Technology, Inc. 1215 W. Crosby Road Carrollton, Texas 75006 (972)323-2200 Fax (972)323-2396

--