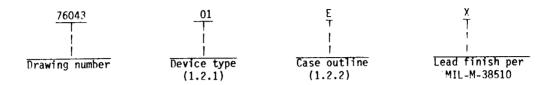
REVISIONS LTR DESCRIPTION DATE APPROVED Remove vendors 18324 and 27014. Convert to Military format. 18 JUNE 1)ilet Ac Device Ol inactive for new 1987 Add LCC package. design use QPL-M38510 product. REV PAGE REV **REV STATUS** OF PAGES **PAGES** 9 10 **Defense Electronics Supply Center** This drawing is available for use by Dayton, Ohio all Departments and Agencies of the Department of Defense TITLE: MICROCIRCUITS, DIGITAL LOW POWER, Original date SCHOTTKY, TTL, ADDER, MONOLITHIC SILICON of drawing: 24 November 1976 SIZE CODE IDENT. NO. DWG NO. 76043 14933 Α AMSC N/A REV С PAGE 1 OF 12 5962-E277

SCOPE

- 1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".
 - 1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Pevice type. The device type shall identify the circuit function as follows:

| Device type Generic number | | Circuit |
|----------------------------|---------|--|
| 01 | 54LS283 | 4-bit binary full adders with fast carry |

1.2.2 <u>Case outlines</u>. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

| Outline letter | <u>Case outline</u> | | | | | | |
|----------------|---|--|--|--|--|--|--|
| E | D-? (16-lead, 1/4" x 7/8"), dual-in-line package | | | | | | |
| F | F-5 (16-lead, 1/4" x 3/8"), flat package | | | | | | |
| 2 | C-2 (20-terminal, .350" x .350"), square chip carrier package | | | | | | |

1.3 Absolute maximum ratings.

1.4 Recommended operating conditions.

| Supply voltage (V _{CC}) | 4.5 V dc minimum to 5.5 V dc maxmium |
|--|--------------------------------------|
| Minimum high level input voltage (VIH) | 2.0 V dc |
| Maximum low level input voltage (VIL) | 0.7 V dc |
| Case operating temperature range (T_C) | -55°C to +125°C |

- 1/ Must withstand the added Pp due to short circuit test (e.g., Ios).
- 2/ When a thermal resistance for this case is specified in MIL-M-38510, appendix C, that value shall supersede the value specified herein.

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2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510

- Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883

Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

- 3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.
- 3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.
 - 3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.
 - 3.2.2 Truth table. The truth table shall be as specified on figure 2.
 - 3.2.3 Logic diagram. The logic diagram shall be as specified on figure 3.
 - 3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.
- 3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended case operating temperature range.
- 3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.
- 3.5 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

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| 4 | 1 | T | | Li | mits | |
|--|-------------------|--|---------------------------|------------------|-------------------|------------------|
| Test | Symbol | Conditions -55°C < T _C < +125°C unless otherwise specified | Group A subgroups | Min | Max | Uni |
| High-level output voltage | v _{OH} | $V_{CC} = 4.5 \text{ V}; I_{OH} = -400 \mu\text{A}$ $V_{IN} = 0.7 \text{ V or 2.0 V}$ | 1,2,3 | 2.5 | | V |
| Low-level output voltage | I V _{OL} | V _{CC} = 4.5 V; I _{OL} = 4 mA V _{IN} = 0.7 V or 2.0 V | 1,2,3 | | 0.4 | V |
| Input clamp voltage | VIC | V _{CC} = 4.5 V; I _{IN} = -18 mA T _C = +25°C | 1 | | -1.5 | ! V |
| High-level input current at any A or B | I IH1 | V _{CC} = 5.5 V; V _{IN} = 2.7 V | 1,2,3 | | 40 | μΑ |
| | I IHS | V _{CC} = 5.5 V; V _{IN} = 5.5 V | 1,2,3 | | 200 | μΑ |
| High-level input current at CO | I IH3 | V _{CC} = 5.5 V; V _{IN} = 2.7 V | 1,2,3 | | 20 | μΑ ! |
| | I I I H4 | $ V_{CC} = 5.5 \text{ V}; V_{IN} = 5.5 \text{ V}$ | 1,2,3 | | 100 | μ A |
| Low-level input current at any A or B | IILI | V _{CC} = 5.5 V; V _{IN} = 0.4 V | 1,2,3 | ! ! ! ! | 80 | mA |
| Low-level input current at CO | I _{IL2} | V _{CC} = 5.5 V; V _{IN} = 0.4 V | 1,2,3 | | 40 | mA |
| Short-circuit output current | 105 | V _{CC} = 5.5 V; V _{OUT} = 0.0 V <u>1</u> / | 1,2,3 | ! ! -15 ! | -130 | i mA |
| Supply current | I cc1 | V _{CC} = 5.5 V All inputs grounded | 11,2,3 | | 39 | mA |
| | I CC2 | V _{CC} = 5.5 V All B inputs = GND other inputs = 4.5 V | 1,2,3 | | 34 | mA |
| | I _{CC3} | | 11,2,3 | |] 34 | mA |
| Functional tests | | See 4.3.1c | 7 | | ! | |

See footnotes at end of table.

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| TAULE | I. Electrici | ur perrormance c | <u>haracteristics</u> - C | on of flucus | <u></u> - | | |
|--|---|--|-----------------------------------|--------------------------------|--------------|-------------------|----------------------|
| Test | Test Symbol Conditions -55°C < T _C < +125°C unless otherwise specified | | | Group A subgroups | | nits Max | l Unit |
| Propagation delay time high-to-low-level | t _{PHL1} | V _{CC} = 5.0 V R _L = 2 kΩ ±5% | C _L = 15 pF ±10% | 9 | | 24 | l ns |
| CO to any Σ 2/ | ! | | | 10,11 | | 34 | ns |
| | [| | C _L = 50 pF ±10% | 19 | | 29 | l ns |
| | ! ! ! | - | | 110,11 | | 41 | ns! |
| Propagation delay time tpLH1 CO to any Σ 2/ | T ! ! | | 9 | | 24 | ns | |
| | | ! ! | | 110,11 | | 34 | l ns |
| | | - | C _L = 50 pF ±10% | 9 | | 29 | ns |
| | ! ! | | | 10,11 | | 41 | l ns |
| Propagation delay time high-to-low level | t _{PHL2} | T ! ! | C _L = 15 pF ±10% | ! 9 | | 24 | ns |
| A_i or B_i to Σ_i $\underline{2}/$ | | | ! | 110,11 | | 34 | ns |
| | | | T C _L = 50 pF ±10% | 9 | | 29 | ns |
| | | | ļ | 10,11 | | 41 | ns |
| Propagation delay time low-to-high level | t _{PLH2} | - | C _L = 15 pF ±10% | 9 | | 24 | ns |
| A_i or B_i to Σ_i $\underline{2}/$ | | ! ! ! | 1 | 110,11 | | 1 34 1 | ns |
| | | ! ! | CL = 50 pF ±10% | 9 | | 29 | ns |
| | ļ | | | 10,11 | | 41 | ns |

See footnotes at end of table.

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| | \blacksquare | | |
|--|----------------|--|--|
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |

| Test | Symbol | Cond | litions | Group A | Limits | | Unit |
|---|--------------------|---|---|--------------|--------|----------------|------------|
| | 1 | -55°C < T _C | -55°C < T _C < +125°C unless otherwise specified | | | Max | Γ |
| Propagation delay time high-to-low-level | t _{PHL3} | $V_{CC} = 5.0 \text{ V}$ $V_{RL} = 2 \text{ k}\Omega \pm 5\%$ | $ C_L = 15 \text{ pF } \pm 10\%$ | 9 | | ! ! 22 ! | ns |
| CO to C4 2/ | | | | 10,11 | | 31 | ns |
| | | ! ! | C _L = 50 pF ±10% | 9 | | 27 | ns |
| | | ! ! ! | | 10,11 | | 38 | ns |
| Propagation delay time tpth tow-to-high level CO to C4 2/ | t _{PLH3} | ! ! | C _L = 15 pF ±10% | 9 | | 17 | ns |
| | | | | 10,11 | | 2 4 | ns |
| | | | C _L = 50 pF ±10% | 9 | | 22 | l I ns |
| | | | | 10,11 | | 31 | ns |
| Propagation delay time tpp | t _{PHL} 4 | T † ! | C _L = 15 pF ±10% | 9 | | 17 | l I ns |
| A_i or B_i to $C4 	extstyle 2/$ | | | | 10,11 | | 24 | l I ns |
| | | | C _L = 50 pF ±10% | 9 | | 22 | ns |
| | | | | 110,11 | | 31 | ns |
| Propagation delay time low-to-high level | t _{PLH4} | T ! ! | | 9 | | 17 | ns |
| A _i or B _i to C4 <u>2</u> / | | 1 | | 10,11 | | 24 | l ns |
| | | ! ! | | 9 | | 22 | ns |
| | ! | | | 10,11 | | 31 | ns |

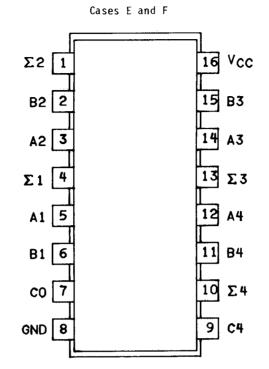
^{1/} Not more than one output should be shorted at a time, and the duration of the short circuit condition should not exceed one second.

^{2/} Propagation delay time testing may be performed using either $C_L = 15$ pF or $C_L = 50$ pF. However, the manufacturer must certify and guarantee that the microcircuits meet the switching test limits specified for a 50 pF load.

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- 3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.
- 3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).
- 3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.
 - 4. QUALITY ASSURANCE PROVISIONS
- 4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).
- 4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:
 - a. Burn-in test (method 1015 of MIL-STD-883).
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125$ °C, minimum.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
- 4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method $\overline{5005}$ of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.
 - 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 4, 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.
 - c. Subgroup 7 tests shall verify the truth table.
 - 4.3.2 Groups C and D inspections.
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125$ °C, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

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Case 2

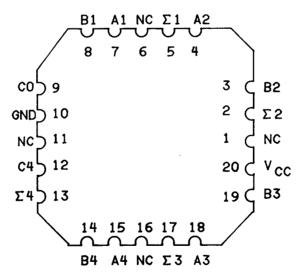


FIGURE 1. Terminal connections.

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| T I | | | | Output | | | | | | |
|-------------|--|--|----------|--|--------------------------|----------|--|--------------------------|--------------|--|
| | Inp | out | | When CO = L When C2 = L | | | When CO = H When C2 = H | | | |
| A1 A3 | B1 B3 | A2 A4 | B2 B4 | Σ1 Σ3 | Σ 2 Σ 4 | C2 C4 | Σ 1 Σ3 | Σ 2 Σ 4 | C2 C4 | |
| ļ L | L | L | L | L | L | L | H | L | L | |
|] H | L | L | L | Н | L | L | L | H | L | |
| | Н | L. | L | н | L | L | L | H | L | |
| Н | н | L | L | L | н | L | н | Н | L | |
| | L | н | L | L | н | L | н | н | L | |
| H | L | Н | L | н | Н : | L | L | L | н | |
| L | н | н | L | н | н | L | i L | L | н | |
| Н | н | Н | L | L | i L | н | Н | L | і н і 1 і | |
| ļ L | L | L | н | L | Н | L | н | н | LI | |
| Н | L | ֡ ֡ ֡֞֜֞֜֜֞֜֞֜֜֞֜֜֞֜֜֞֜֜֞֜֜֜֞֜֜֓֓֓֓֓֓֡֜֡֡֓֓֓֡֡֡֡֡֡֡֡ | н | н | н | L | L | L | Н | |
| L | н | L | Н | н | Н | L | L | L | н | |
| Н | н | L | Н | L | L | Н | H | L | н | |
| L | L | Н | Н | L | L | Н | н | L | н | |
| Н | ֡ ֡֞֜֞֜֞֜֞֜֞֞֜֞֜֞֜֞֜֞֜֞֞֜֓֓֓֓֓֓֡֡֞֜֝֓֡֓֡֡֡֡֡֡֡֡֡֡ | Н | Н | H | L | Н | Ĺ | Н | i H i | |
| L | Н | H | н | Н | L | Н | L | Н | Н | |
| н | H | Н I | н | | н | Н | | Н | H I | |

H = High level L = Low level

NOTE: Input conditions at A1, B1, A, B2, and C0 are used to determine outputs $\Sigma 1$ and $\Sigma 2$ and the value of the internal carry C2. The values at C2, A3, B3, A4, and B4, are then used to determine outputs $\Sigma 3$, $\Sigma 4$, and C4.

FIGURE 2. Truth table.

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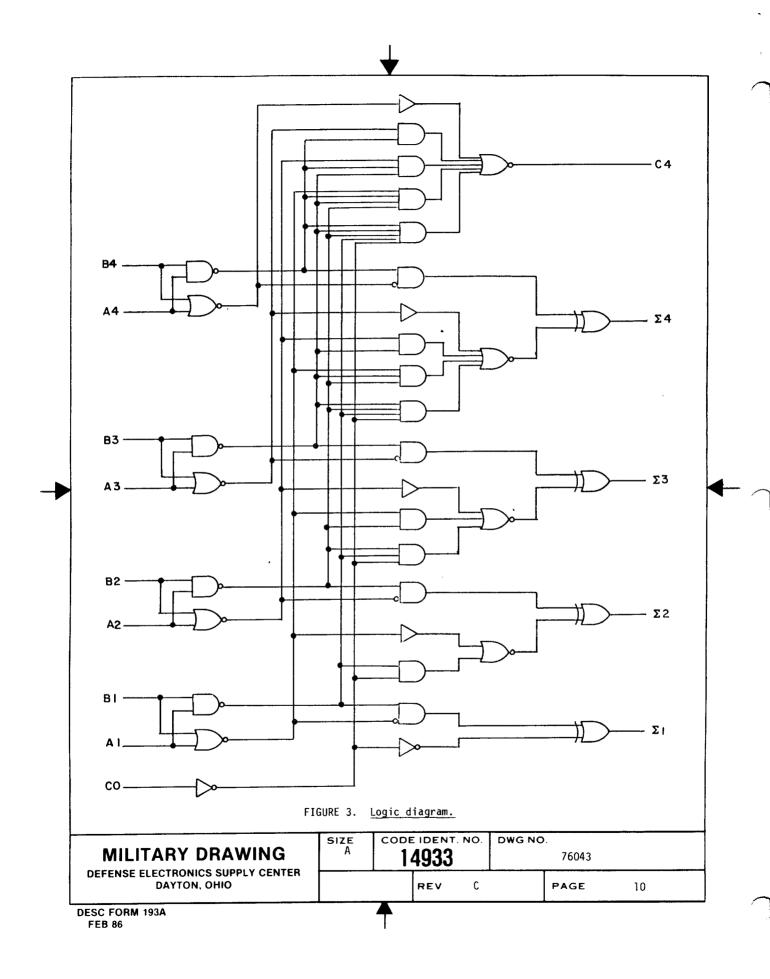


TABLE II. Electrical test requirements.

| MIL-STD-883 test requirements | Subgroups (per method 5005, table I) |
|--|--|
| Interim electrical parameters (method 5004) | |
| Final electrical test parameters (method 5004) | 1*,2,3,9 |
| Group A test requirements (method 5005) | 1,2,3,7,9 |
| Groups C and D end-point electrical parameters (method 5005) | 1,2,3 |

* PDA applies to subgroup 1.

** Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

- PACKAGING
- 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.
- NOTES
- 6.1 <u>Intended use.</u> Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.
 - 6.2 Replaceability. Replaceability is determined as follows:
 - a. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
 - b. When a QPL source is established, the part numbered device specified in this drawing will be replaced by the microcircuit identified as part number M38510/31202B--.
- 6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved sources of supply. Approved sources of supply are listed herein. Additional sources will be added as they become available. The vendors listed herein have agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

| Military drawing part number | Vendor CAGE number | Vendor similar part number <u>1</u> / | Replacement military specification part number |
|---|------------------------------|--|---|
| 7604301EX <u>2</u> / | 01295 07263 04713 | SNJ54LS283J 54LS283DMQB 54LS283BEXJC | M38510/31202BEX |
| 7604301FX <u>2</u> / | 01295 07263 04713 | SNJ54LS283W 54LS283FM0B 54LS283BFXJC | M38510/31202BFX |
| 76043012X <u>2</u> / | 01295 07263 04713 | SNJ54LS283FK 54LS283LMQB 54LS283M/B2CJC | M38510/31202BFX |

- $\frac{1}{2}$ Caution. Do not use this number for item acquisition. Items acquired to the vendor similar part number only may not satisfy the performance requirements of this drawing.
- $\underline{2}$ / Inactive for new design. Use QPL-38510 product.

| Vendor CAGE number | Vendor name and address |
|-----------------------|---|
| 01295 | Texas Instruments, Inc. P. O. Box 6448 Midland, TX 79701 |
| 07263 | Fairchild Semiconductor 333 Westerm Avenue South Portland, ME 04106 |
| 04713 | Motorola, Inc. 7402 South Price Road Tempe, AZ 85283 |

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