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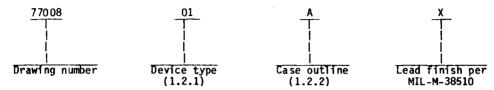
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DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

 $1.1\,$ Scope. This drawing describes device requirements for class B microcircuits in accordance with $1.\overline{2.1}$ of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	LM139	Quad voltage comparator

1.2.2 <u>Case outlines</u>. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Case outline
F-1 (14-lead, .280" x .260" x .085"), flat package
D-1 (14-lead, .785" x .310" x .200"), dual-in-line package C-2 (20-terminal, .358" x .358" x .100"), square chip carrier package

1.3 Absolute maximum ratings.

1.4 Recommended operating conditions.

 $\underline{1}$ / Derate above +100°C ambient, 10 mW/°C.

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2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MII ITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MIL TTARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

- 3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.
- 3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.
 - 3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.
 - 3.2.2 Functional diagram. The functional diagram shall be as specified on figure 2.
 - 3.2.3 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.
- 3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full ambient operating temperature range.
- 3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.
- 3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.
- 3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

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Test Symbol		Conditions -55°C < T _A < +125°C				Group A	<u>L1</u>	 Unit		
	1	-5 un1	ess other	ise s	pecified	subgroups	Min	Max		
Input offset voltage	v _{IO}	R _S = 0	Ω , $V_0 = 1$ V to 30	,4 V		1	ļ	±5.0	ļ m∀	
	<u> </u>					2,3	<u> </u>	±9.0	<u> </u>	
Input offset current	IIIO	I _{IN} (+)	$I_{IN}(+)$ - $I_{IN}(-)$ with output in the linear range		1 1	i i T	±25	l nA		
	<u> </u>				2,3	<u> </u>	±100	<u> </u>		
Input bias current	IIB	I _{IN} (+)	$I_{ m IN}(+)$ or $I_{ m IN}(-)$ with output in the linear range		1	-100	-1	l nA		
	<u> </u>				2,3	- 300	-1	<u> </u>		
Input common-mode voltage range	V _{I CR}	V+ = 5	V to 30 \		1,2,3	0	V+ -2.0	V		
Voltage gain	Av	R _L > 1	5 kΩ, V+ =	: 15 V		4	50] V/m	
		<u> </u>				5,6	25	<u> </u>	<u> </u>	
Output sink current	 I(sink) 	$V_{IN}(-) = 1 V, V_{IN}(+) = 0 V, V_0 \ge 1.5 V, T_A = +25^{\circ}C$				1	6 !	 	 mA 	
Saturation voltage			$V_{IN}(-) = 1 V, V_{IN}(+) = 0 V,$ $I(sink) \le 4 \text{ mA}$		1		400	mV		
]	I(sink 			2,3		700	Ī		
Output leakage current	I _{OL}	A IN(+)	$V_{IN}(+) > 1 V, V_0 = 30 V, V_{IN}(-) = 0 V$		1		.5	ĮμA		
		 A W(-)	= U ¥			2,3	<u> </u>	1	<u> </u>	
Supply current	Icc	 R _L = ∞ 	, V+ = 5 V	to 30	O V	1,2,3	 	3	mA	
Input voltage common mode rejection ratio	CMRR	R _{L > 1}	$L > 15 \text{ k}\Omega$, V+ = 30 V, CM = 0 V to 28 V			1,2,3	70 1		l dB	
Power supply rejection ratio	PSRR	V+ = 5	V to 30 V		1,2,3	70		dB		
 			5 V, R _L = input ste	, = +25°C	 9 		5	l μs 		
			mV overdrive, T _A = +25°C					2.5	Γ !	
1/ Guaranteed if not test		e limit		d.						
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Device type	01	
Case outlines	A and C	2
Terminal number	Terminal	symbol
1 1 2 3 3 4 4 5 5 6 7 7 8 9 1 10 11 12 13 14 15 16 17 18 19 20	OUT 2 OUT 1 Y+ IN 1- IN 1+ IN 2- IN 2+ IN 3- IN 3- IN 4- IN 4- IN 4+ GND OUT 4 OUT 3	NC OUT 2 OUT 1 V+

FIGURE 1. Terminal connections.

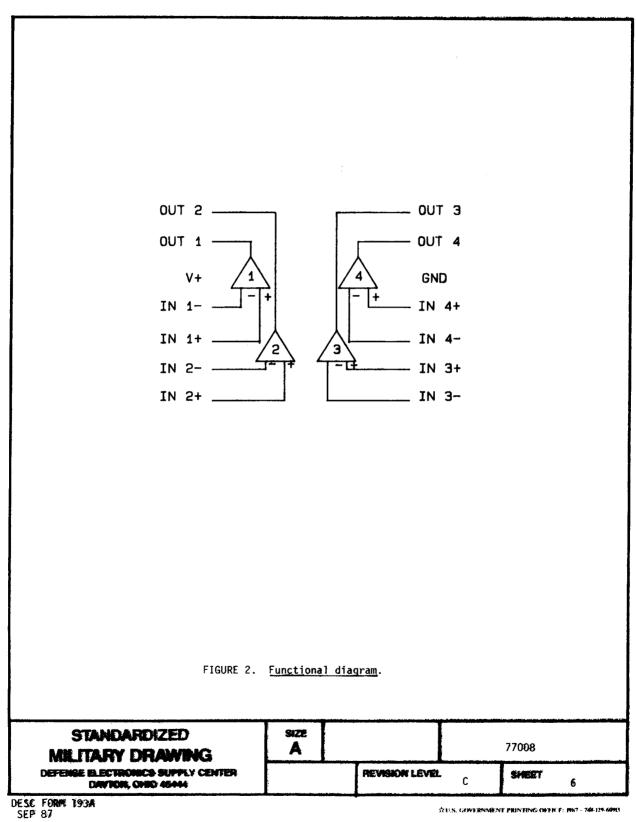
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DEFENSE ELECTRONICS SUPPLY CENTER
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- 3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).
- 3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.
 - 4. QUALITY ASSURANCE PROVISIONS
- 4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).
- 4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:
 - a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C or D using the circuit submitted with the certificate of compliance (see $3.5\ herein$).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
- 4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method $\frac{5005}{5005}$ of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.
 - 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 7, 8, 10 and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.
 - 4.3.2 Groups C and D inspections.
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

 MIL-STD-883 test requirements 	Subgroups (per method 5005, table I)
 Interim electrical parameters (method 5004)	1
 Final electrical test parameters (method 5004)	1*, 2, 3, 4, 5, 6, 9
Group A test requirements (method 5005)	1, 2, 3, 4, 5, 6, 9
 Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

* PDA applies to subgroup 1.

5. PACKAGING

- 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.
 - 6. NOTES
- 6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.
 - 6.2 Replaceability. Replaceability is determined as follows:
 - a. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
 - b. When a QPL source is established, the part numbered device specified in this drawing will be replaced by the microcircuit identified as part number M38510/11201B--.
- 6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved sources of supply. Approved sources of supply are listed herein. Additional sources will be added as they become available. The vendors listed herein have agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1</u> /	Replacement military specification part number
7700801AX <u>2</u> /	3/		M38510/11201BAX
7700801CX <u>2</u> /	06665 07933 18324	PM-139Y/883 LM139J/883B LM139/BCA	M38510/11201BCX
77008012X	06 665	PM139RC/883	

 $[\]frac{1}{2} \frac{\text{Caution.}}{\text{to this}} \text{ nont use this number for item acquisition.} \text{ Items acquired to this number may not satisfy the performance requirements of this drawing.} \\ \frac{2}{3} \text{Inactive for new design.} \text{ Use applicable M38510/11201 device.} \\ \frac{3}{2} \text{Inactive form an approved source.} \\$

Vendor CAGE number	Vendor name and address
06665	Precision Monolithics Incorporated 1500 Space Park Drive P.O. Box 58020 Santa Clara, CA 95050
07933	Raytheon Company Semiconductor Division 350 Ellis Street Mountain View, CA 94040
18324	Signetics Incorporated 4130 S. Market Court Sacramento, CA 95834

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