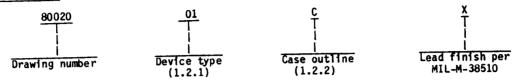
REVISIONS DATE APPROVED DESCRIPTION LTR Add LCC package. Ç 22 JULY Convert to military drawing. 1987 Change load resistance in table 1. Change code indent no. to 67268. REV PAGE Ç С С C REV С C С **REV STATUS OF PAGES** PAGES 6 **Defense Electronics** This drawing is available for use by **Supply Center** all Departments and Agencies of the Dayton, Ohio Department of Defense MICROCIRCUITS, DIGITAL, LOW POWER
TITLE:SCHOTTKY TTL, BUS TRANSCEIVER WITH
THREE-STATE OUTPUTS MONOLITHIC SILICON Original date of drawing: 19 MAY 1981 CODE IDENT. NO. DWG NO. SIZE 80020 67268 AMSC N/A REV PAGE **OF** С 5962-E475

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1	cr	OPF

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device types. The device types shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	54LS242	Quadruple bus transceivers with three-state outputs inverted
02	54LS243	Quadruple bus transceivers with three-state outputs non-inverted

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
С	D-1 (14-lead $1/4$ x $3/4$), dual-in-line package
D	F-2 (14-lead, $1/4$ " x $3/8$ "), flat-package
2	C-2 (20-terminal .350" x .350"), square chip carrier package

1.3 Absolute maximum ratings.

1.4 Recommended operating conditions.

Supply voltage (V_{CC}) - - - - - - - - - - - 4.5 V dc minimum to 5.5 V dc maximum Minimum high level input voltage (V_{IH}) - - - - - 2.0 V dc Maximum low level input voltage (V_{IL}) - - - - - 0.7 V dc Case operating temperature range (T_{C}) - - - - - - - - - - - - - - - 55°C to +125°C

1/ Must withstand the added PD due to short circuit test (e.g., I_{OS}).

2/ When a thermal resistance for this case is specified in MIL-M-38510, appendix C, that value shall supersede the value specified herein.

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2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510

- Microcircuits. General Specification for.

STANDARD

MILITARY

MIL-STD-883

Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

- 3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.
- 3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.
 - 3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.
 - 3.2.2 Truth table. The truth table shall be as specified on figure 2.
 - 3.2.3 Logic diagram. The logic diagram shall be as specified on figure 3.
 - 3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.
- 3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended case operating temperature range.
- 3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.
- 3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.
- 3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.
- 3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

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	1	ABLE I. Electric	al performance characteristic	<u>s</u> .			
				Group A	1.10	mits	Γ
Test	 Symbol 	Co -55°C < unless oth	nditions T _C < +125°C erwTse specified	subgroups	IMin		Unit
High-level input voltage	I VIH			1,2,3	2.0	 	V
Low-level input voltage	VIL			1,2,3		10.7 	V
Input clamp voltage	AIC	V _{CC} = 4,5 V,	$I_{IN} = -18 \text{ mA},$	1		-1.5	I V
High-level output voltage	VOH	V _{CC} = 4.5 V, V _{IL} = 0.7 V,	V _{IH} = 2.0 V, I _{OH} = -3 mA	1,2,3	2.4		V
Low-level output voltage	VOL	VCC = 4.5 V, VIH = 2.0 V,	I _{OL} = 12 mA, VIL = 0.7 V	1,2,3		10.4	V
Off-state output current, high level voltage	T _{OZH}	V _{CC} = 5.5 V, V _{IH} = 2.0 V, V _{IL} = 0.7 V	V _{OH} = 2.7 V,	1,2,3		[40 []	μΑ
applied Off-state output current, low level voltage	I _{OZL}	V _{CC} = 5.5 V, V _{IH} = 2.0 V, V _{IL} = 0.7 V	V _{OL} = 0.4 V,	1,2,3 		-200 	μ Α
applied High-level input current	IIHI	V _{CC} = 5.5 V,	V _{IN} = 5.5 V	1,23		0.1 	l mA
High-level input current, any input	I _{IH2}	V _{CC} = 5.5 V,	V _{IN} = 2.7 V	1,2,3		20	μ A
Low-level input current	IIL	V _{CC} = 5.5 V,	V _{IL} = 0.4 V	1,2,3	0	1-200 i i	İ
Output short circuit current 1/	I ₀ s	V _{CC} = 5.5 V		1,2,3	-40 	-225 	mA
Supply current	Icc	V _{CC} = 5.5 V	Outputs high 01 and 02	1,2,3	Ť	38	l mA
			Outputs low 01	1,2,3		50	mA
			Outputs low 02	1,2,3		1 54	mA
			All outputs disabled 01	1,2,3	1	i 50	mA
			All outputs disabled 02	1,2,3	Ì	54	l mA
Functional tests	 	See 4.4.1(c)		7			

See footnote at end of table.

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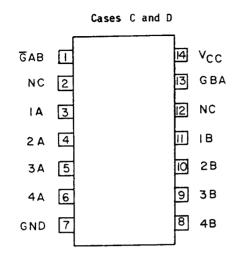
	1	T	Group A	TH	T	
Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	subgroups	Min	Max 	Unit
Propagation delay time	l t _{PLH} 	V _{CC} = 5.0 V, R _L = 110Ω ±5%, C _L = 45 pF ±10%	9,10,11		25	ns
	t _{PHL}	 	9,10,11		30	ns
Enable time	 t _{PZL}	<u> </u> 	9,10,11	 	46	l ns
	I TtpZH I	 	9,10,11	 	36	ns
Disable time	i t _{PLZ} 	<u> </u> 	9,10,11		39	ns
	 tpHZ	+	9,10,11		46	l ns

- L/ Not more than one output should be shorted at a time, and the duration of the short circuit condition should not exceed one second.
- 3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.
 - 4. QUALITY ASSURANCE PROVISIONS
- 4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-M-38510 and method 5005 of MIL-STD-883, except as modified herein.
- 4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:
 - a. Burn-in test (method 1015 of MIL-STD-883).
 - (1) Test condition A, B, C, or D using thed circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

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Case 2

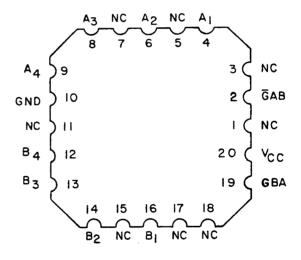


FIGURE 1. Terminal connections.

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Device type 01

:	 Control inputs		port l
GAB	GBA	A	В
Н	н	0	I
L	Н	*	*
Н	L	Isol	lated
L	L	I	0

*Possibly destructive oscillation may occur if the transceivers are enabled in both directions at once. I = Input, O = Inverting output

Device type 02

:	Control inputs		port tus
GAB	GBA	A	B 1
Н	Н	0	I
L	н	*	*
Н	L	Iso	lated
L	L	I	0

*Possibly destructive oscillation may occur if the transceivers are enabled in both directions at once. I = Input, O = Inverting output

FIGURE 2. Truth table.

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Device type 02

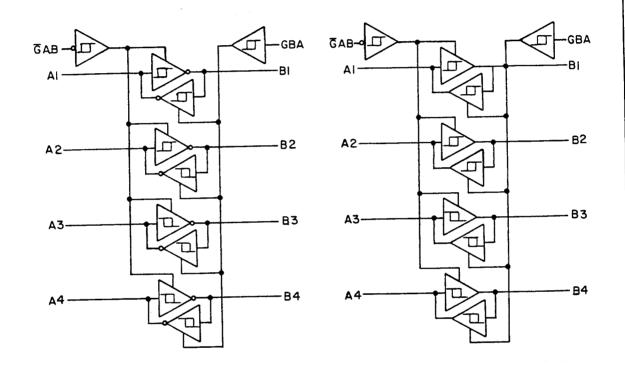


FIGURE 3. Logic diagram.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
 Initial electrical parameters (method 5004)	
Final electrical test parameters (method 5004)	1*, 2, 3, 9
Group A test requirements (method 5005)	1, 2, 3, 7, 9 10, 11**
Groups C and D end-point l electrical parameters l (method 5005)	1, 2, 3

- * PDA applies to subgroup 1.
- ** Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.
- 4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-SID-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.
 - 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 4, 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.
 - c. Subgroup 7 tests shall verify the truth table.
 - 4.3.2 Groups C and D inspections.
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.
 - 5. PACKAGING
 - 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

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6. NOTES

- 6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.
 - 6.2 Replaceability. Replaceability is determined as follows:
 - a. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
 - b. When a QPL source is established, the part numbered devices specified in this drawing will be replaced by the microcircuits identified as part numbers M38510/32801B--.
- 6.3 <u>Comments</u>. Comments on this drawing should be directed to DESC-ECS, Dayton, OH 45444, or telephone 513-296-5375.
- 6.4 Approved sources of supply. Approved sources of supply are listed herein. Additional sources will be added as they become available. The vendors listed herein have agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1</u> / 	Replacement military specification part number
8002001CX <u>2</u> /	01295 04713	 SNJ54LS242J 54LS242BCBJC 	M38510/32801BCX
8002001DX	01295 04713	 SNJ54LS242W 54LS242BDBJC 	M38510/32801BDX
80020012X <u>2</u> /	01295 04713	 SNJ54LS242FK 54LS242B2BJC 	M38510/32801B2X
8002002CX <u>2</u> /	01295 04713	 SNJ 54 LS243J 54LS243BCBX 	M38510/32802BCX
8002002DX	01295 04713	 SNJ54 LS243W 54LS243BDBJC 	M38510/32802BDX
80020022X 2/	01295 04713	 SNJ54LS243FK 54LS242B2BJC 	 M38510/32802B2X

 $[\]frac{1}{a}$ Caution. Do not use this number for item acquisition. Items $\frac{1}{acquired}$ to this number may not satisfy the performance requirements of this drawing.

 $\underline{2}/$ Inactive for new design. Use QPL-38510 product.

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Vendor CAGE vendor name
and address

01295
Texas Instruments, Incorporated
P. O. Box 6448
Midland, TX 79701

04713
Motorola Incorporated
7402 South Price Road
Tempe, AZ 85283

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