C Change to military drawing format. Page 2, delete sink current. Add source for D package. Table I, pages 7 and 8, delete tests $\frac{\Delta V_{IO}}{\Delta T}$, $\frac{\Delta I_{IO}}{\Delta T}$, and t_{LTHR} . Delete minimum limits for $\pm I_{IB}$, V_{OL} , and I_{CEX} . Editorial changes throughout. Change drawing CAGE to 67268. D Delete vendor CAGE 01295. Change test conditions for V_{IO} , I_{IO} , I_{IO} , I_{IB} , V_{STROBE} , and I_{CEX} . Change test limits. Add tests strobe on voltage, strobe off voltage, differential input voltage range, and input voltage range. Add footnote for cases 01CX, 01DX, and 01PX. Add vendor CAGE 27014 for case 01GX. Delete voltage gain test.											RE\	/ISIC	ONS													
Add source for D package. Table 1, pages 7 and 8, delete tests \[\lambda V_{10}, \lambda I_{10}, \text{ and } t_{LTHR}. \] Delete minimum limits for \(\pm I_{1B}, V_{0L}, \) \[\frac{\lambda I}{\lambda T} \] and \(I_{CEX}. \) Editorial changes throughout. Change drawing CAGE to 67268. Delete vendor CAGE 01295. Change test conditions for \(V_{10}, I_{10},	TR .						D	ESC	RIPTI	ON									DATE	(YR-	MO-D/	A)	AP	PRO	VED	•
IIB, VSTROBE, and ICEX. Change test limits. Add tests strobe on voltage, strobe off voltage, differential input voltage range, and input voltage range. Add footnote for cases OICX, OIDX, and OIPX. Add vendor CAGE 27014 for case OIGX.	$\begin{array}{c} \text{Add s} \\ \underline{^{\Delta V}_{IO}}, \\ \underline{^{\Delta T}} \\ \text{and I} \end{array}$	$\frac{\nabla \mathbf{I}}{\nabla \mathbf{I}}$	e fo	or [and	t LTI	:kag: IR:	e. T Del	able ete	e I, min	pag imur	ges n li	/ a: mit:	nd 8 s fo	r ±	I IB	ν ₀	L'		87	AUG	31		M	14	B	104
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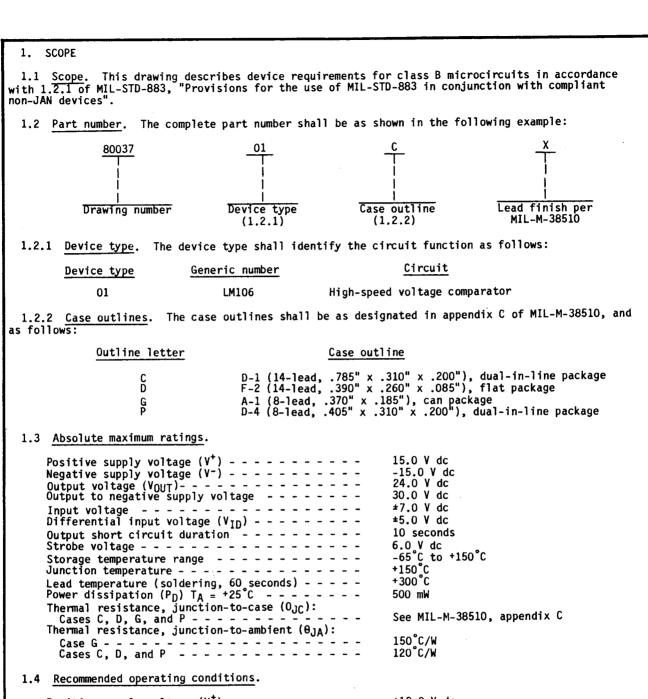
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2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION.

MILITARY

MIL-M-38510

- Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883

Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

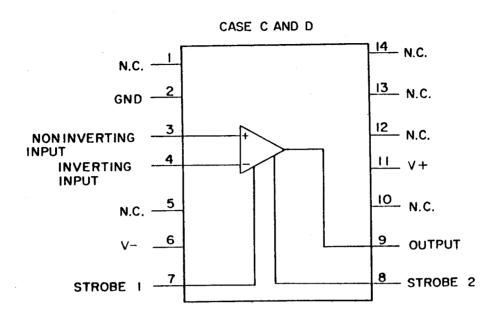
3. REQUIREMENTS

- 3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.
- 3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.
- 3.2.1 Functional diagram and terminal connections. The functional diagram and terminal connections shall be as specified on figure 1.
 - 3.2.2 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.
- 3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full ambient operating temperature range.
- 3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.
- 3.5 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.
- 3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.
- 3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-SID-883 (see 3.1 herein).

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NOTE: N.C.= No connection

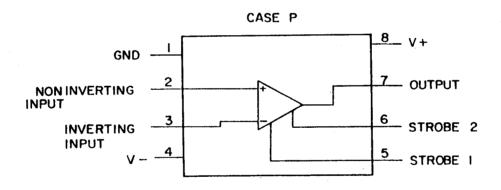
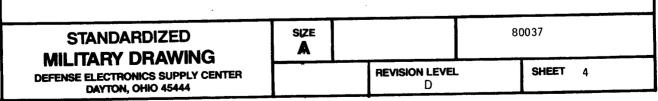
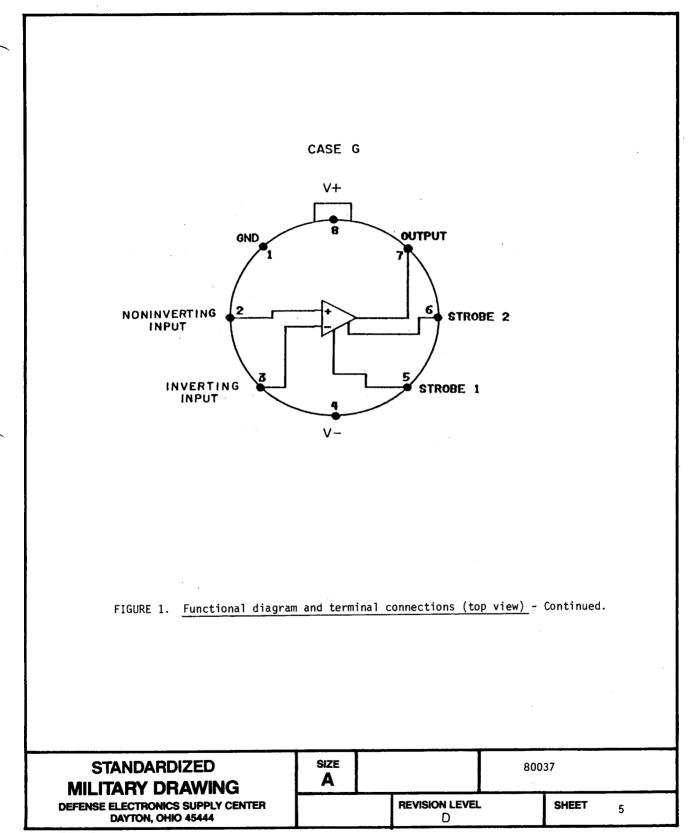


FIGURE 1. Functional diagram and terminal connections (top view).



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Test	 Symbol	· 	Conditions 1/	 Group A	l Lí] 	
1030	 	V ⁺ = 1 -55°(unless	12 V, $V^- = -\overline{6}$ V $C < T_A < +125$ C otherwise specified	subgroups	Min	Max	Unit
Input offset voltage	۷10	V- = -12 V, O. -5 V <u><</u> V _{CM} <u><</u> 5	.5 V ≤ V _{OUT} ≤ 4.4 V	1 2, 3	-2 -3	2	mV
Input offset current	I IO	V = -12 V, 0. -5 V <u><</u> V _{CM} <u>< !</u>	.5 V ≤ V _{OUT} ≤ 4.4 V	1, 2	-3 -7] 3 7	 μ A
Input bias current	±I _{IB}	V = -12 V V _{STROBE} = 2.5	V	1, 2		20	† -
Strobe current	ISTROBE	VSTROBE = 400	mV, V _{ID} = -5 mV	1, 2, 3	 	-3.2	mA
High level output voltage	v _{OH}	I _{OH} = -400 μA,	, V _{ID} ≥ 5 mV	1, 2, 3	2.5	5.5	٧
Low level output voltage	vel output V _{OL}					1.0	v
			T _A = +25°C, I _{OL} = 100	mA 1	 	1.5	
	 		II _{OL} = 16 mA	1, 2, 3	 	0.4	
Output leakage	ICEX	8 V < VOUT < 2 V _{ID} ≥ 5 mV < 2	24 V	1, 3	<u> </u>	1.0	 μΑ
Positive supply current	+ICC	V _{ID} = -5 mV, \	/~ = -12 V	1, 2, 3		10	l mA
Negative supply current	l-I _{CC}			1, 2, 3		 -3.6 	mA
Differential input voltage range	I V _{ID}			1, 2, 3	±5	 	V
Strobe on voltage	V STROBE			1, 2, 3	0.9	 	V
Strobe off voltage	V _Н STROBE			1, 2, 3		2.2	٧
Input voltage range	٧I	-7 V <u>></u> V ⁻ > -1	12 V, V ⁺ = 12 V	1, 2, 3	±5		V
1/ Unless otherwise	specifi	ed, strobes are	at high level for all t	ests.			
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4. QUALITY ASSURANCE PROVISIONS

- 4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).
- 4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:
 - a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
- 4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method $\overline{5005}$ of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.
 - 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 4, 5, 6, 7, 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.
 - 4.3.2 Groups C and D inspections.
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test conditions, method 1005 of MIL-STD-883
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

 MIL-STD-883 test requirements 	Subgroups (per method 5005, table I)
 Interim electrical parameters (method 5004)	
 Final electrical test parameters (method 5004)	1*, 2, 3
Group A test requirements (method 5005)	1, 2, 3
 Groups C and D end-point electrical parameters (method 5005)	1

^{*} PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

- 6.1 <u>Intended use.</u> Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.
 - 6.2 Replaceability. Replaceability is determined as follows:
 - a. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
 - b. When a QPL source is established, the part numbered device specified in this drawing will be replaced by the microcircuit identified as part number M38510/10303B--.
- 6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

 Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1</u> /	Replacement military specification part number
8003701CX	2/	LM106	M38510/10303BCX
8003701DX	2/	LM106	M38510/10303BDX
8003701GX	27014	LM106H/883	M38510/10303BGX
8003701PX	2/	LM106	M38510/10303BPX

 $[\]frac{1}{2}$ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

2/ This case outline no longer available from an approved source.

Vendor CAGE number 27014 Vendor name and address

National Semiconductor 2900 Semiconductor Drive P.O. Box 58090 Santa Clara, CA 95052-8090

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