

REVISIONS																			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED																
B	Page 1, paragraph 1.3; minimum ratings given to supply and input voltage ranges. Table I: Change V <sub>OH</sub> conditions and add footnote to table I. Change drawing CAGE number to 67268. Change to military drawing format.	1988 JAN 26	<i>Mike Lyne</i>																
<p style="text-align: center; font-weight: bold; font-size: 1.2em;">CURRENT CAGE CODE 67268</p>																			

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REV STATUS OF SHEETS	REV	B	B	B	B	B	B	B	B	B									
	SHEET	1	2	3	4	5	6	7	8	9									

PMIC N/A  <div style="text-align: center; font-weight: bold;">STANDARDIZED MILITARY DRAWING</div> <p style="font-size: 0.8em;">THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE</p> <p style="font-weight: bold; font-size: 0.8em;">AMSC N/A</p>	PREPARED BY <i>Christopher A. Rauch</i> CHECKED BY <i>DA Di Cenzo</i> APPROVED BY <i>Mike Lyne</i> DRAWING APPROVAL DATE 2 June 1981 REVISION LEVEL B	<div style="text-align: center; font-weight: bold;">DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444</div> <hr/> MICROCIRCUIT, DIGITAL, LOW POWER SCHOTTKY TTL, VOLTAGE CONTROLLED OSCILLATOR, MONOLITHIC SILICON
	SIZE <b>A</b>	CAGE CODE <b>14933</b>
	SHEET    1       OF       9	

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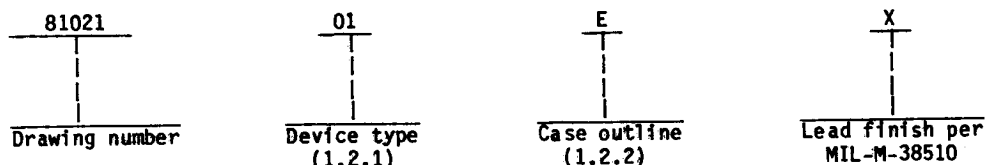
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5962-E620

DISTRIBUTION STATEMENT A.    Approved for public release; distribution is unlimited.

## 1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	54LS629	Dual voltage controlled oscillator

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

<u>Outline letter</u>	<u>Case outline</u>
E	D-2 (16-lead, 1/4" x 7/8"), dual-in-line package
F	F-5 (16-lead, 1/4" x 3/8"), flat package
2	C-2 (20-terminal, .350" x .350"), square chip carrier package

### 1.3 Absolute maximum ratings.

Supply voltage range, $V_{CC}$ 1/	- - - - -	-0.5 V dc to +7.0 V dc
Input voltage range:		
Enable input	- - - - -	-1.5 V dc to +7.0 V dc
Frequency control or range input	- - - - -	$V_{CC}$
Storage temperature range	- - - - -	-65°C to +150°C
Maximum power dissipation ( $P_D$ ) 2/	- - - - -	303 mW
Lead temperature (soldering, 10 seconds)	- - - - -	+300°C
Thermal resistance, junction-to-case ( $\theta_{JC}$ )	- - - - -	See MIL-M-38510, appendix C
Junction temperature ( $T_J$ )	- - - - -	+175°C

#### 1.4 Recommended operating conditions.

Supply voltage ( $V_{CC}$ )	- - - - -	4.5 V dc minimum to 5.5 V dc maximum
Minimum high level input voltage ( $V_{IH}$ )	- - - - -	2.0 V dc
Maximum low level input voltage ( $V_{IL}$ )	- - - - -	0.7 V dc
Input voltage at frequency control or range input, ( $V_{I(freq)}$ or $V_{I(rng)}$ ) $\frac{3}{-}$	- - - - -	0.0 V dc minimum to 5.0 V dc maximum
Output frequency ( $f_o$ )	- - - - -	1 Hz to 20 MHz
Case operating temperature range ( $T_r$ )	- - - - -	-55°C to +125°C

- 1/ Symbol  $V_{CC}$ , is used for the voltage applied to both the  $V_{CC}$  and  $\ominus V_{CC}$  terminals unless otherwise noted.
- 2/ Must withstand the added  $P_D$  due to short circuit test, e.g.,  $I_{OS}$ .
- 3/ Voltage values are with respect to the appropriate ground terminal.

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## 2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

### SPECIFICATION

#### MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

### STANDARD

#### MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

## 3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Logic diagram. The logic diagram shall be specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full case operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-833 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T <sub>C</sub> ≤ +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
High level output voltage	V <sub>OH</sub>	V <sub>CC</sub> = 4.5 V; V <sub>IH</sub> = 2.0 V I <sub>OH</sub> = -1.2 mA V <sub>IL</sub> = 0.7 V	1,2,3	2.5		V
Low level output voltage	V <sub>OL</sub>	V <sub>CC</sub> = 4.5 V I <sub>OL</sub> = 12 mA V <sub>IH</sub> = 2.0 V V <sub>IL</sub> = 0.7 V	1,2,3		0.4	V
Input clamp voltage	V <sub>IC</sub>	V <sub>CC</sub> = 4.5 V; I <sub>IN</sub> = -18 mA T <sub>C</sub> = +25°C	1		-1.5	V
High level input current	I <sub>IH1</sub>	V <sub>CC</sub> = 5.5 V; V <sub>IH</sub> = 2.7 V	1,2,3		40	μA
	I <sub>IH2</sub>	V <sub>CC</sub> = 5.5 V; V <sub>IH</sub> = 7.0 V			200	μA
Low level input current	I <sub>IL</sub>	V <sub>CC</sub> = 5.5 V; V <sub>IL</sub> = 0.4 V	1,2,3		-0.8	mA
Short circuit output current	I <sub>OS</sub>	V <sub>CC</sub> = 5.5 V V <sub>OUT</sub> = GND <u>1/</u>	1,2,3	-40	-225	mA
Supply current	I <sub>CC</sub>	V <sub>CC</sub> = 5.5 V, Enable = 4.5 V	1,2,3		55	mA
Functional tests		See 4.3.1c	7			
Output frequency	f <sub>o</sub>	V <sub>CC</sub> = 5.0 V R <sub>L</sub> = 667Ω ±5%, C <sub>L</sub> = 45 pF ±10% C <sub>ext</sub> = 50 pF <u>2/</u> <u>3/</u>	9,10,11	15	25	MHz
		V <sub>I</sub> (freq) = 5 V, V <sub>I</sub> (rng) = 0 V  V <sub>I</sub> (freq) = 1 V, V <sub>I</sub> (rng) = 5 V		1.1	2.1	MHz

- 1/ Not more than one output should be shorted at a time, and the duration of the short circuit condition should not exceed one second.
- 2/ Frequency testing may be performed using either C<sub>L</sub> = 45 pF or C<sub>L</sub> = 50 pF. However, the manufacturer must certify and guarantee that the microcircuits meet the switching test limits specified for a 50 pF load.
- 3/ Sample testing of output frequency may be performed.

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3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

#### 4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2)  $T_A = +125^{\circ}\text{C}$ , minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

##### 4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroup 7 tests shall verify the truth table.

##### 4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test conditions, method 1005 of MIL-STD-883):

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2)  $T_A = +125^{\circ}\text{C}$ , minimum.

(3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

#### 5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

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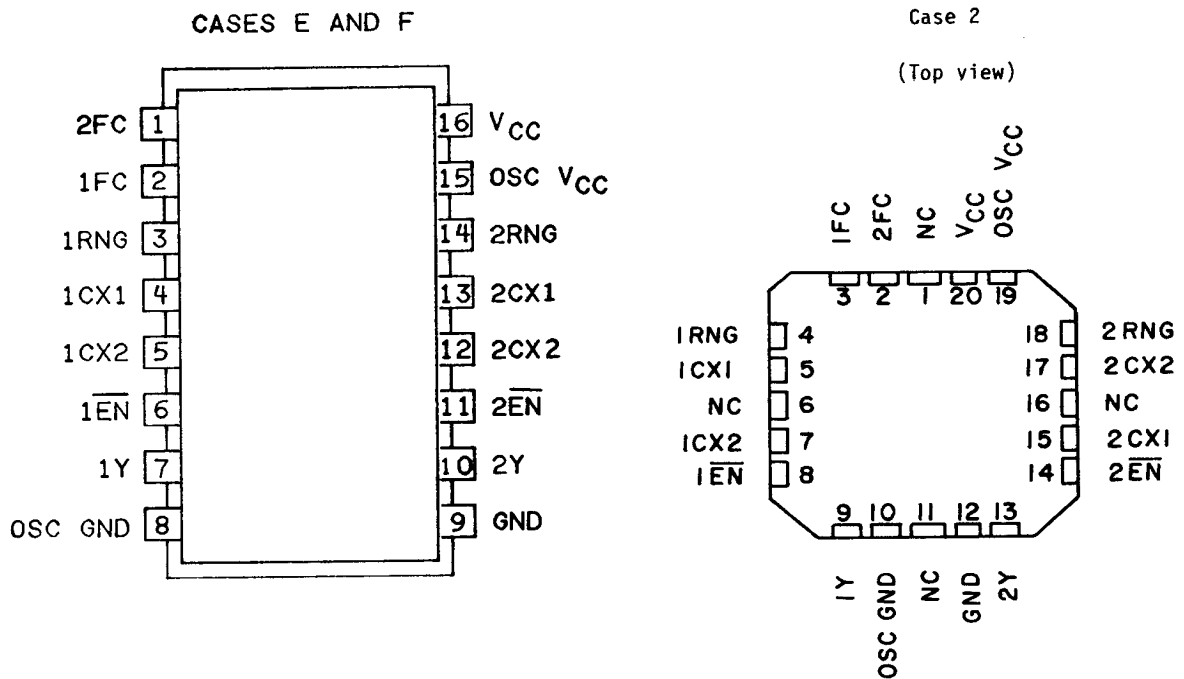


FIGURE 1. Terminal connections.

Enable	Y
L	Enabled
H	H

Caution: Crosstalk may occur in this device if both VCO's are operated simultaneously.

FIGURE 2. Truth table.

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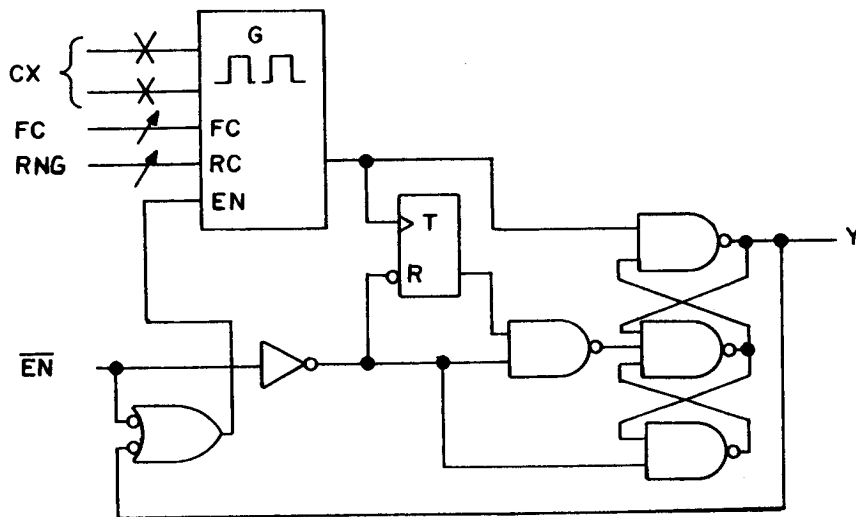


FIGURE 3. Logic diagram.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*, 2, 3, 9
Group A test requirements (method 5005)	1, 2, 3, 7, 9, 10, 11**
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

\*PDA applies to subgroup 1.

\*\*Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

#### 6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1/</u>
8102101EX	01295	SNJ54LS629J
8102101FX	01295	SNJ54LS629W
81021012X	01295	SNJ54LS629FK

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE  
number

01295

Vendor name  
and address

Texas Instruments Inc.  
P.O. Box 6448  
Midland, TX 79701

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