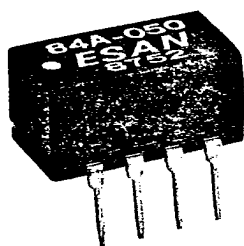


- Dual In/Out Delay
- 8 Pin Din Package
- TTL Compatible



Specifications:

- Delay tolerance : $\pm 5\%$ or 2ns
whichever is greater
- Rise time : 4ns max.
- Temp. coefficient : 100 ppm/°C
- Operating Temp. Range : -0°C to 70°C
- Minimum Pulse Width : 40% of total delay
- Supply Voltage : $5\text{Vdc} \pm 5\%$
- Logic 1 Input current : 50 μA max.
- Logic 0 Input current : -2 mA max.
- Logic 1 Output : 2.75V min.
- Logic 0 Output : 0.4V max.
- Logic 1 Fan out : 20/TAP max.
- Logic 0 Fan out : 10/TAP max.
- Power Dissipation : 375 mW max.

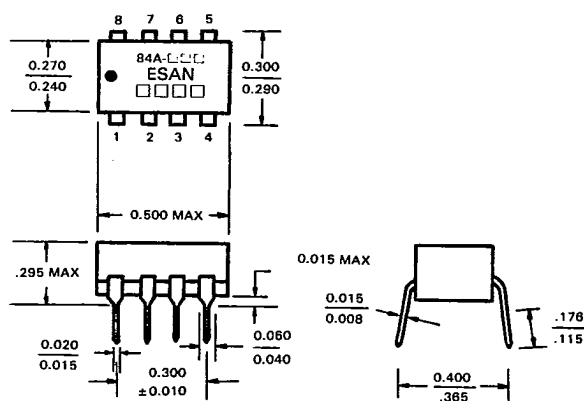
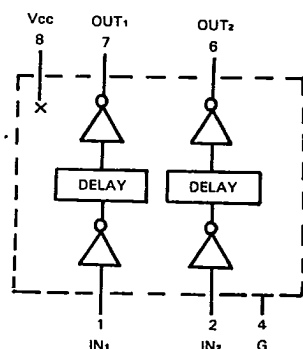
Test Conditions:

- Input pulse voltage : 3.2V
- Input pulse width : $1.2 \times$ total delay
- Input pulse spacing : $2 \times$ total delay
- Input pulse rise time : 2 ns
- Rise time : 0.75V to 2.4V
- Time delay measured : @ 1.5V level
- All measurement : @ $V_{cc} = 5.0\text{V}$, $T_A = 25^{\circ}\text{C}$

Features:

- Schottky TTL interface
- Low power schottky available named "84AL- xxx" series
- Both Leading and Trailing edge available under request named "84B- xxx" series

PART NO.	DELAY TIME (ns)
84A-025	25
84A-050	50
84A-075	75
84A-100	100
84A-125	125
84A-150	150
84A-200	200
84A-250	250
84A-300	300
84A-400	400
84A-500	500



Unit: Inch