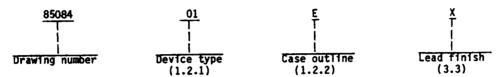
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|                           |           |       |         |          |       |     |       |        |      |          |               |       |       |           |                |
|                           |           |       |         |          |       |     |       |        |      |          |               |       |       |           |                |
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|                           |           |       |         |          |       |     |       |        |      |          |               |       |       |           |                |
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|                           |           |       |         |          |       |     |       |        |      |          |               |       |       |           |                |
| Prepared in accordance    | with DOD  | -STD- | 100     |          |       |     |       | · · ·  | Sel  | ected    | item          | drawi | ing   |           | <del></del>    |
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| Original date of drawing: | PREPA     | d l   | BY<br>/ | D.       |       | 4.  |       | DEFE   | MSE  |          | RONIE<br>Ton, |       | PPLY  | CENT      | ER             |
| 28 September 1985         | CHECK     |       | -       | Gn<br>Gn |       |     | 1     | N-PO   | MI(  | CROCI    | RCUITS        | , DIG | SITAL | , ADV     | ANCED<br>IVERS |
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| 1. | SCC | \PF |
|----|-----|-----|

- 1.1 Scope. This drawing describes the requirements for monolithic silicon, advanced low-power Schottky TTL, decoder/driver microcircuits. This drawing provides for a level of microcircuit quality and reliability assurance for acquisition of microcircuits in accordance with MIL-M-38510.
  - 1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type Generic number Circuit

O1 54LS145 BCD-to-decimal decoder/driver

1.2.2 <u>Case outlines</u>. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter

E
D-2 (16-lead, 1/4" x 7/8"), dual-in-line package
F
F-5 (16-lead, 1/4" x 3/8"), flat package
C-2 (20-terminal, .350" x .350"), square chip
carrier package

1.3 Absolute maximum ratings.

1.4 Recommended operating conditions.

Supply voltage ( $V_{CC}$ ) - - - - - - - - - - +4.5 V dc minimum to +5.5 V dc maximum Minimum high level input voltage ( $V_{IL}$ ) - - - - 2.0 V dc Maximum low level input voltage ( $V_{IL}$ ) - - - - - 0.7 V dc Case operating temperature range ( $V_{CC}$ ) - - - - - - - - - - - - - - 55°C to +125°C

1/ When a thermal resistance for this case is specified in MIL-M-38510, appendix C, that value shall supersede the value indicated herein.

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## 2. APPLICABLE DOCUMENTS

## 2.1 Government documents.

2.1.1 Specification and standard. The following specification and standard form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Department of Defense Index of Specifications and Standards and supplement thereto, cited in the solicitation.

SPECIFICATION

**MILITARY** 

MIL-M-38510

- Microcircuits, General Specification for.

STANDARD

**MILITARY** 

MIL-STD-883

- Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

## 3. REQUIREMENTS

- 3.1 Item requirements. The individual item requirements shall be in accordance with MIL-M-38510, and as specified herein. The country of manufacture requirement of MIL-M-38510 does not apply.
- 3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.
- 3.2.1 Design documentation. The design documentation shall be in accordance with MIL-M-38510 and, unless otherwise specified in the contract or purchase order, shall be retained by the manufacturer but be available for review by the acquiring activity or contractor upon request.
  - 3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.
  - 3.2.3 Logic diagram. The logic diagram shall be as specified on figure 2.
  - 3.2.4 Truth table. The truth table shall be as specified on figure 3.
  - 3.2.5 Case outlines. The case outlines shall be in accordance with 1.2.2.
  - 3.3 Lead material and finish. The lead material and finish shall be in accordance with MIL-M-38510.
- 3.4 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended case operating temperature range.
- 3.5 Marking. Marking shall be in accordance with MIL-M-38510 except the part number shall be in accordance with 1.2 herein. The Vendor Similar Part Number may also be marked in accordance with 6.8 herein. Both part numbers, when used, shall be printed on the same surface. The "M38510/XXX" part number and the "JAN" or "J" mark shall not be used. Lead finish letter "X" is used only as specified in MIL-M-38510 and shall not be marked on the microcircuit or its packaging. The country of origin shall be marked on the microcircuit.

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| TABLE | I. | Electrical | performance | char | acteri | stics. |
|-------|----|------------|-------------|------|--------|--------|
|       |    |            |             |      |        |        |

|                                    |                 | Conditions  |                           | Lim  |               |              |
|------------------------------------|-----------------|---|---------------------------|--|---------------|--------------|
| Test                               | Symbol i        | $-55^{\circ}\text{C} < \text{T}_{\text{C}} < +125^{\circ}\text{C}$ , unless otherwise specified       | Group A<br> subgroups<br> | Min  | Max           | Unit         |
| Off-state output voltage           | VO(off)         |   | 1, 2, 3                   |  | 15            | ٧            |
| On-state output<br>voltage         | VO(on)<br>      | V <sub>CC</sub> = 4.5 V I <sub>OL</sub> = 12 mA<br>V <sub>IL</sub> = 0.7 V<br>V <sub>IN</sub> = 2.0 V | 1, 2, 3                   |  | 0.4           | ٧            |
| Input clamp<br>voltage             | V <sub>IC</sub> | V <sub>CC</sub> = 4.5 V I <sub>IN</sub> = -18 mA<br>T <sub>C</sub> = +25°C                            | 1                         |  | -1.5          | ٧            |
| Low-level input<br>current         | IIIL            | VCC = 5.5 V<br>VIN = 0.4 V  | 1, 2, 3                   |  | <br> -0.4<br> | raA          |
| High-level input<br>current        | IIIH1           | YCC = 5.5 V<br>  YIN = 2.7 V  | 1, 2, 3                   |  | 20            | μ <b>A</b>   |
|                                    | I IH2           | Y <sub>CC</sub> = 5.5 Y<br>  Y <sub>IN</sub> = 7.0 Y  | 1, 2, 3                   | !<br> <br> <br>                                  | 0.1           | mA           |
| Off-state output<br>current        | IO(off)         | V <sub>CC</sub> = 4.5 V   | 1, 2, 3                   | <br> <br>  | 250           | l μ <b>A</b> |
| Supply current                     | Icc             | <br> Y <sub>CC</sub> = 5.5 Y<br> ATT inputs grounded<br> ATT outputs open                             | 1, 2, 3                   | <br> <br>  | 13            | mA<br> <br>  |
| Propagation delay time, low to     | tpLH            | 1 Vcc = 5.0 V   | 9                         | 1  | 50            |              |
| high level<br>output               | '-"             | RL = 665 \( \alpha \text{ \frac{\pi}{2}} \)  RL = 50 pF \( \alpha \text{ \frac{\pi}{2}} \)            | 10,11                     | <del> </del>                                     | 70            | l ns<br>I    |
| Propagation delay<br>time, high to | tpHL            | Ť<br>!  | 9                         | 1  | 50            | l<br>I ns    |
| low level                          | 1               | !<br>}  | 10,11                     | <del>                                     </del> | 70            |              |

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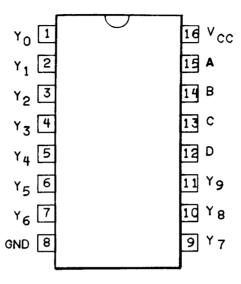
- 3.6 Quality assurance requirements. Microcircuits furnished under this drawing shall have been subjected to, and passed all the requirements, tests, and inspections detailed herein including screening and quality conformance inspections.
- 3.6.1 Screening. Screening shall be in accordance with method 5004, class B, of MIL-STD-883 and 4.2 herein.
- 3.6.2 Qualification. Qualification inspection for the device type specified herein shall not be required.
- 3.6.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-M-38510 and 4.4 herein.
- 3.7 Manufacturer eligibility. To be eligible to supply microcircuits to this drawing, a manufacturer shall have manufacturer certification in accordance with MIL-M-38510 for at least one line and have Part I listing on Qualified Products List QPL-38510 for at least one device type (not necessarily the one for which the acquisition of this drawing is to apply).
- 3.8 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply (see 6.7 and 6.8).
  - 4. QUALITY ASSURANCE PROVISIONS
- 4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-M-38510 and method 5005 of MIL-STD-883, except as modified herein.
- 4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:
  - a. Burn-in test (method 1015 of MIL-STD-883).
    - (1) Test condition A, B, C, or D.
    - (2)  $T_A = +125^{\circ}C$ , minimum.
  - b. Initial and final electrical test parameters shall be as specified in table II herein, except initial electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
  - c. The percent defective allowable (PDA) shall be as specified in MIL-M-38510.
- 4.3 Qualification inspection. Qualification inspection for the device type specified herein shall not be required.
- 4.4 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-M-38510 and method 5005 of MIL-STD-883. Groups A and B inspections shall be performed on each inspection lot or as specified in method 5005 of MIL-STD-883. Groups C and D shall be performed on a periodic basis in accordance with MIL-M-38510. Generic test data (see 6.5) may be used to satisfy the requirements for groups C and D inspections. Manufacturers shall keep lot records for 5 years (minimum), monitor for compliance to the prescribed procedures, and observe that satisfactory manufacturing conditions and records on lots are maintained for these devices. The records, including an attributes summary of all screening and quality conformance inspections conducted on each lot shall be available for review by customers at all times.

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Case 2

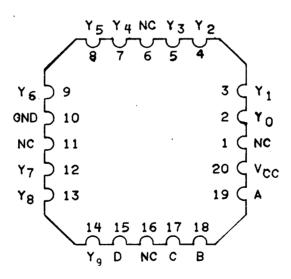
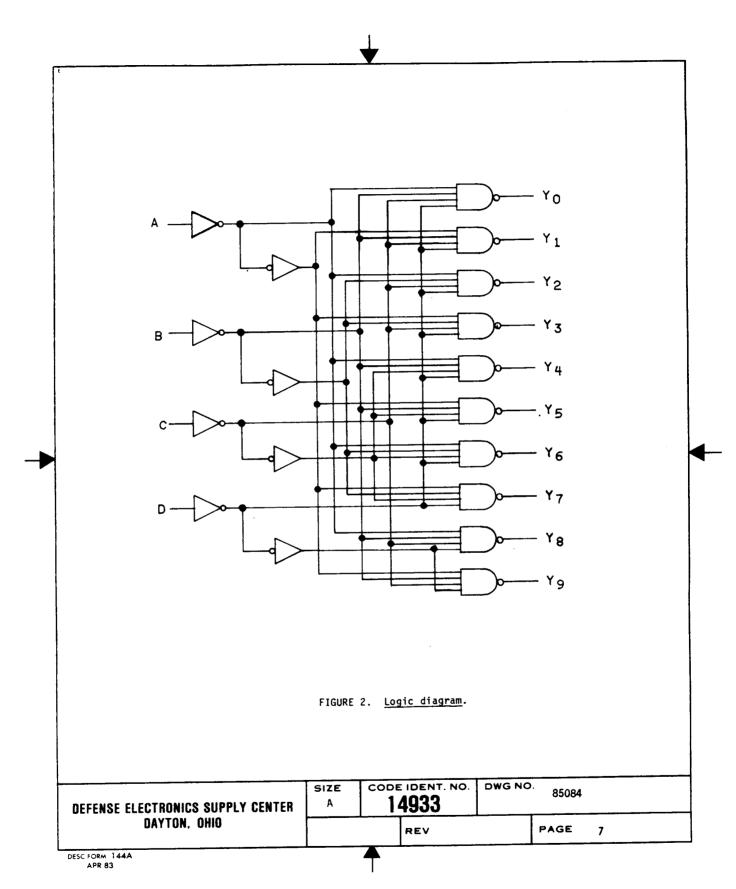


FIGURE 1. Terminal connections (top view).

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| No.      |   | INP | UTS |    |   |   |   |   | OUT | PUTS |   |   |   |   |
|----------|---|-----|-----|----|---|---|---|---|-----|------|---|---|---|---|
| "0.      | D | С   | В   | A  | 0 | 1 | 2 | 3 | 4   | 5    | 6 | 7 | 8 | 9 |
| 0        | L | L   | L   | L  | L | Н | H | Н | Н   | Н    | Н | Н | Н | Н |
| 1        | L | L   | L   | Н  | н | L | Н | Н | Н   | Н    | Н | Н | Н | Н |
| 2        | L | L   | Н   | L  | Н | Н | L | H | Н   | H    | Н | H | H | H |
| 3        | L | L   | Н   | Н  | Н | Н | Н | L | Н   | Н    | H | Н | Н | Н |
| 4        | L | Н   | L   | L  | Н | Н | Н | Н | L   | Н    | Н | Н | Н | H |
| 5        | L | Н   | L   | Н  | Н | Н | Н | Н | Н   | L    | Н | Н | Н | Н |
| 6        | L | Н   | Н   | L. | н | Н | Н | Н | Н   | Н    | L | Н | H | Н |
| 7        | L | Н   | Ħ   | Н  | н | Н | Н | Н | Н   | Н    | Н | L | Н | Н |
| 8        | Н | L   | L   | L  | н | Н | Н | Н | Н   | Н    | Н | Н | L | H |
| 9        | Н | L   | L   | Н  | Н | Н | Н | Н | Н   | Н    | Н | Н | H | L |
|          | Н | L   | Н   | L  | Н | Н | Н | Н | Н   | Н    | Н | Н | Н | Н |
|          | Н | L   | H   | Н  | Н | Н | Н | Н | Н   | Н    | Н | Н | Н | H |
| 017      | H | Н   | L   | L  | Н | Н | Н | Н | Н   | Н    | Н | Н | Н | н |
| INVAL ID | Н | H   | L   | Н  | н | H | Н | Н | Н   | Н    | Н | H | Н | н |
| =        | Н | Ħ   | H   | L  | Н | H | Н | Н | Н   | Н    | Н | Н | H | н |
|          | Н | Н   | Н   | Н  | Н | Н | Н | Н | Н   | Н    | Н | Н | Н | н |

H = high level (off), L = low level (on)

FIGURE 3. <u>Truth table</u>.

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## TABLE II. Electrical test requirements.

| MIL-STD-883 test requirements                                       | Subgroups  <br>  (per method  <br>  5005, table I) |
|---|--|
| Initial electrical parameters<br>(pre burn-in) (method 5004)        |  |
| Final electrical test parameters<br> (method 5004)                  | 1*, 2, 3, 9  |
| Group A test requirements<br>(method 5005)                          | 1, 2, 3, 7, 9                                      |
| Groups C and D end-point<br>electrical parameters<br>(method 5005)  | 1, 2, 3  |
| Additional electrical subgroups<br>for group C periodic inspections | 10, 11**   |

- \* PDA applies to subgroup 1 (see 4.2c).
- \*\* Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.
- 4.4.1 Group A inspection. Group A inspection shall consist of the test subgroups and LTPD values shown in table I of method 5005 of MIL-STD-883, class B, and as follows:
  - a. Tests shall be as specified in table II herein.
  - b. Subgroups 4, 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.
  - c. Subgroup 7 tests sufficiently to verify the truth table.
- 4.4.2 Group B inspection. Group B inspection shall consist of the test subgroups and LTPD values shown in table IIb of method 5005 of MIL-STD-883, class B.
- 4.4.3 Groups C and D inspections. Groups C and D inspections shall consist of the test subgroups and LTPD values shown in tables III and IV of method 5005 of MIL-STD-883, class B, and as follows:
  - a. End-point electrical parameters shall be as specified in table II herein.
  - b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
    - (1) Test condition A, B, C, or D.
    - (2)  $T_A = +125$ °C, minimum.
    - (3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.
  - 5. PACKAGING
- 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

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- 6.1 Notes. Only the note "Reevaluation of lot quality" of the notes specified in MIL-M-38510 shall apply to this drawing.
- 6.2 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. This drawing is intended exclusively to prevent the proliferation of unnecessary duplicate specifications, drawings, and stock catalog listings. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, this drawing will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.
  - 6.3 Ordering data. The acquisition document should specify the following:
    - a. Complete part number (see 1.2).
    - b. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
    - c. Requirements for certificate of compliance, if applicable.
    - d. Requirements for notification of change of product or process to contracting activity, if applicable.
    - e. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements shall not affect the part number. Unless otherwise specified, these requirements will not apply to direct shipment to the Government.
- 6.4 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- 6.5 Generic test data. Generic test data may be used to satisfy the requirements of 4.4.3. Group C generic test data shall be on date codes no more than 1 year old and on a die in the same microcircuit group (see appendix E of MIL-M-38510) with the same material, design and process and from the same plant as the die represented. Group D generic data shall be on date codes no more than 1 year old and on the same package type (terms, definitions, and symbols of MIL-M-38510) and from the same plant as the package represented. The vendor is required to retain the generic data for a period of not less than 5 years from the date of shipment.
- 6.6 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, OH 45444, or telephone 513-296-5375.
- 6.7 Submission of certificate of compliance. The certificate of compliance submitted to DESC-ECS, prior to listing as an approved source of supply in 6.8, shall state that the manufacturer's product meets the requirements herein.

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6.8 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.8) has been submitted to DESC-ECS.

| DESC drawing part number | Vendor<br>  FSCM<br>  number | Vendor<br>  similar part<br>  number |
|--------------------------|------------------------------|--------------------------------------|
| 8508401EX                | 01295                        | <br> SNJ54LS145J                     |
| 8508401FX                | 01295                        | SNJ54LS145W                          |
| 85084012X                | 01295                        | SNJ54LS145FK                         |

Vendor FSCM number

Vendor name and address

01295

Texas Instruments, Inc. P.O. Box 6448 Midland, TX 79701

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