POWER OPERATIONAL AMPLIFIERS

PA51 • PA51A

HTTP://WWW.APEXMICROTECH.COM (800) 546-APEX (800) 546-2739

FEATURES

- WIDE SUPPLY RANGE ±10 to ±40V
- HIGH OUTPUT CURRENT ±10A Peak
- SECOND SOURCEABLE OPA501, 8785
- CLASS "C" OUTPUT Low Cost
- LOW QUIESCENT CURRENT 2.6mA

APPLICATIONS

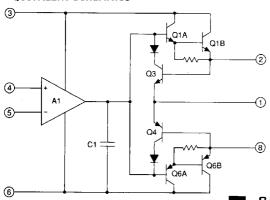
- DC SERVO AMPLIFIER
- MOTOR/SYNCHRO DRIVER
- VALVE AND ACTUATOR CONTROL
- DC OR AC POWER REGULATOR

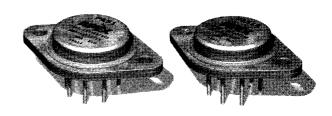
DESCRIPTION

The PA51 and PA51A are high voltage, high output current operational amplifiers designed to drive resistive, inductive and capacitive loads. Their complementary common emitter output stage is the simple class C type optimized for low frequency applications where crossover distortion is not critical. These amplifiers are not recommended for audio, transducer or deflection coil drive circuits. The safe operating area (SOA) is fully specified and can be observed for all operating conditions by selection of user programmable current limiting resistors. Both amplifiers are internally compensated for all gain settings. For continuous operation under load, mounting on a heatsink of proper rating is recommended. Do not use isolation washers!

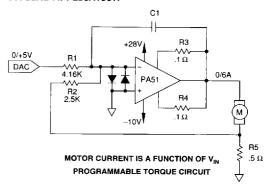
This hybrid integrated circuit utilizes thick film conductors, ceramic capacitors and semiconductor chips to maximize reliability, minimize size and give top performance. Ultrasonically bonded aluminum wires provide reliable interconnections at all operating temperatures. The 8-pin TO-3 package is electrically isolated and hermetically sealed. The use of compressible thermal washers and/or improper mounting torque will void the product warranty. Please see "General Operating Considerations".

EQUIVALENT SCHEMATIC



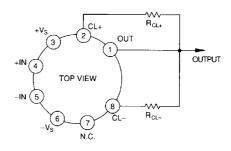


TYPICAL APPLICATION



The linear relationship of torque output to current input of the modern torque motor makes this simple control circuit ideal for many material processing and testing applications. The sense resistor develops a feedback voltage proportional to motor current and the small signal properties of the Power Op Amp insure accuracy. With this closed loop operation, temperature induced impedance variations of the motor winding are automatically compensated.

EXTERNAL CONNECTIONS



0878636 0002422 719

PA51 • PA51A

ABSOLUTE MAXIMUM RATINGS

80V SUPPLY VOLTAGE, +Vs to -Vs 10A OUTPUT CURRENT, within SOA POWER DISSIPATION, internal 97W $\pm V_s - 3V$ INPUT VOLTAGE, differential INPUT VOLTAGE, common mode ±۷s 200°C TEMPERATURE, junction1 TEMPERATURE, pin solder -10s TEMPERATURE RANGE, storage 300°C -65 to +150°C OPERATING TEMPERATURE RANGE, case -55 to +125°C

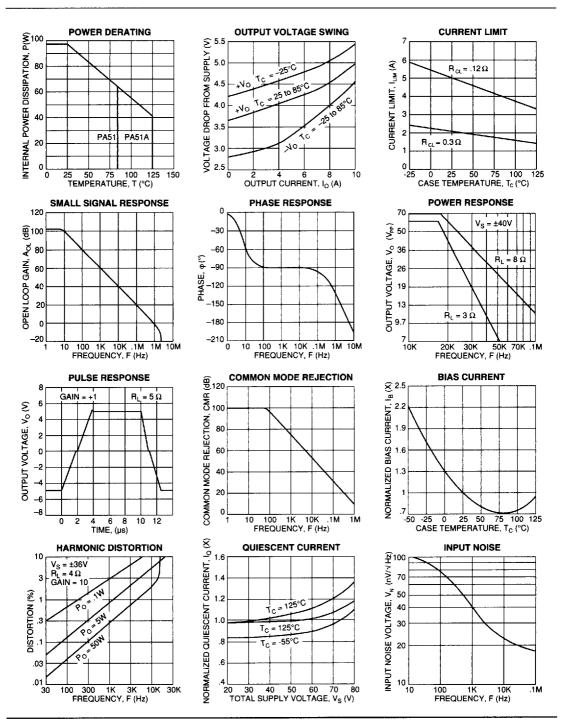
SPECIFICATIONS			PA51			PA51A		
PARAMETER	TEST CONDITIONS 2, 5	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
INPUT								
OFFSET VOLTAGE, initial OFFSET VOLTAGE, vs. temperature OFFSET VOLTAGE, vs. supply OFFSET VOLTAGE, vs. power BIAS CURRENT, initial BIAS CURRENT, vs. temperature BIAS CURRENT, vs. supply OFFSET CURRENT, initial OFFSET CURRENT, vs. temperature INPUT IMPEDANCE, common mode INPUT IMPEDANCE, differential INPUT CAPACITANCE COMMON MODE VOLTAGE RANGE ³ COMMON MODE REJECTION, DC ³	$\begin{split} &T_{\text{C}} = 25^{\circ}\text{C} \\ &\text{Full temperature range} \\ &T_{\text{C}} = 25^{\circ}\text{C} \\ &T_{\text{C}} = 25^{\circ}\text{C} \\ &T_{\text{C}} = 25^{\circ}\text{C} \\ &T_{\text{C}} = 25^{\circ}\text{C} \\ &\text{Full temperature range} \\ &T_{\text{C}} = 25^{\circ}\text{C} \\ &\text{Full temperature range} \\ &T_{\text{C}} = 25^{\circ}\text{C} \\ &T_{\text{C}} = 25^{\circ}\text{C}, V_{\text{CM}} = \pm V_{\text{S}} - 6V \end{split}$	±V _s -6	±5 ±10 ±35 ±20 ±15 ±.05 ±.02 ±5 ±.01 250 10 3 ±V _s -3 110	±10 ±65 ±40 ±12	* 80	±2	±5 ±40 ±20 ±3	mV μV/°C μV/V μV/W nA °C nA/°C nA/°C nA/°C MΩ pF V dB
GAIN								
OPEN LOOP GAIN at 10Hz GAIN BANDWIDTH PRODUCT @ 1MH: POWER BANDWIDTH PHASE MARGIN	Full temp. range, full load $T_c = 25^{\circ}C$, full load $T_c = 25^{\circ}C$, $I_0 = 8A$, $V_0 = 40V_{PP}$ Full temperature range	94	115 1 16 45		*	*		dB MHz kHz
OUTPUT								
VOLTAGE SWING ³ VOLTAGE SWING ³ VOLTAGE SWING ³ CURRENT SETTLING TIME to .1% SLEW RATE CAPACITIVE LOAD, unity gain CAPACITIVE LOAD, gain > 4	$\begin{array}{l} T_{_{C}}=25^{\circ}\text{C, }l_{_{O}}=10\text{A} \\ \text{Full temp. range, }l_{_{O}}=4\text{A} \\ \text{Full temp. range, }l_{_{O}}=68\text{mA} \\ T_{_{C}}=25^{\circ}\text{C} \\ T_{_{C}}=25^{\circ}\text{C, 2V step} \\ T_{_{C}}=25^{\circ}\text{C, R}_{_{L}}=6\Omega \\ \text{Full temperature range} \\ \text{Full temperature range} \end{array}$	±V _s -8 ±V _s -6 ±V _s -6 ±10	±V _s -5 ±V _s -4	1.5 SOA	•	*		V V A μs V/μs nF
POWER SUPPLY								
VOLTAGE CURRENT, quiescent	Full temperature range T _c = 25°C	±10	±28 2.6	±36 10	•	±34	±40	mA
THERMAL								
RESISTANCE, AC, junction to case* RESISTANCE, DC, junction to case RESISTANCE, junction to air TEMPERATURE RANGE, case	F > 60Hz F < 60Hz Meets full range specifications	-25	1.0 1.5 30	1.2 1.8 +85	-55	*	+125	.CVM .CVM

NOTES:

- The specification of PA51A is identical to the specification for PA51 in applicable column to the left.
- Long term operation at the maximum junction temperature will result in reduced product life. Derate internal power dissipation to achieve high MTTF.
- 2. The power supply voltage specified under the TYP rating applies unless otherwise noted as a test condition.
- 3. $+V_s$ and $-V_s$ denote the positive and negative supply rail respectively. Total V_s is measured from $+V_s$ to $-V_s$.
- 4. Rating applies if the output current alternates between both output transistors at a rate faster than 60Hz.
- Full temperature range specifications are guaranteed but not 100% tested.

CAUTION

The internal substrate contains beryllia (BeO). Do not break the seal. If accidentally broken, do not crush, machine, or subject to temperatures in excess of 850°C to avoid generating toxic fumes.



PA51 • PA51A

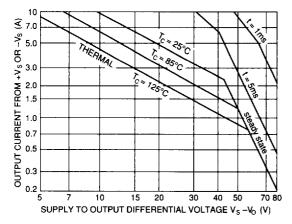
GENERAL

Please read the "General Operating Considerations" section, which covers stability, supplies, heatsinking, mounting, current limit, SOA interpretation, and specification interpretation. Additional information can be found in the application notes. For information on the package outline, heatsinks, and mounting hardware, consult the "Accessory and Package Mechanical Data" section of the handbook.

SAFE OPERATING AREA (SOA)

The output stage of most power amplifiers has three distinct limitations:

- The current handling capability of the transistor geometry and the wire bonds.
- The second breakdown effect which occurs whenever the simultaneous collector current and collector-emitter voltage exceeds specified limits.
- 3. The junction temperature of the output transistors.



The SOA curves combine the effect of of all limits for this Power Op Amp. For a given application, the direction and magnitude of the output current should be calculated or measured and checked against the SOA curves. This is simple for resistive loads but more complex for reactive and EMF generating loads. The following guidelines may save extensive analytical efforts.

Under transient conditions, capacitive and dynamic* inductive loads up to the following maximums are safe:

C/	APACITIVE L	INDUCTIVE LOAD			
$\pm V_s$	$I_{LIM} = 5A$	$I_{LIM} = 10A$	$I_{LIM} = 5A$	I _{LIM} = 10A	
40V	400μF	200μF	11mH	4.3mH	
35V	800µF	400μF	20mH	5.0mH	
30V	1,600μF	800μF	35mH	6.2mH	
25V	5.0mF	2.5mF	50mH	15mH	
20V	10mF	5.0mF	400mH	20mH	
15V	20mF	10mF	**	100mH	

- If the inductive load is driven near steady state conditions, allowing the output voltage to drop more than 8V below the supply rail with I_{LIM} = 10A or 15V below the supply rail with I_{LIM} = 5A while the amplifier is current limiting, the inductor should be capacitively coupled or the current limit must be lowered to meet SOA criteria.
- ** Second breakdown effect imposes no limitation but thermal limitations must still be observed.
- The amplifier can handle any EMF generating or reactive load and short circuits to the supply rail or shorts to common if the current limits are set as follows at T_C = 85°C.

±V _s	SHORT TO $\pm V_s$ C, L, OR EMF LOAD	SHORT TO COMMON		
45V	0.1 A	1.3A		
40V	0.2A	1.5A		
35V	0.3A	1.6A		
30V	0.5A	2.0A		
25V	1.2A	2.4A		
20V	1.5A	3.0A		
15V	2.0A	4.0A		

These simplified limits may be exceeded with further analysis using the operating conditions for a specific application.

The output stage is protected against transient flyback. However, for protection against sustained, high energy flyback, external fast-recovery diodes should be used.

CURRENT LIMIT

Proper operation requires the use of two current limit resistors, connected as shown in the external connection diagram. The minimum value for R_{CL} is .06 ohm, however for optimum reliability it should be set as high as possible. Refer to the "General Operating Considerations" section of the handbook for current limit adjust details.

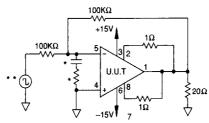


PA51M

HTTP://WWW.APEXMICROTECH.COM (800) 546-APEX (800) 546-2739

SG	PARAMETER	SYMBOL	TEMP.	POWER	TEST CONDITIONS	MIN	MAX	UNITS
1	Quiescent current	l _a	25°C	±34V	$V_{IN} = 0, A_{V} = 100, R_{CL} = .1\Omega$		10	mA
1	Input offset voltage	Vos	25°C	±34V	$V_{IN} = 0, A_{V} = 100$		±10	mV
1	Input offset voltage	Vos	25°C	±10V	$V_{IN} = 0, A_{V} = 100$		±16	m∨
1	Input offset voltage	V _{os}	25°C	±40V	$V_{iN} = 0, A_{v} = 100$		±11.2	m∨
1	Input bias current, +IN	+16	25°C	±34V	$V_{in} = 0$		±40	nA
1	Input bias current, - IN	-I	25°C	±34V	$V_{iN} = 0$		±40	пА
i	Input offset current	los	25°C	±34V	$V_{iN} = 0$		±10	nA
3	Quiescent current	l _a	-55°C	±34V	$V_{IN} = 0$, $A_V = 100$, $R_{CL} = .1\Omega$		10	mA
3	Input offset voltage	Vos	-55°C	±34V	$V_{IN} = 0, A_{V} = 100$	}	±15.2	mV
3	Input offset voltage	Vos	-55°C	±10V	$V_{IN} = 0, A_{V} = 100$		±21.2	mV
3	Input offset voltage	Vos	-55°C	±40V	$V_{IN} = 0, A_V = 100$ $V_{IN} = 0, A_V = 100$		±16.4	mV
3			_55°C	±34V	$V_{IN} = 0, A_V = 100$ $V_{IN} = 0$		±72	nA
	Input bias current, +IN	+l _B	-55°C				±72	nA
3	Input bias current, -IN	-l _B	į.	±34V	V _{IN} = 0	ł	1	ı
3	Input offset current	los	–55°C	±34V	$V_{IN} = 0$	1	±26	nA
2	Quiescent current	l _o	125°C	±34V	$V_{IN} = 0$, $A_{V} = 100$, $R_{CL} = .1\Omega$		13	mA
2	Input offset voltage	Vos	125°C	±34V	$V_{IN} = 0, A_{V} = 100$		±16.5	mV
2	Input offset voltage	Vos	125°C	±10V	$V_{IN} = 0, A_{V} = 100$		±22.5	mν
2	Input offset voltage	Vos	125°C	±40V	$V_{IN} = 0, A_{V} = 100$		±17.7	mV
2	Input bias current, +IN	+l _B	125°C	±34V	$V_{IN} = 0$		±80	nA
2	Input bias current, -IN	-l _B	125°C	±34V	$V_{IN} = 0$		±80	nA
2	Input offset current	los	125°C	±34V	$V_{IN} = 0$		±30	nA
4	Output voltage, I _O =10A	V _o	25°C	±18V	$R_{i} = 1\Omega$	10		l _v
4	Output voltage, I ₀ = 68mA	v _o	25°C	±40V	$R_1 = 500\Omega$	34		ľ
4			25°C	±30V	$R_1 = 50002$ $R_2 = 6\Omega$	24		ľ
	Output voltage, I _O = 4A	V _o	1	1 1	_ -	5	7.9	Å
4	Current limits	l _{CL}	25°C	±16V	$R_{CL} = 1\Omega, R_{CL} = .1\Omega$	} 3	1 1	mV
4	Stability/noise	E _N	25°C	±34V	$R_L = 500\Omega$, $A_V = +1$, $C_L = 1.5$ nF	1.0	1	
4	Siew rate	SR	25°C	±34V	$R_L = 500\Omega$	1.0	10	V/μs
4	Open loop gain	A _{OL}	25°C	±34V	$R_L = 500\Omega$, $F = 10Hz$	94		dB
4	Common-mode rejection	CMR	25°C	±15V	$R_L = 500\Omega$, $F = DC$, $V_{CM} = \pm 9V$	70	1	dB
6	Output voltage, I _o =10A	v _o	-55°C	±18V	$R_L = 1\Omega$	10		V
6	Output voltage, $I_0 = 68mA$	V _o	55°C	±40V	$R_L = 500\Omega$	34		V
6	Output voltage, I _o = 4A	V _o	-55°C	±30V	$R_L = 6\Omega$	24		٧
6	Stability/noise	E _N	-55°C	±34V	$R_L = 500\Omega$, $A_V = +1$, $C_L = 1.5 nF$	1	1	mV
6	Slew rate	SR	-55°C	±34V	$R_1 = 500\Omega$	1.0	10	V/μs
6	Open loop gain	A _{QL}	-55°C	±34V	$R_1 = 500\Omega$, $F = 10Hz$	94		dB
6	Common-mode rejection	CMR	-55°C	±15V	$R_L = 500\Omega$, $F = DC$, $V_{CM} = \pm 9V$	70		d₿
5	Output voltage, I _O = 8A	V _o	125°C	±16V	$R_1 = 1\Omega$	8		v
5	Output voltage, 1 ₀ = 68mA	v _o	125°C	±40V	$R_1 = 500\Omega$	34		v
5	Output voltage, I _O = 4A	v _o	125°C	±30V	$R_L = 6\Omega$	24		v
5	Stability/noise	E _N	125°C	±34V	$R_{t} = 500\Omega$, $A_{v} = +1$, $C_{t} = 1.5nF$	-	1	mV
5	Slew rate	SR	125°C	±34V	$R_i = 500\Omega$	1.0	10	V/µs
5	Open loop gain	A _{OL}	125°C	±34V	$R_i = 500\Omega$, $F = 10Hz$	94		dΒ
5		CMR	125°C	±15V	$R_L = 500\Omega$, $F = DC$, $V_{CM} = \pm 9V$	70		dB
0	Common-mode rejection	CIVIT	120 6	TIDV	11 = 20022, F = 200, V _{CM} = ±3V	1 70		. 00

BURN IN CIRCUIT



- These components are used to stabilize device due to poor high frequency characteristics of burn in board.
- Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.

■ 0878636 0002426 364 |