

<Opaque mini-flat package type>

△: Under application

○: Approved

(Ta=25°C)

Type	Model No.	Internal connection diagram	Features	Approved by safety standards	Package	Absolute maximum ratings			Electro-optical characteristics						
						Forward current I _F (mA)	Isolation voltage (AC) V _{ISO} (Vrms)	Collector-emitter voltage V _{CEO} (V)	Current transfer ratio CTR (%) MIN.	I _F (mA)	V _{CE} (V)	tr (μs) TYP.	I _C (mA)	R _L (Ω)	V _{CE} (V)
Single phototransistor output	PC353T		With base terminal	○	Mini-flat 5-pin	50	3 750	80	50	5	5	4	2	100	2
	PC354NT		AC input response	○*	Mini-flat 4-pin	±50	3 750	35	20	±1	5	4	2	100	2
	☆PC3H3		Half-pitch, high resistance to noise			±50	2500	70	20	±1	5	4	2	100	2
	PC3H4		Half-pitch	○		±50	2500	70	20	±1	5	4	2	100	2
	PC356NT		High collector-emitter voltage	○*		50	3 750	80	100	1	5	6	2	100	2
	PC357NT		General purpose	○*		50	3 750	35	50	5	5	4	2	100	2
	☆PC3H2		Half-pitch, high resistance to noise			50	2500	70	20	1	5	4	2	100	2
	PC3H7		Half-pitch	○		50	2500	70	20	1	5	4	2	100	2
	PC358T		High collector-emitter voltage	○*		50	3 750	120	20	5	5	4	2	100	2
	PC450T11		Built-in zener diode for absorption of surge voltage	○		50	3 750	(BV _{CEO} =40 to 60V)	1 500	5	2	50	2	100	2
	PC451	(Same as PC817)	High collector-emitter voltage	○		50	3 750	350	40	5	5	4	2	100	2
	☆PC3Q63		Half-pitch (lead space) (4-ch), high resistance to noise		Mini-flat 16-pin	±50	2 500	70	20	±1	5	4	2	100	2
	PC3Q64Q*1		Half-pitch (lead space) (4-ch)	○*		±50	2 500	35	20	±1	5	4	2	100	2
	PC3Q66Q*1		Half-pitch (lead space), high collector-emitter voltage (4-ch)	○*		50	2 500	80	100	1	5	6	2	100	2
	☆PC3Q62		Half-pitch (lead space) (4-ch), high resistance to noise			50	2 500	70	20	1	5	4	2	100	2
	PC3Q67Q*1		Half-pitch (lead space) (4-ch)	○*		50	2 500	35	50	5	5	4	2	100	2
Darlington phototransistor output	PC355NT	(Same as PC815)	High sensitivity type	○*	Mini-flat 4-pin	50	3 750	35	600	1	2	60	2	100	2
	PC452	(Same as PC852)	High isolation voltage, high collector-emitter voltage	○*		50	3 750	300	1 000	1	2	100	20	100	2

*1 High-temperature tested models

* Optionally available for VDE approved model (Expertise approval).

OPIC PHOTOCOUPLERS (1)

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Model No.	Internal connection diagram	Features	Approved by safety standards		Package	Absolute maximum ratings		Electro-optical characteristics*4						
			UL	TÜV (VDE 0884)		Forward current I _F (mA)	Isolation voltage (AC) V _{ISO} (V _{rms})	Low level output voltage				Threshold input current		
								V _{OL} (V) MAX.	T _a (°C)	I _{OL} (mA)	I _F (mA)	I _{FHL} (mA) MAX.	I _{FLH} (mA) MAX.	R _L (Ω)
PC400		Digital output, normal-off operation	○	—	Mini-flat 5-pin	50	3 750	0.4	0 to +70	16	4	2.0	—	280
PC401		Digital output, normal-on operation	○	—		50	3 750	0.4	0 to +70	16	0	—	2.0	280
PC410		High speed (10 Mb/s)	○	—		20	2 500	0.6	0 to +70	13	5	5.0	—	350
PC900V*1,2		Digital output, normal-off operation	○	○*3	6-pin DIP	50	5 000	0.4	0 to +70	16	4	2.0	—	280
PC930*1		High sensitivity, normal-off operation, open collector output	○	—		20	5 000	0.4	0 to +70	16	1	1.0	—	280

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