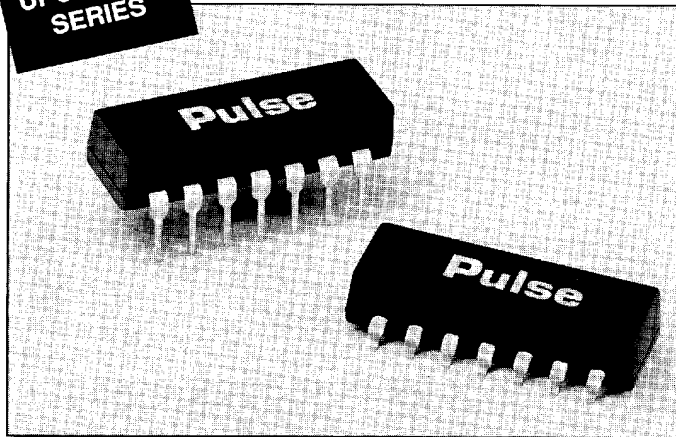


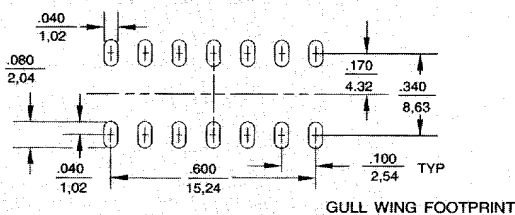
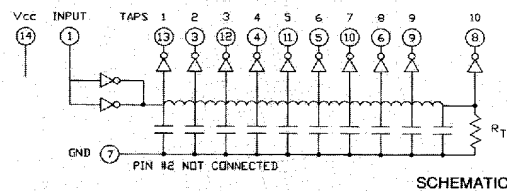
10 TAP SURFACE MOUNT AND AUTO-INSERTABLE DDMs



UPGRADED
SERIES



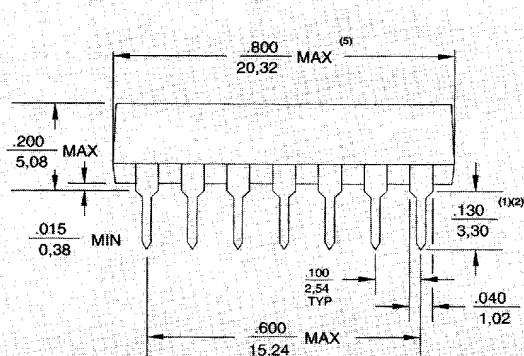
- LEADING AND TRAILING EDGE PRECISION
- AVAILABLE WITH SURFACE MOUNT GULL WING LEADS
- AUTOMATICALLY INSERTABLE DIP PACKAGE
- 10 EQUALLY SPACED TAPS
- TTL BUFFERED
- 14 PIN INDUSTRY STANDARD PINOUT



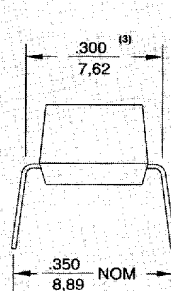
Part Number		Total Delay	Tap Delay	Rise Time
Dip	Gull Wing	(ns)	(ns)	(ns MAX)
PE-24107	PE-24107W	25.0	**2.5	3.0
PE-24108	PE-24108W	30.0	3.0	3.0
PE-24110	PE-24110W	40.0	4.0	3.0
PE-24112	PE-24112W	50.0	5.0	3.0
PE-24113	PE-24113W	60.0	6.0	3.0
PE-24114	PE-24114W	75.0	7.5	3.0
PE-24115	PE-24115W	100.0	10.0	3.0
PE-24116	PE-24116W	125.0	12.5	4.0
*PE-24117	*PE-24117W	150.0	15.0	4.0
*PE-24118	*PE-24118W	200.0	20.0	4.0

Delay Tolerance: $\pm 2\text{ns}$ or $\pm 5\%$ (whichever is greater) @ $V_{CC}=5.0\text{V}$ $T=25^\circ\text{C}$ No Load
Tap Delay is referenced to the input. *These parts available with leading edge precision only.
** The time delay at the first and second tap of PE-24107 equals 5.0 ns.

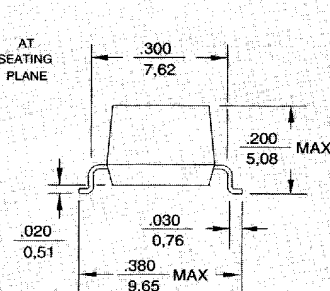
BASIC DIP PACKAGE⁽⁴⁾



THRU HOLE



GULL WING



LEAD CO-PLANARITY $\pm .002"$

Notes:

1. All dip Lead dimensions are .130 X .010 X .020 inches.
2. All lead length tolerances are $+.020/-0.010$ inches except where specified.
3. All tolerances (except lead length) are $\pm .010$ inches except where indicated.
4. Pin #1 identified by a white dot on top surface.
5. All MAX length tolerances not inclusive of mold stacking offset or gate flash.

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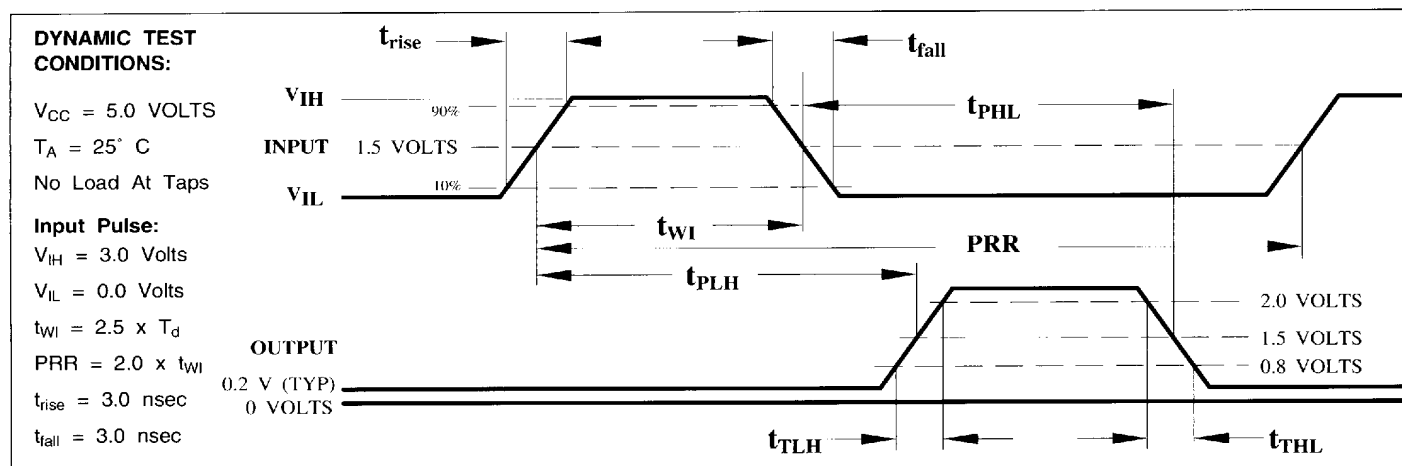
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9000-3865

892-2



10 TAP SURFACE MOUNT AND AUTO-INSERTABLE DDM'S



D.C. ELECTRICAL CHARACTERISTICS

PARAMETER	TEST CONDITION	MIN.	MAX.	UNITS
ABSOLUTE MAXIMUM RATINGS				
V_{CC}	Supply Voltage		+7.0	Volts
V_{IH}	High Level Input Voltage		+5.5	Volts
T_A	Operating Temperature Range	Free Air	0	$^\circ$ C
	Storage Temperature Range	Free Air	-55	+125 $^\circ$ C

RECOMMENDED OPERATION CONDITIONS

V_{CC}	Supply Voltage		4.75	5.25	Volts
V_{IH}	High Level Input Voltage (Logic 1)		2		Volts
V_{IL}	Low Level Input Voltage (Logic 0)	$V_{CC} = \text{Min.}$ $I_I = 18 \mu A$		.8	Volts
I_I	Input Current (Maximum @ V_{IH} Max.)	$V_{CC} = \text{Max.}$ $V_{IH} = 5.5$		1	mA
I_{IH}	High Level Input Current	$V_{CC} = \text{Max.}$ $V_{IH} = 2.70$		100	μA
I_{IL}	Low Level Input Current	$V_{CC} = \text{Max.}$ $V_{IL} = 0.5$		-4	mA
V_{OH}	High Level Output Voltage	$V_{CC} = \text{Min.}$ $V_{IH} = 2.0V$ $I_{OH} = -1mA$	2.7		Volts
V_{OL}	Low Level Output Voltage	$V_{CC} = \text{Min.}$ $V_{IL} = 0.8V$ $I_{OL} = 20mA$		.5	Volts
I_{OS}	Short Circuit Output Current	$V_{CC} = \text{Max.}$ (1 Output ≤ 1 Sec.)	-40	-100	mA
N	Normalized Fan-out (per device)	$V_{CC} = \text{Max.}$		20	Loads
N	Normalized Fan-out (per output)	$V_{CC} = \text{Max.}$		10	Loads
I_{CCH}	Supply Current High Level Output	$V_{CC} = \text{Max.}$ $V_{IH} = 3.2$		60	mA
I_{CCL}	Supply Current Low Level Output	$V_{CC} = \text{Max.}$ $V_{IL} = 0$		120	mA

Performance warranty of products offered on data sheet 892-2 is limited to the parameters specified. Data is subject to change without notice.
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